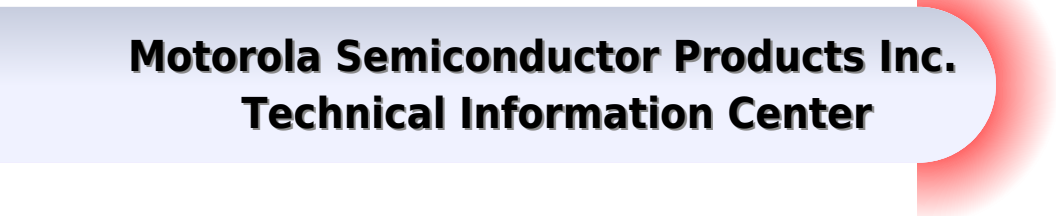


The table below shows output drive and input loading characteristics for selected TTL logic families. Calculate the number of LS (Low-Power Schottky) gates that an ALS (Advanced Low-Power Schottky) gate can drive when its output is **LOW**.

Device Family		Output Drive*	Input Loading
TTL	Standard TTL	$I_{OH} = 400 \mu A$ $I_{OL} = 16 \text{ mA}$	$I_{IH} = 40 \mu A$ $I_{IL} = 1.6 \text{ mA}$
	Low-Power Schottky	$I_{OH} = 400 \mu A$ $I_{OL} = 8 \text{ mA}$	$I_{IH} = 20 \mu A$ $I_{IL} = 400 \mu A$
	Advanced Low-Power Schottky	$I_{OH} = 400 \mu A$ $I_{OL} = 8 \text{ mA}$	$I_{IH} = 20 \mu A$ $I_{IL} = 100 \mu A$
	FAST Fairchild Advanced Schottky TTL	$I_{OH} = 1 \text{ mA}$ $I_{OL} = 20 \text{ mA}$	$I_{IH} = 20 \mu A$ $I_{IL} = 0.6 \text{ mA}$

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1981 Supplement to the TTL Data Book for Design Engineers, Second Edition Texas Instruments Incorporated. Semiconductor Group,1981 **Motorola FAST and LS** Motorola Semiconductor Products Inc. Technical Information Center,1986 *NASA Technical Memorandum* ,1981 Schottky and Low-power Schottky Data Book Including Digital Signal Processing Handbook Advanced Micro Devices (Firm),1978 **Signal Integrity and Radiated Emission of High-Speed Digital Systems** Spartaco Caniggia,Francescaromana Maradei,2008-11-20 Before putting digital systems for information technology or telecommunication applications on the market an essential requirement is to perform tests in order to comply with the limits of radiated emission imposed by the standards This book provides an investigation into signal integrity SI and electromagnetic interference EMI problems Topics such as reflections crosstalk switching noise and radiated emission RE in high speed digital systems are covered which are essential for IT and telecoms applications The highly important topic of modelling is covered which can reduce costs by enabling simulation data to demonstrate that a product meets design specifications and regulatory limits According to the new European EMC directive this can help to avoid the expensive use of large semi anechoic chambers or open area test sites for radiated emission assessments Following a short introduction to signalling and radiated interference in digital systems the book provides a detailed characterization of logic families in terms of static and dynamic characteristic useful for modelling techniques Crosstalk in multi coupled line structures are investigated by analytical graphical and circuit based methods and techniques to mitigate these phenomena are provided Grounding filtering and shielding with multilayer PCBs are also examined and design rules given Written by authors with extensive experience in industry and academia Explains basic conceptual problems from a theoretical and practical point of view by using numerous measurements and simulations Presents models for mathematical and SPICE like circuit simulators Provides examples of using full wave codes for SI and RE investigations Companion website containing lists of codes and sample material Signal Integrity and Radiated Emission of High Speed Digital Systems is a valuable resource to industrial designers of information technology telecommunication equipment and automation equipment as well as to development engineers It will also be of interest to managers and designers of consumer electronics and researchers in electronics **Digital Electronics** John Morris,2013-09-05 An essential companion to John C Morris s Analogue Electronics this clear and accessible text is designed for electronics students teachers and enthusiasts who already have a basic understanding of electronics and who wish to develop their knowledge of digital techniques and applications Employing a discovery based approach the author covers fundamental theory before going on to develop an appreciation of logic networks integrated circuit applications and analogue digital conversion A section on digital fault finding and useful ic data sheets completes the book The TTL Data Book Texas Instruments Incorporated,1984 *Programming the Parallel Port* Dhananjay Gadre,1998-01-02 Why purchase expensive add on cards or bus interfaces when you can develop effective and

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