

# A HIGH PERFORMANCE ARCHITECTURE FOR PROLOG

by

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# High Performance Architecture For Prolog



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## High Performance Architecture For Prolog:

A High Performance Architecture for Prolog T.P. Dobry, 2012-12-06      **A High Performance Architecture for Prolog**  
, 1987      A High Performance Architecture for Prolog Thaddeus Paul Dobry, 1987      *VLSI for Artificial Intelligence and Neural Networks* Jose G. Delgado-Frias, W.R. Moore, 2012-12-06 This book is an edited selection of the papers presented at the International Workshop on VLSI for Artificial Intelligence and Neural Networks which was held at the University of Oxford in September 1990 Our thanks go to all the contributors and especially to the programme committee for all their hard work Thanks are also due to the ACM SIGARCH the IEEE Computer Society and the IEE for publicizing the event and to the University of Oxford and SUNY Binghamton for their active support We are particularly grateful to Anna Morris Maureen Doherty and Laura Duffy for coping with the administrative problems Jose Delgado Frias Will Moore April 1991 vii

PROLOGUE Artificial intelligence and neural network algorithms computing have increased in complexity as well as in the number of applications This in turn has posed a tremendous need for a larger computational power than can be provided by conventional scalar processors which are oriented towards numeric and data manipulations Due to the artificial intelligence requirements symbolic manipulation knowledge representation non deterministic computations and dynamic resource allocation and neural network computing approach non programming and learning a different set of constraints and demands are imposed on the computer architectures for these applications      **VLSI for Artificial Intelligence** Jose G. Delgado-Frias, Will Moore, 2012-12-06      Concepts, Design, and Performance Analysis of a Parallel Prolog Machine Joachim Beer, 1989-12-13 This monograph presents a novel execution model for the parallel execution of standard sequential Prolog In this execution model Prolog procedure calls can be efficiently pipelined and the author shows how even fully deterministic Prolog programs can be effectively mapped onto the proposed architecture The design is based on a highly optimized abstract Prolog specific instruction set A special feature of this work is a sophisticated classification scheme for Prolog variables which substantially reduces the overhead for unification with occur check To support the model an architecture consisting of a circular pipeline of independent processors has been designed This pipeline has been designed to work as a co processor to a UNIX based workstation In contrast to other attempts to execute sequential Prolog in parallel the proposed model does not restrict the use of any of the standard Prolog language features The book gives a full account of the execution model the system architecture and the abstract Prolog instruction set      *Implementations of Logic Programming Systems* Evan Tick, Giancarlo Succi, 2012-12-06 This volume is a collection of research papers in the area of the implementation of logic programming systems It will be of immediate interest to practitioners who seek an understanding of how to efficiently manage memory generate fast code perform sophisticated static analyses and design high performance runtime features A major theme throughout the book is how to effectively leverage host implementation systems and technologies to implement target systems The book is also beneficial for future reference because it summarizes a wealth of systems implementation

experience of the researchers shaping the field over the past ten years Another theme of the book is compilation techniques to boost performance The field of static analysis for logic programs is a rapidly developing field that deserves a volume on its own Implementations of Logic Programming Systems is an excellent reference and may be used as a text for a course on the subject

Handbook of VLSI Chip Design and Expert Systems A. F. Schwarz, 2014-05-10 Handbook of VLSI Chip Design and Expert Systems provides information pertinent to the fundamental aspects of expert systems which provides a knowledge based approach to problem solving This book discusses the use of expert systems in every possible subtask of VLSI chip design as well as in the interrelations between the subtasks Organized into nine chapters this book begins with an overview of design automation which can be identified as Computer Aided Design of Circuits and Systems CAD/CAS This text then presents the progress in artificial intelligence with emphasis on expert systems Other chapters consider the impact of design automation which exploits the basic capabilities of computers to perform complex calculations and to handle huge amounts of data with a high speed and accuracy This book discusses as well the characterization of microprocessors The final chapter deals with interactive I/O devices This book is a valuable resource for system design experts circuit analysts and designers logic designers device engineers technologists and application specific designers

*Programming Language Implementation and Logic Programming* Jaan Penjam, 1994-08-24 This volume constitutes the proceedings of the 6th International Symposium on Programming Language Implementation and Logic Programming PLILP 94 held in Madrid Spain in September 1994 The volume contains 27 full research papers selected from 67 submissions as well as abstracts of full versions of 3 invited talks by renowned researchers and abstracts of 11 system demonstrations and poster presentations Among the topics covered are parallelism and concurrency implementation techniques partial evaluation synthesis and language issues constraint programming meta programming and program transformation functional logic programming and program analysis and abstract interpretation

High Performance Computing Constantine Polychronopoulos, Kazuki Joe, Akira Fukuda, Shinji Tomita, 1999-05-12 This book constitutes the refereed proceedings of the Second International Symposium on High Performance Computing ISHPC 99 held in Kyoto Japan in May 1999 The 23 revised full papers presented were carefully selected from a total of 61 submissions Also included are the abstracts of several invited talks and 12 reviewed short papers corresponding to the poster presentations given at the symposium The papers address many current issues in high performance computing and communication regarding hardware and network architectures as well as regarding software and theoretical foundations also advanced applications are studied in a variety of fields including modeling visualisation and computational science

Parallel Execution of Parlog Andrew Cheese, 1992-05-20 This monograph concentrates on the implementation of concurrent logic programming languages on loosely coupled parallel computer architectures Two different abstract models are also considered a graph reduction based model and a more imperative approach The design of a specialized instruction set is presented that is general enough to be used for the whole family of

concurrent logic programming languages In particular the language Parlog is concentrated upon because it is the most efficiently implementable of all the non flat logic programming languages Other issues arising from the implementation of a programming language are discussed in particular the design of a load balancing scheme which deals with the large number of short lived processes inherent in a correct language implementation Detailed descriptions of all the data structures and synchronisation algorithms are also presented Resource exhaustion is also discussed and handled within the implementations Detailed performance results for both models of computation are given From the material it should be possible for the reader to create an implementation

**VLSI for Neural Networks and Artificial Intelligence** Jose G. Delgado-Frias,W.R. Moore,2013-06-29 Neural network and artificial intelligence algorithms and computing have increased not only in complexity but also in the number of applications This in turn has posed a tremendous need for a larger computational power that conventional scalar processors may not be able to deliver efficiently These processors are oriented towards numeric and data manipulations Due to the neurocomputing requirements such as non programming and learning and the artificial intelligence requirements such as symbolic manipulation and knowledge representation a different set of constraints and demands are imposed on the computer architectures organizations for these applications Research and development of new computer architectures and VLSI circuits for neural networks and artificial intelligence have been increased in order to meet the new performance requirements This book presents novel approaches and trends on VLSI implementations of machines for these applications Papers have been drawn from a number of research communities the subjects span analog and digital VLSI design computer design computer architectures neurocomputing and artificial intelligence techniques This book has been organized into four subject areas that cover the two major categories of this book the areas are analog circuits for neural networks digital implementations of neural networks neural networks on multiprocessor systems and applications and VLSI machines for artificial intelligence The topics that are covered in each area are briefly introduced below 1987 IEEE International Symposium on Circuits and Systems ,1987 *Logic Programming* ,1990 1988 papers presented at the joint meeting of the Fifth International Conference on Logic Programming and the Fifth Symposium on Logic Programming

**Parallel and Constraint Logic Programming** Ioannis Vlahavas,Panagiotis Tsarchopoulos,Ilias Sakellariou,2012-12-06 Constraint Logic Programming CLP an area of extreme research interest in recent years extends the semantics of Prolog in such a way that the combinatorial explosion a characteristic of most problems in the field of Artificial Intelligence can be tackled efficiently By employing solvers dedicated to each domain instead of the unification algorithm CLP drastically reduces the search space of the problem which leads to increased efficiency in the execution of logic programs CLP offers the possibility of solving complex combinatorial problems in an efficient way and at the same time maintains the advantages offered by the declarativeness of logic programming The aim of this book is to present parallel and constraint logic programming offering a basic understanding of the two fields to the reader new to the area The first part of the book gives an

introduction to the fundamental aspects of conventional logic programming which is necessary for understanding the parts that follow The second part includes an introduction to parallel logic programming architectures and implementations proposed in the area Finally the third part presents the principles of constraint logic programming The last two parts also include descriptions of the supporting facilities for the two paradigms in two popular systems ECLIPSe and SICStus These platforms have been selected mainly because they offer both parallel and constraint features Annotated and explained examples are also included in the relevant parts offering a valuable guide and a first practical experience to the reader Finally applications of the covered paradigms are presented The authors felt that a book of this kind should provide some theoretical background necessary for the understanding of the covered logic programming paradigms and a quick start for the reader interested in writing parallel and constraint logic programming programs However it is outside the scope of this book to provide a deep theoretical background of the two areas In that sense this book is addressed to a public interested in obtaining a knowledge of the domain without spending the time and effort to understand the extensive theoretical work done in the field namely postgraduate and advanced undergraduate students in the area of logic programming This book fills a gap in the current bibliography since there is no comprehensive book of this level that covers the areas of conventional parallel and constraint logic programming

**Parallel and Constraint Logic Programming An Introduction to Logic Parallelism and Constraints** is appropriate for an advanced level course on Logic Programming or Constraints and as a reference for practitioners and researchers in industry

**A Code Mapping Scheme for Dataflow Software Pipelining** Guang R. Gao, 2012-12-06 This monograph evolved from my Ph D dissertation completed at the Laboratory of Computer Science MIT during the Summer of 1986 In my dissertation I proposed a pipelined code mapping scheme for array operations on static dataflow architectures The main addition to this work is found in Chapter 12 reflecting new research results developed during the last three years since I joined McGill University results based upon the principles in my dissertation The terminology dataflow software pipelining has been consistently used since publication of our 1988 paper on the argument fetching dataflow architecture model at McGill University 43 In the first part of this book we describe the static data flow graph model as an operational model for concurrent computation We look at timing considerations for program graph execution on an ideal static dataflow computer examine the notion of pipe lining and characterize its performance We discuss balancing techniques used to transform certain graphs into fully pipelined data flow graphs In particular we show how optimal balancing of an acyclic data flow graph can be formulated as a linear programming problem for which an optimal solution exists As a major result we show the optimal balancing problem of acyclic data flow graphs is reduceable to a class of linear programming problem the net work flow problem for which well known efficient algorithms exist This result disproves the conjecture that such problems are computationally hard

**Parallel Machines: Parallel Machine Languages** Robert A. Iannucci, 2012-12-06 It is universally accepted today that parallel processing is here to stay but that

software for parallel machines is still difficult to develop. However, there is little recognition of the fact that changes in processor architecture can significantly ease the development of software. In the seventies, the availability of processors that could address a large name space directly eliminated the problem of name management at one level and paved the way for the routine development of large programs. Similarly, today, processor architectures that can facilitate cheap synchronization and provide a global address space can simplify compiler development for parallel machines. If the cost of synchronization remains high, the programming of parallel machines will remain significantly less abstract than programming sequential machines. In this monograph, Bob Iannucci presents the design and analysis of an architecture that can be a better building block for parallel machines than any von Neumann processor. There is another very interesting motivation behind this work. It is rooted in the long and venerable history of dataflow graphs as a formalism for expressing parallel computation. The field has bloomed since 1974 when Dennis and Misunas proposed a truly novel architecture using dataflow graphs as the parallel machine language. The novelty and elegance of dataflow architectures has, however, also kept us from asking the real question: What can dataflow architectures buy us that von Neumann architectures can't? In the following, I explain in a roundabout way how Bob and I arrived at this question.

*Parallel Architectures for Data/knowledge-based Systems* L. L. Miller, A. R. Hurson, 1995

*Programming Language Implementation and Logic Programming* Jan Małuszyński, Martin Wirsing, 1991-08-14

This volume contains the papers which have been accepted for presentation at the Third International Symposium on Programming Language Implementation and Logic Programming PLILP 91 held in Passau, Germany, August 26-28, 1991. The aim of the symposium was to explore new declarative concepts, methods, and techniques relevant for the implementation of all kinds of programming languages, whether algorithmic or declarative ones. The intention was to gather researchers from the fields of algorithmic programming languages as well as logic, functional, and object-oriented programming. This volume contains the two invited talks given at the symposium by H. Ait Kaci and D. B. MacQueen, 32 selected papers, and abstracts of several system demonstrations. The proceedings of PLILP 88 and PLILP 90 are available as Lecture Notes in Computer Science Volumes 348 and 456.

Logic Programming '86 Eiiti Wada, 1987-06-03

This volume contains the papers presented and subsequently submitted for publication at the 5th Logic Programming Conference held June 23-26, 1986, in Tokyo. Topics covered include Prolog machine architecture, Prolog processors, variations of Prolog language, and applications of Prolog to natural language understanding and expert systems. Most of the works reported in the volume are related to the Japanese Fifth Generation Computer Project, allowing readers to compare results from this project with those from similar projects currently being conducted in other countries.

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