
FORMAL SEMANTICS AND PROOF TECHNIQUES FOR OPTIMIZING VHDL MODELS

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Formal Semantics And Proof Techniques For Optimizing Vhdl Models

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Formal Semantics and Proof Techniques for Optimizing VHDL Models Kothanda Umamageswaran,Sheetanshu L. Pandey,Philip A. Wilsey,2012-12-06 Formal Semantics and Proof Techniques for Optimizing VHDL Models presents a formal model of VHDL that clearly specifies both the static and dynamic semantics of VHDL It provides a mathematical framework for representing VHDL constructs and shows how those constructs can be formally manipulated to reason about VHDL The dynamic semantics is presented as a description of what the simulation of VHDL means In particular it specifies what values the signals of a VHDL description will take if the description were to be executed An advantage of the approach is that the semantic model can be used to validate different simulation algorithms The book also presents an embedding of the dynamic semantics in a proof checker which is then used to prove equivalences of classes of VHDL descriptions Formal Semantics and Proof Techniques for Optimizing VHDL Models is written for hardware designers who are interested in the formal semantics of VHDL

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Correct Hardware Design and Verification Methods ,1993 **Static Analysis** Agostino Cortesi,Gilberto File,1999-09-08 Static analysis is increasingly recognized as a fundamental research area aimed at studying and developing tools for high performance implementations and verification systems for all programming language paradigms The last two decades have witnessed substantial developments in this field ranging from theoretical frameworks to design implementation and application of analyzers in optimizing compilers Since 1994 SAS has been the annual conference and forum for researchers in all aspects of static analysis This volume contains the proceedings of the 6th International Symposium on

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VLSI Logic Synthesis and Design R. W. Dutton, 1991. Very Good. No Highlights or Markup. All pages are intact.

Dissertation Abstracts International, 1991. *Index to IEEE Publications* Institute of Electrical and Electronics Engineers, 1995. Issues for 1973 cover the entire IEEE technical literature.

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