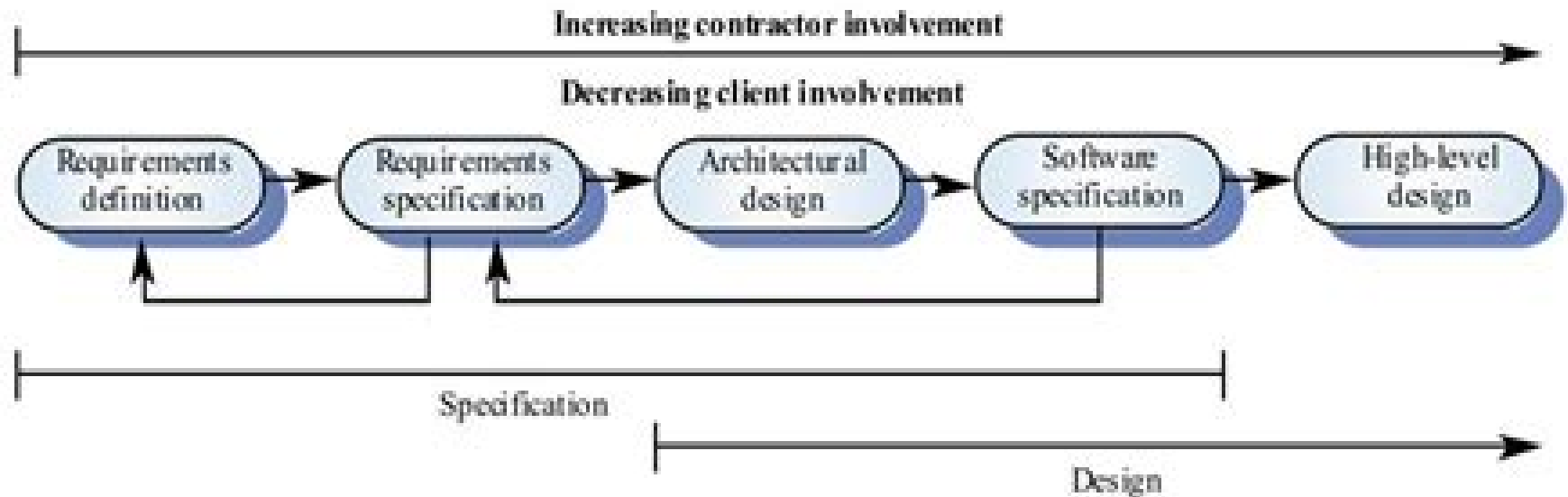


Specification and Design



Formal Specification And Design

Bruce S. Davie



Formal Specification And Design:

Formal Specification and Design L. M. G. Feijs, H. B. M. Jonkers, 2005-11-10 Formal specification is a method for precisely modelling computer based systems that combines concepts from software engineering and mathematical logic In this book the authors describe algebraic and state based specification techniques from the unified view of the Common Object oriented Language for Design COLD a wide spectrum language in the tradition of VDM and Z The kernel language is explained in detail with many examples including set representation a display device an INGRES like database system and a line editor Fundamental techniques such as initial algebra semantics loose semantics partial functions hiding sharing predicate and dynamic logic abstraction functions representation of invariants and black box correctness are also presented More advanced ideas for example Horn logic and large systems are given in the final part Appendices contain full details of the language s syntax and a specification library Techniques for software development and design are emphasised throughout so the book will be an excellent choice for courses in these areas **Formal Specification of Design Patterns** Deepa Balasundaram, 2010 Formal Specification Level Mathias Soeken, Rolf Drechsler, 2014-11-12 This book introduces a new level of abstraction that closes the gap between the textual specification of embedded systems and the executable model at the Electronic System Level ESL Readers will be enabled to operate at this new Formal Specification Level FSL using models which not only allow significant verification tasks in this early stage of the design flow but also can be extracted semi automatically from the textual specification in an interactive manner The authors explain how to use these verification tasks to check conceptual properties e g whether requirements are in conflict as well as dynamic behavior in terms of execution traces *The Use of Formal Specification of Software* H.K. Berg, W.K. Giloi, 2012-12-06 The notion that program design is an engineering task alleviating the software crisis has been with us for about a decade With the recognized advantages of obeying to certain software design disciplines we are approaching the era of enforced system development standards which will ensure that end products will meet rigorous design requirements On the one hand advances in system architecture further the application of system development standards to software and firmware design and production On the other hand the growth in complexity of future system architectures in particular distributed systems with their special problems of cooperation and parallelism necessitate the use of rigorous specification and design techniques In addition to hampering the design process the lack of engineering techniques hinders research In many cases trial designs that are presented in abstract and informal terms do not force the designer to face the full problem spectrum and therefore may not sufficiently provide insight into the design process To prepare for the forthcoming discipline and to provide a snapshot view of recent advances in software and firmware engineering we organized in June of 1979 a seminar entitled The Use of Formal Specification of Software and Firmware The seminar took place at the Heinrich Hertz Institute Berlin and attracted over 60 participants most of them from the industry Formal Specification and Verification in VLSI Design Bruce S. Davie, 1990 **The Handbook**

of Formal Methods in Human-Computer Interaction Benjamin Weyers, Judy Bowen, Alan Dix, Philippe

Palanque, 2017-04-24 This book provides a comprehensive collection of methods and approaches for using formal methods within Human Computer Interaction HCI research the use of which is a prerequisite for usability and user experience UX when engineering interactive systems World leading researchers present methods tools and techniques to design and develop reliable interactive systems offering an extensive discussion of the current state of the art with case studies which highlight relevant scenarios and topics in HCI as well as presenting current trends and gaps in research and future opportunities and developments within this emerging field The Handbook of Formal Methods in Human Computer Interaction is intended for HCI researchers and engineers of interactive systems interested in facilitating formal methods into their research or practical work *ZB 2000: Formal Specification and Development in Z and B* Jonathan P.

Bowen, Steve Dunne, Andrew Galloway, Steve King, 2000-08-16 This book constitutes the refereed proceedings of the First International Conference of B and Z Users ZB 2000 held in York UK in August September 2000 The 25 revised full papers presented together with four invited contributions were carefully reviewed and selected for inclusion in the book The book documents the recent advances for the Z formal specification notion and for the B method the full scope ranging from foundational and theoretical issues to advanced applications tools and case studies is covered High-Integrity System Specification and Design Jonathan P. Bowen, Michael G. Hinchey, 2012-12-06 Errata detected in Taylor s Logarithms London 4to 1792 sic 14 18 3 6 Kk Co sine of 3398 3298 Nautical Almanac 1832 In the list of ERRATA detected in Taylor s Logarithms for cos 4 18 3 read cos 14 18 2 Nautical Almanac 1833 ERRATUM of the ERRATUM of the ERRATA of TAYLOR S Logarithms For cos 4 18 3 read cos 14 18 3 Nautical Almanac 1836 In the 1820s an Englishman named Charles Babbage designed and partly built a calculating machine originally intended for use in deriving and printing logarithmic and other tables used in the shipping industry At that time such tables were often inaccurate copied carelessly and had been instrumental in causing a number of maritime disasters Babbage s machine called a Difference Engine because it performed its calculations using the principle of partial differences was intended to substantially reduce the number of errors made by humans calculating the tables Babbage had also designed but never built a forerunner of the modern printer which would also reduce the number of errors admitted during the transcription of the results Nowadays a system implemented to perform the function of Babbage s engine would be classed as safety critical That is the failure of the system to produce correct results could result in the loss of human life mass destruction of property in the form of ships and cargo as well as financial losses and loss of competitive advantage for the shipping firm *Specification and Verification of Systolic Arrays* Nam Ling, Magdy A. Bayoumi, 1999 Circuits and architectures have become more complex in terms of structure interconnection topology and data flow Design correctness has become increasingly significant as errors in design may result in strenuous debugging or even in the repetition of a costly manufacturing process Although circuit simulation has been used traditionally and widely as the

technique for checking hardware and architectural designs it does not guarantee the conformity of designs to specifications Formal methods therefore become vital in guaranteeing the correctness of designs and have thus received a significant amount of attention in the CAD industry today This book presents a formal method for specifying and verifying the correctness of systolic array designs Such architectures are commonly found in the form of accelerators for digital signal image and video processing These arrays can be quite complicated in topology and data flow In the book a formalism called STA is defined for these kinds of dynamic environments with a survey of related techniques A framework for specification and verification is established Formal verification techniques to check the correctness of the systolic networks with respect to the algorithmic level specifications are explained The book also presents a Prolog based formal design verifier named VSTA developed to automate the verification process as using a general purpose theorem prover is usually extremely time consuming Several application examples are included in the book to illustrate how formal techniques and the verifier can be used to automate proofs

Formal Methods for Open Object-based Distributed Systems Elie Najm, Jean-Bernard Stefani, 2016-02-13 Object based Distributed Computing is being established as the most pertinent basis for the support of large heterogeneous computing and telecommunications systems The advent of Open Object based Distributed Systems OODS brings new challenges and opportunities for the use and development of formal methods Formal Methods for Open Object based Distributed Systems presents the latest research in several related fields and the exchange of ideas and experiences in a number of topics including formal models for object based distributed computing semantics of object based distributed systems and programming languages formal techniques in object based and object oriented specification analysis and design refinement and transformation of specifications multiple viewpoint modeling and consistency between different models formal techniques in distributed systems verification and testing types service types and subtyping specification verification and testing of quality of service constraints and formal methods and the object life cycle It contains the selected proceedings of the International Workshop on Formal Methods for Open Object based Distributed Systems sponsored by the International Federation for Information Processing and based in Paris France in March 1996

Industrial Applications of Formal Methods to Model, Design and Analyze Computer Systems Dan Craigen, Susan Gerhart, 2012-12-02 Formal methods are mathematically based techniques often supported by reasoning tools that can offer a rigorous and effective way to model design and analyze computer systems The purpose of this study is to evaluate international industrial experience in using formal methods The cases selected are representative of industrial grade projects and span a variety of application domains The study had three main objectives To better inform deliberations within industry and government on standards and regulations To provide an authoritative record on the practical experience of formal methods to date and To suggest areas where future research and technology development are needed This study was undertaken by three experts in formal methods and software engineering Dan Craigen of ORA Canada Susan Gerhart of Applied Formal Methods and Ted Ralston

of Ralston Research Associates Robin Bloomfield of Adelard was involved with the Darlington Nuclear Generating Station Shutdown System case Support for this study was provided by organizations in Canada and the United States The Atomic Energy Control Board of Canada AECB provided support for Dan Craigen and for the technical editing provided by Karen Summerskill The U S Naval Research Laboratories NRL Washington DC provided support for all three authors The U S National Institute of Standards and Technology NIST provided support for Ted Ralston

An Example of Formal Specification as an Aid to Design and Development Robert B. Terwilliger, Mark J. Maybee, Leon J. Osterweil, 1988 **The Use of Formal Specification of Software** H K Berg, W K Giloi, 1980-01-01 *International Workshop on Software Specification and Design [proceedings]*. , 1993 **Formal specification as a design tool** J. Guttag, J. J. Horning, 1980

ZUM '98: The Z Formal Specification Notation Jonathan P. Bowen, Andreas Fett, Michael G. Hinchey, 1998-09-16 1 In a number of recent presentations most notably at FME 96 one of the foremost scientists in the field of formal methods C A R Hoare has highlighted the fact that formal methods are not the only technique for producing reliable software This seems to have caused some controversy not least amongst formal methods practitioners How can one of the founding fathers of formal methods seemingly denounce the field of research after over a quarter of a century of support This is a question that has been posed recently by some formal methods skeptics However Prof Hoare has not abandoned formal methods He is reiterating 2 albeit more radically his 1987 view that more than one tool and notation will be required in the practical industrial development of large scale complex computer systems and not all of these tools and notations will be or even need be formal in nature Formal methods are not a solution but rather one of a selection of techniques that have proven to be useful in the development of reliable complex systems and to result in hardware and software systems that can be produced on time and within a budget while satisfying the stated requirements After almost three decades the time has come to view formal methods in the context of overall industrial scale system development and their relationship to other techniques and methods

We should no longer consider the issue of whether we are pro formal or anti formal but rather the degree of formality if any that we need to support in system development This is a goal of ZUM 98 the 11th International Conference of Z Users held for the first time within continental Europe in the city of Berlin Germany

Formal Specification and Verification of Digital Systems George J. Milne, 1994 Algorithms and Architectures for Real-Time Control 1991 P.J. Fleming, D.I.

Jones, 2014-07-22 Computer scientists have long appreciated that the relationship between algorithms and architecture is crucial Broadly speaking the more specialized the architecture is to a particular algorithm then the more efficient will be the computation The penalty is that the architecture will become useless for computing anything other than that algorithm This message holds for the algorithms used in real time automatic control as much as any other field These Proceedings will provide researchers in this field with a useful up to date reference source of recent developments

Formal Methods in Circuit Design Victoria Stavridou, 1993-07-22 Graduate level account of hardware verification and algebraic specification

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Specification and Design, 1989

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