

Error Detection Circuits

Michael Gössel, Vitaly Ocheretny, Egor Sogomonyan, Daniel Marienfeld

Error Detection Circuits:

Error Detection Circuits Michael Gössel, Steffen Graf, 1993 The first comprehensive description of systematic methods for designing optional error detection circuits Table of Contents Overview and Introduction Combinatorial Error Detection Circuitry Sequential Error Detection Circuits Design Algorithms for Error Detection Circuits Appendix References Index 100 Error Detecting Logic for Digital Computers Frederick F. Sellers, Muyue Xiao, Mu-yüeh Hsiao, Leroy W. illustrations On-line Error Detection and Fast Recover Techniques for Dependable Embedded Processors Matthias Bearnson, 1968 Pflanz, 2003-07-31 This book presents a new approach to on line observation and concurrent checking of processors by refining and improving known techniques and introducing new ideas The proposed on line error detection and fast recover techniques support and complement other established methods In combination with other on line observation priniciples and with a combined hardware software test these techniques are used to fulfill a complete self check scheme for an embedded Radiation Effects and Soft Errors in Integrated Circuits and Electronic Devices Ronald Donald processor Schrimpf, Dan M. Fleetwood, 2004 This book provides a detailed treatment of radiation effects in electronic devices including effects at the material device and circuit levels The emphasis is on transient effects caused by single ionizing particles single event effects and soft errors and effects produced by the cumulative energy deposited by the radiation total ionizing dose effects Bipolar Si and SiGe metal oxide semiconductor MOS and compound semi conductor technologies are discussed In addition to considering the specific issues associated with high performance devices and technologies the book includes the background material necessary for understanding radiation effects at a more general level Adaptive Clocking and Dynamic Error Detection in Integrated Circuits Suparna Das, 2008 **Concurrent Error Detection with Latency in Sequential Circuits** Lawrence Paul Holmquist, 1989 Handbook of Integrated Circuit Industry Yangyuan Wang, Min-Hwa Chi, Jesse Jen-Chung Lou, Chun-Zhang Chen, 2023-11-27 Written by hundreds experts who have made contributions to both enterprise and academics research these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits and cover topics related to the technology evolution trends fabrication applications new materials equipment economy investment and industrial developments of integrated circuits Especially the coverage is broad in scope and deep enough for all kind of readers being interested in integrated circuit industry Remarkable data collection update marketing evaluation enough working knowledge of integrated circuit fabrication clear and accessible category of integrated circuit products and good equipment insight explanation etc can make general readers build up a clear overview about the whole integrated circuit industry. This encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field In addition this book provides enough guide lines and knowledges to benefit enterprisers being interested in integrated circuit industry Error Detection. Decoding, and Error Correction for Circuit Codes John Christopher Beebee, 1972 New Methods of Concurrent Checking

Michael Gössel, Vitaly Ocheretny, Egor Sogomonyan, Daniel Marienfeld, 2008-04-26 Computers are everywhere around us We for example as air passengers car drivers laptop users with Internet connection cell phone owners hospital patients inhabitants in the vicinity of a nuclear power station students in a digital library or customers in a supermarket are dependent on their correct operation Computers are incredibly fast inexpensive and equipped with almost unimag able large storage capacity Up to 100 million transistors per chip are quite common today a single transistor for each citizen of a large capital city in the world can be 2 easily accommodated on an ordinary chip The size of such a chip is less than 1 cm This is a fantastic achievement for an unbelievably low price However the very small and rapidly decreasing dimensions of the transistors and their connections over the years are also the reason for growing problems with reliability that will dramatically increase for the nano technologies in the near future Can we always trust computers Are computers always reliable Are chips sufficiently tested with respect to all possible permanent faults if we buy them at a low price or have errors due to undetected permanent faults to be discovered by c current checking Besides permanent faults many temporary or transient faults are also to be expected Circuits and Systems Advances in Near Threshold Computing Sanghamitra Roy, 2021-05-11 Modern society is witnessing a sea change in ubiquitous computing in which people have embraced computing systems as an indispensable part of day to day existence Computation storage and communication abilities of smartphones for example have undergone monumental changes over the past decade However global emphasis on creating and sustaining green environments is leading to a rapid and ongoing proliferation of edge computing systems and applications As a broad spectrum of healthcare home and transport applications shift to the edge of the network near threshold computing NTC is emerging as one of the promising low power computing platforms An NTC device sets its supply voltage close to its threshold voltage dramatically reducing the energy consumption Despite showing substantial promise in terms of energy efficiency NTC is yet to see widescale commercial adoption This is because circuits and systems operating with NTC suffer from several problems including increased sensitivity to process variation reliability problems performance degradation and security vulnerabilities to name a few To realize its potential we need designs techniques and solutions to overcome these challenges associated with NTC circuits and systems The readers of this book will be able to familiarize themselves with recent advances in electronics systems focusing on near threshold computing **Reversible Computation** Iain Phillips, Hafizur Rahaman, 2017-06-26 This book constitutes the refereed proceedings of the 9th International Conference on Reversible Computation RC 2017 held in Kolkata India in July 2017 The 13 full and 5 short papers included in this volume together with one invited paper were carefully reviewed and selected from 47 submissions. The papers are organized in the following topical sections foundations reversible circuit synthesis reversible circuit optimization testing and fault tolerance and quantum circuits Fault Analysis in Cryptography Marc Joye, Michael Tunstall, 2012-06-21 In the 1970s researchers noticed that radioactive particles produced by elements naturally present in packaging material could cause bits to flip in

sensitive areas of electronic chips Research into the effect of cosmic rays on semiconductors an area of particular interest in the aerospace industry led to methods of hardening electronic devices designed for harsh environments Ultimately various mechanisms for fault creation and propagation were discovered and in particular it was noted that many cryptographic algorithms succumb to so called fault attacks Preventing fault attacks without sacrificing performance is nontrivial and this is the subject of this book Part I deals with side channel analysis and its relevance to fault attacks The chapters in Part II cover fault analysis in secret key cryptography with chapters on block ciphers fault analysis of DES and AES countermeasures for symmetric key ciphers and countermeasures against attacks on AES Part III deals with fault analysis in public key cryptography with chapters dedicated to classical RSA and RSA CRT implementations elliptic curve cryptosystems and countermeasures using fault detection devices resilient to fault injection attacks lattice based fault attacks on signatures and fault attacks on pairing based cryptography Part IV examines fault attacks on stream ciphers and how faults interact with countermeasures used to prevent power analysis attacks Finally Part V contains chapters that explain how fault attacks are implemented with chapters on fault injection technologies for microprocessors and fault injection and key retrieval experiments on a widely used evaluation board This is the first book on this topic and will be of interest to researchers and practitioners engaged with cryptographic engineering **Emerging Technologies and Circuits** Amara Amara, Thomas Ea, Marc Belleville, 2010-09-28 Emerging Technologies and Circuits contains a set of outstanding papers keynote and tutorials presented during 3 days at the International Conference On Integrated Circuit Design and Technology ICICDT held in June 2008 in Minatec Grenoble Official Gazette of the United States Patent and Trademark Office, 2002 Hardware and Embedded Systems - CHES 2008 Elisabeth Oswald, 2008-07-18 by Andrey Bogdanov Thomas Eisenbarth Andy Rupp and Christopher Wolf The purpose of the award is to formally acknowledge excellence in research We would like to c gratulate the authors of these two papers Manuals Combined: U.S. Navy FIRE CONTROLMAN Volumes 01 - 06 & FIREMAN, Over 1 600 total pages 14097 FIRE CONTROLMAN SUPERVISOR Covers Fire Controlman supervisor responsibilities organization administration inspections and maintenance supervision and training combat systems subsystems and their maintenance and weapons exercises 14098 FIRE CONTROLMAN VOLUME 01 ADMINISTRATION AND SAFETY Covers general administration technical administration electronics safety and hazardous materials as they pertain to the FC rating 14099A FIRE CONTROLMAN VOLUME 02 FIRE CONTROL SYSTEMS AND RADAR FUNDAMENTALS Covers basic radar systems fire control systems and radar safety as they relate to the Fire Controlman rating 14100 FIRE CONTROLMAN VOLUME 03 DIGITAL DATA SYSTEMS Covers computer and peripheral fundamentals and operations configurations and hardware operator controls and controlling units components and circuits central processing units and buses memories input output and interfacing instructions and man machine interfaces magnetic tape storage magnetic disk storage CD ROM storage printers data conversion devices and switchboards 14101 FIRE CONTROLMAN VOLUME 04 FIRE

CONTROL MAINTENANCE CONCEPTS Introduces the Planned Maintenance System and discusses methods for identifying and isolating system faults liquid cooling systems used by Fire Controlmen battery alignment purpose equipment and alignment considerations and radar collimation 14102 FIRE CONTROLMAN VOLUME 05 DISPLAY SYSTEMS AND DEVICES Covers basic display devices and input devices associated with Navy tactical data systems as used by the FC rating 14103 FIRE CONTROLMAN VOLUME 06 DIGITAL COMMUNICATIONS Covers the fundamentals of data communications the Link 11 and Link 4A systems and local area networks 14104A FIREMAN Provides information on the following subject areas engineering administration engineering fundamentals the basic steam cycle gas turbines internal combustion engines ship propulsion pumps valves and piping auxiliary machinery and equipment instruments shipboard electrical equipment and environmental controls **Reliability Abstracts and Technical Reviews** ,1967 Official Gazette of the United **States Patent Office** United States. Patent Office,1970 ICCCE 2021 Amit Kumar, Stefan Mozar, 2022-05-15 This book is a collection of research articles presented at the 4th International Conference on Communications and Cyber Physical Engineering ICCCE 2021 held on April 9 and 10 2021 at CMR Engineering College Hyderabad India ICCCE is one of the most prestigious conferences conceptualized in the field of networking and communication technology offering in depth information on the latest developments in voice data image and multimedia Discussing the latest developments in voice and data communication engineering cyber physical systems network science communication software image and multimedia processing research and applications as well as communication technologies and other related technologies it includes contributions from both academia and industry This book is a valuable resource for scientists research scholars and PG students working to formulate their research ideas and find the future directions in these areas Further it may serve as a reference work to understand the latest engineering and technologies used by practicing engineers in the field of communication engineering Concurrent Error Detection Steven Scott Gorshe, 2002 Concurrent error detection CED is the detection of errors or faults in a circuit or data path concurrent with normal operation of that circuit The general approach for CED is to calculate a check symbol for the inputs to the circuit under operation predict the check symbol that will result for the output of the circuit for those inputs and compare the predicted check symbol to the one that is actually calculated for the output If the predicted and actual check symbols are different an error or fault has been detected. The alternative to this check symbol prediction is to use a second copy of the circuit under operation and compare the results of the two circuits For some classes of circuits the prediction of the output check symbol can require less circuitry than a second copy of the circuit being tested Four examples of these types of circuits are examined in this dissertation Arithmetic Logic Units ALUs array multipliers self synchronous scrambler descrambler pairs with their intervening data path and switch fabrics Faults in integrated circuits tend to produce unidirectional errors Unidirectional errors are those in which all of the errors are in the same direction e g 0 to 1 errors within the block of data covered by a given check symbol For this reason codes that are

optimized for unidirectional errors are the focus of investigation for most of the applications In particular the Bose Lin codes are examined for those applications where unidirectional errors are expected to be typical In order to examine the performance of the Bose Lin codes in one of these applications it was necessary to determine the theoretical performance for Bose Lin codes for error rates beyond what had been previously studied This analysis of Bose Lin codes with large numbers of burst errors also included a further generalization of the codes

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