
INTERACTION BETWEEN COMPILERS AND COMPUTER ARCHITECTURES

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Eighth Workshop On Interaction Between Compilers And Computer Architecture

Oege de Moor, Michael I. Schwartzbach



Eighth Workshop On Interaction Between Compilers And Computer Architecture:

Eighth Workshop on Interaction Between Compilers and Computer Architecture, 2004-01-01 INTERACT 2004 presents new ideas and explores recent developments in compiler techniques and computer architectures that enhance each other's capabilities and performance. Its papers cover various aspects of interaction between compilers and architectures in the design of high performance microprocessors, embedded controllers, multiprocessors, and other parallel computer systems.

Interaction Between Compilers and Computer Architectures Gyungho Lee, Pen-Chung Yew, 2013-03-14 Effective compilers allow for a more efficient execution of application programs for a given computer architecture while well conceived architectural features can support more effective compiler optimization techniques. A well thought out strategy of trade offs between compilers and computer architectures is the key to the successful designing of highly efficient and effective computer systems. From embedded micro controllers to large scale multiprocessor systems, it is important to understand the interaction between compilers and computer architectures. The goal of the Annual Workshop on Interaction between Compilers and Computer Architectures INTERACT is to promote new ideas and to present recent developments in compiler techniques and computer architectures that enhance each other's capabilities and performance. *Interaction Between Compilers and Computer Architectures* is an updated and revised volume consisting of seven papers originally presented at the Fifth Workshop on Interaction between Compilers and Computer Architectures INTERACT 5 which was held in conjunction with the IEEE HPCA 7 in Monterrey, Mexico, in 2001. This volume explores recent developments and ideas for better integration of the interaction between compilers and computer architectures in designing modern processors and computer systems. *Interaction Between Compilers and Computer Architectures* is suitable as a secondary text for a graduate level course and as a reference for researchers and practitioners in industry. INTERACT-8 2004, 2004

Performance Evaluation and Benchmarking Lizy Kurian John, Lieven Eeckhout, 2018-10-03 Computer and microprocessor architectures are advancing at an astounding pace. However, increasing demands on performance coupled with a wide variety of specialized operating environments act to slow this pace by complicating the performance evaluation process. Carefully balancing efficiency and accuracy is key to avoid slowdowns, and such a balance can be achieved with an in-depth understanding of the available evaluation methodologies. *Performance Evaluation and Benchmarking* outlines a variety of evaluation methods and benchmark suites, considering their strengths, weaknesses, and when each is appropriate to use. Following a general overview of important performance analysis techniques, the book surveys contemporary benchmark suites for specific areas such as Java, embedded systems, CPUs, and Web servers. Subsequent chapters explain how to choose appropriate averages for reporting metrics and provide a detailed treatment of statistical methods, including a summary of statistics, how to apply statistical sampling for simulation, how to apply SimPoint, and a comprehensive overview of statistical simulation. The discussion then turns to benchmark subsetting methodologies and the fundamentals of analytical modeling, including queuing

models and Petri nets Three chapters devoted to hardware performance counters conclude the book Supplying abundant illustrations examples and case studies Performance Evaluation and Benchmarking offers a firm foundation in evaluation methods along with up to date techniques that are necessary to develop next generation architectures Distributed Computing and Internet Technology Sanjay K. Madria,2006-12-07 This book constitutes the refereed proceedings of the Third International Conference on Distributed Computing and Internet Technology ICDCIT 2006 held in Bhubaneswar India in December 2006 The 24 revised full papers and 10 revised short papers presented together with 1 keynote address and 1 invited talk cover the main areas distributed computing internet technology system security data mining and software engineering **INTERACT-7 2003** ,2003 Languages and Compilers for High Performance Computing Rudolf Eigenmann,Zhiyuan Li,Samuel P. Midkiff,2005-08-25 The 17th International Workshop on Languages and Compilers for High Performance Computing was hosted by Purdue University in September 2004 on Purdue campus in West Lafayette Indiana USA **Embedded and Networking Systems** Gul N. Khan,Krzysztof Iniewski,2017-07-12 Embedded and Networking Systems Design Software and Implementation explores issues related to the design and synthesis of high performance embedded computer systems and networks The emphasis is on the fundamental concepts and analytical techniques that are applicable to a range of embedded and networking applications rather than on specific embedded architectures software development or system level integration This system point of view guides designers in dealing with the trade offs to optimize performance power cost and other system level non functional requirements The book brings together contributions by researchers and experts from around the world offering a global view of the latest research and development in embedded and networking systems Chapters highlight the evolution and trends in the field and supply a fundamental and analytical understanding of some underlying technologies Topics include the co design of embedded systems code optimization for a variety of applications power and performance trade offs benchmarks for evaluating embedded systems and their components and mobile sensor network systems The book also looks at novel applications such as mobile sensor systems and video networks A comprehensive review of groundbreaking technology and applications this book is a timely resource for system designers researchers and students interested in the possibilities of embedded and networking systems It gives readers a better understanding of an emerging technology evolution that is helping drive telecommunications into the next decade **Computational Science and Its Applications - ICCSA 2006** Osvaldo Gervasi,Vipin Kumar,C.J. Kenneth Tan,David Taniar,Antonio Laganà,Youngsong Mun,Hyunseung Choo,2006-05-11 The five volume set LNCS 3980 3984 constitutes the refereed proceedings of the International Conference on Computational Science and Its Applications ICCSA 2006 The volumes present a total of 664 papers organized according to the five major conference themes computational methods algorithms and applications high performance technical computing and networks advanced and emerging applications geometric modelling graphics and visualization information systems and information technologies This is Part IV

Compiler Construction Oege de Moor, Michael I. Schwartzbach, 2009-03-27 This book constitutes the refereed proceedings of the 18th International Conference on Compiler Construction CC 2009 held in York UK in March 2009 as part of ETAPS 2009 the European Joint Conferences on Theory and Practice of Software Following a very thorough review process 18 full research papers were selected from 72 submissions Topics covered include traditional compiler construction compiler analyses runtime systems and tools programming tools techniques for specific domains and the design and implementation of novel language constructs

Languages and Compilers for Parallel Computing Guang R. Gao, Lori Pollock, John Cavazos, Xiaoming Li, 2010-06-09 This book constitutes the thoroughly refereed post workshop proceedings of the 22nd International Workshop on Languages and Compilers for Parallel Computing LCPC 2009 held in Newark DE USA in October 2009 The 25 revised full papers and 5 revised short papers presented were carefully reviewed and selected from 52 submissions The papers address all aspects of languages compiler techniques run time environments and compiler related performance evaluation for parallel and high performance computing and extend the area of interest to new parallel computing accelerators such as IBM Cell processor and Graphic Processing Unit GPU presenting the latest work of leading researchers and practitioners in the field

Parallel Processing and Applied Mathematics Roman Wyrzykowski, Jack Dongarra, Marcin Paprzycki, Jerzy Wasniewski, 2003-08-01 This book constitutes the thoroughly refereed post proceedings of the 4th International Conference on Parallel Processing and Applied Mathematics PPAM 2002 held in Naleczow Poland in September 2001 The 101 papers presented were carefully reviewed and improved during two rounds of reviewing and revision The book offers topical sections on distributed and grid architectures scheduling and load balancing performance analysis and prediction parallel non numerical algorithms parallel programming tools and environments parallel numerical algorithms applications and evolutionary computing and neural networks

Applied Parallel Computing Bo Kagström, Erik Elmroth, Jack Dongarra, Jerzy Wasniewski, 2007-09-22 This book constitutes the thoroughly refereed post proceedings of the 8th International Workshop on Applied Parallel Computing PARA 2006 It covers partial differential equations parallel scientific computing algorithms linear algebra simulation environments algorithms and applications for blue gene L scientific computing tools and applications parallel search algorithms peer to peer computing mobility and security algorithms for single chip multiprocessors

Languages and Compilers for Parallel Computing José Nelson Amaral, 2008-12 This book constitutes the thoroughly refereed post conference proceedings of the 21th International Workshop on Languages and Compilers for Parallel Computing LCPC 2008 held in Edmonton Canada in July August 2008 The 18 revised full papers and 6 revised short papers presented were carefully reviewed and selected from 35 submissions The papers address all aspects of languages compiler techniques run time environments and compiler related performance evaluation for parallel and high performance computing and comprise also presentations on program analysis that are precursors of high performance in parallel environments

Languages and Compilers for Parallel Computing Chua-Huang Huang, 1996-01-24 This book

presents the refereed proceedings of the Eighth Annual Workshop on Languages and Compilers for Parallel Computing held in Columbus Ohio in August 1995 The 38 full revised papers presented were carefully selected for inclusion in the proceedings and reflect the state of the art of research and advanced applications in parallel languages restructuring compilers and runtime systems The papers are organized in sections on fine grain parallelism interprocedural analysis program analysis Fortran 90 and HPF loop parallelization for HPF compilers tools and libraries loop level optimization automatic data distribution compiler models irregular computation object oriented and functional parallelism Custom Memory Management Methodology Francky Catthoor, Sven Wuytack, G.E. de Greef, Florin Banica, Lode Nachtergaele, Arnout Vandecappelle, 2013-03-09 The main intention of this book is to give an impression of the state of the art in system level memory management data transfer and storage related issues for complex data dominated real time signal and data processing applications The material is based on research at IMEC in this area in the period 1989 1997 In order to deal with the stringent timing requirements and the data dominated characteristics of this domain we have adopted a target architecture style and a systematic methodology to make the exploration and optimization of such systems feasible Our approach is also very heavily application driven which is illustrated by several realistic demonstrators partly used as red thread examples in the book Moreover the book addresses only the steps above the traditional high level synthesis scheduling and allocation or compilation traditional or ILP oriented tasks The latter are mainly focussed on scalar or scalar stream operations and data where the internal structure of the complex data types is not exploited in contrast to the approaches discussed here The proposed methodologies are largely independent of the level of programmability in the data path and controller so they are valuable for the realisation of both hardware and software systems Our target domain consists of signal and data processing systems which deal with large amounts of data Proceedings of the Estonian Academy of Sciences, Engineering, 2005-12 Distributed Computer Control Systems 1988 Th. d'Epinay Lalive, M.G. Rodd, 2014-06-28 Continuing the forward thinking of previously held distributed computer control systems meetings this volume discusses both the positive and negative views on trends in OSI based communications the development of the fieldbus the importance of the incorporation into basic real time operating systems to be used for distributed systems of concepts such as time stamping and access to global time bases and the influence of artificial intelligence based technologies on the distributed computer control world **Embedded Systems: Design, Analysis and Verification** Gunar Schirner, Marcelo Götz, Achim Rettberg, Mauro C. Zanella, Franz J. Rammig, 2013-06-13 This book constitutes the refereed proceedings of the 4th IFIP TC 10 International Embedded Systems Symposium IESS 2013 held in Paderborn Germany in June 2013 The 22 full revised papers presented together with 8 short papers were carefully reviewed and selected from 42 submissions The papers have been organized in the following topical sections design methodologies non functional aspects of embedded systems verification performance analysis real time systems embedded system applications and real time aspects

in distributed systems The book also includes a special chapter dedicated to the BMBF funded ARAMIS project on Automotive Railway and Avionics Multicore Systems *Speculative Execution in High Performance Computer Architectures*
David Kaeli, Pen-Chung Yew, 2005-05-26 Until now there were few textbooks that focused on the dynamic subject of speculative execution a topic that is crucial to the development of high performance computer architectures Speculative Execution in High Performance Computer Architectures describes many recent advances in speculative execution techniques It covers cutting edge research

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