Flash Memories

Jalil Boukhobza,Pierre Olivier

Flash Memories:

   Inside NAND Flash Memories Rino Micheloni,Luca Crippa,Alessia Marelli,2010-07-27 Digital photography MP3 digital video etc make extensive use of NAND based Flash cards as storage media To realize how much NAND Flash memories pervade every aspect of our life just imagine how our recent habits would change if the NAND memories suddenly disappeared To take a picture it would be necessary to find a film as well as a traditional camera disks or even magnetic tapes would be used to record a video or to listen a song and a cellular phone would return to be a simple mean of communication rather than a multimedia console The development of NAND Flash memories will not be set down on the mere evolution of personal entertainment systems since a new killer application can trigger a further success the replacement of Hard Disk Drives HDDs with Solid State Drives SSDs SSD is made up by a microcontroller and several NANDs As NAND is the technology driver for IC circuits Flash designers and technologists have to deal with a lot of challenges Therefore SSD system developers must understand Flash technology in order to exploit its benefits and countermeasure its weaknesses Inside NAND Flash Memories is a comprehensive guide of the NAND world from circuits design analog and digital to Flash reliability including radiation effects from testing issues to high performance DDR interface from error correction codes to NAND applications like Flash cards and SSDs   3D Flash Memories Rino Micheloni,2016-05-26 This book walks the reader through the next step in the evolution of NAND flash memory technology namely the development of 3D flash memories in which multiple layers of memory cells are grown within the same piece of silicon It describes their working principles device architectures fabrication techniques and practical implementations and highlights why 3D flash is a brand new technology After reviewing market trends for both NAND and solid state drives SSDs the book digs into the details of the flash memory cell itself covering both floating gate and emerging charge trap technologies There is a plethora of different materials and vertical integration schemes out there New memory cells new materials new architectures 3D Stacked BiCS and P BiCS 3D FG 3D VG 3D advanced architectures basically each NAND manufacturer has its own solution Chapter 3 to chapter 7 offer a broad overview of how 3D can materialize The 3D wave is impacting emerging memories as well and chapter 8 covers 3D RRAM resistive RAM crosspoint arrays Visualizing 3D structures can be a challenge for the human brain this is way all these chapters contain a lot of bird s eye views and cross sections along the 3 axes The second part of the book is devoted to other important aspects such as advanced packaging technology i e TSV in chapter 9 and error correction codes which have been leveraged to improve flash reliability for decades Chapter 10 describes the evolution from legacy BCH to the most recent LDPC codes while chapter 11 deals with some of the most recent advancements in the ECC field Last but not least chapter 12 looks at 3D flash memories from a system perspective Is 14nm the last step for planar cells Can 100 layers be integrated within the same piece of silicon Is 4 bit cell possible with 3D Will 3D be reliable enough for enterprise and datacenter applications These are some of the questions that

this book helps answering by providing insights into 3D flash memory design process technology and applications **Flash Memories** Paulo Cappelletti,Carla Golla,Piero Olivo,Enrico Zanoni,2013-11-27 A Flash memory is a Non Volatile Memory NVM whose unit cells are fabricated in CMOS technology and programmed and erased electrically In 1971 Frohman Bentchkowsky developed a folating polysilicon gate tran sistor 1 2 in which hot electrons were injected in the floating gate and removed by either Ultra Violet UV internal photoemission or by Fowler Nordheim tunneling This is the unit cell of EPROM Electrically Pro grammable Read Only Memory which consisting of a single transistor can be very densely integrated EPROM memories are electrically programmed and erased by UV exposure for 20 30 mins In the late 1970s there have been many efforts to develop an electrically erasable EPROM which resulted in EEPROMs Electrically Erasable Programmable ROMs EEPROMs use hot electron tunneling for program and Fowler Nordheim tunneling for erase The EEPROM cell consists of two transistors and a tunnel oxide thus it is two or three times the size of an EPROM Successively the combination of hot carrier programming and tunnel erase was rediscovered to achieve a single transistor EEPROM called Flash EEPROM The first cell based on this concept has been presented in 1979 3 the first commercial product a 256K memory chip has been presented by Toshiba in 1984 4 The market did not take off until this technology was proven to be reliable and manufacturable 5 **VLSI-Design of Non-Volatile Memories** Giovanni Campardo,Rino Micheloni,David Novosel,2005-01-18 VLSI Design for Non Volatile Memories is intended for electrical engineers and graduate students who want to enter into the integrated circuit design world Non volatile memories are treated as an example to explain general design concepts Practical illustrative examples of non volatile memories including flash types are showcased to give insightful examples of the discussed design approaches A collection of photos is included to make the reader familiar with silicon aspects Throughout all parts of this book the authors have taken a practical and applications driven point of view providing a comprehensive and easily understood approach to all the concepts discussed Giovanni Campardo and Rino Micheloni have a solid track record of leading design activities at the STMicroelectronics Flash Division David Novosel is President and founder of Intelligent Micro Design Inc Pittsburg PA *Flash Memories* Paulo Cappelletti,Carla Golla,Piero Olivo,2014-09-01 Flash Memory Integration Jalil Boukhobza,Pierre Olivier,2017-03-10 4 zettabytes 4 billion terabytes of data generated in 2013 44 zettabytes predicted for 2020 and 185 zettabytes for 2025 These figures are staggering and perfectly illustrate this new era of data deluge Data has become a major economic and social challenge The speed of processing of these data is the weakest link in a computer system the storage system It is therefore crucial to optimize this operation During the last decade storage systems have experienced a major revolution the advent of flash memory Flash Memory Integration Performance and Energy Issues contributes to a better understanding of these revolutions The authors offer us an insight into the integration of flash memory in computer systems their behavior in performance and in power consumption compared to traditional storage systems The book also presents in their entirety various methods for measuring

the performance and energy consumption of storage systems for embedded as well as desktop server computer systems We are invited on a journey to the memories of the future Ideal for computer scientists featuring low level details to concentrate on system issues Tackles flash memory aspects while spanning domains such as embedded systems and HPC Contains an exhaustive set of experimental results conducted in the Lab STICC laboratory Provides details on methodologies to perform performance and energy measurements on flash storage systems **Memories in Wireless Systems** Rino Micheloni,Giovanni Campardo,Piero Olivo,2008-07-24 For the technological progress in communication technology it is necessary that the advanced studies in circuit and software design are accompanied with recent results of the technological research and physics in order to exceed its limitations This book is a guide which treats many components used in mobile communications and in particular focuses on non volatile memories It emerges following the conducting line of the non volatile memory in the wireless system On the one hand it develops the foundations of the interdisciplinary issues needed for design analysis and testing of the system On the other hand it deals with many of the problems appearing when the systems are realized in industrial production These cover the difficulties from the mobile system to the different types of non volatile memories The book explores memory cards multichip technologies and algorithms of the software management as well as error handling It also presents techniques of assurance for the single components and a guide through the Datasheet lectures *Machine Learning and Non-volatile Memories* Rino Micheloni,Cristian Zambelli,2022-05-25 This book presents the basics of both NAND flash storage and machine learning detailing the storage problems the latter can help to solve At a first sight machine learning and non volatile memories seem very far away from each other Machine learning implies mathematics algorithms and a lot of computation non volatile memories are solid state devices used to store information having the amazing capability of retaining the information even without power supply This book will help the reader understand how these two worlds can work together bringing a lot of value to each other In particular the book covers two main fields of application analog neural networks NNs and solid state drives SSDs After reviewing the basics of machine learning in Chapter 1 Chapter 2 shows how neural networks can mimic the human brain to accomplish this result neural networks have to perform a specific computation called vector by matrix VbM multiplication which is particularly power hungry In the digital domain VbM is implemented by means of logic gates which dictate both the area occupation and the power consumption the combination of the two poses serious challenges to the hardware scalability thus limiting the size of the neural network itself especially in terms of the number of processable inputs and outputs Non volatile memories phase change memories in Chapter 3 resistive memories in Chapter 4 and 3D flash memories in Chapter 5 and Chapter 6 enable the analog implementation of the VbM also called neuromorphic architecture which can easily beat the equivalent digital implementation in terms of both speed and energy consumption SSDs and flash memories are strictly coupled together as 3D flash scales there is a significant amount of work that has to be done in order to optimize the overall performances of SSDs

Machine learning has emerged as a viable solution in many stages of this process After introducing the main flash reliability issues Chapter 7 shows both supervised and un supervised machine learning techniques that can be applied to NAND In addition Chapter 7 deals with algorithms and techniques for a pro active reliability management of SSDs Last but not least the last section of Chapter 7 discusses the next challenge for machine learning in the context of the so called computational storage No doubt that machine learning and non volatile memories can help each other but we are just at the beginning of the journey this book helps researchers understand the basics of each field by providing real application examples hopefully providing a good starting point for the next level of development **CMOS Processors and Memories** Krzysztof Iniewski,2010-08-09 CMOS Processors and Memories addresses the state of the art in integrated circuit design in the context of emerging computing systems New design opportunities in memories and processor are discussed Emerging materials that can take system performance beyond standard CMOS like carbon nanotubes graphene ferroelectrics and tunnel junctions are explored CMOS Processors and Memories is divided into two parts processors and memories In the first part we start with high performance low power processor design followed by a chapter on multi core processing They both represent state of the art concepts in current computing industry The third chapter deals with asynchronous design that still carries lots of promise for future computing needs At the end we present a hardware design space exploration methodology for implementing and analyzing the hardware for the Bayesian inference framework This particular methodology involves analyzing the computational cost and exploring candidate hardware components proposing various custom architectures using both traditional CMOS and hybrid nanotechnology CMOL The first part concludes with hybrid CMOS Nano architectures The second memory part covers state of the art SRAM DRAM and flash memories as well as emerging device concepts Semiconductor memory is a good example of the full custom design that applies various analog and logic circuits to utilize the memory cell s device physics Critical physical effects that include tunneling hot electron injection charge trapping Flash memory are discussed in detail Emerging memories like FRAM PRAM and ReRAM that depend on magnetization electron spin alignment ferroelectric effect built in potential well quantum effects and thermal melting are also described CMOS Processors and Memories is a must for anyone serious about circuit design for future computing technologies The book is written by top notch international experts in industry and academia It can be used in graduate course curriculum *Fast, Efficient and Predictable Memory Accesses* Lars Wehmeyer,Peter Marwedel,2006-09-08 Speed improvements in memory systems have not kept pace with the speed improvements of processors leading to embedded systems whose performance is limited by the memory This book presents design techniques for fast energy efficient and timing predictable memory systems that achieve high performance and low energy consumption In addition the use of scratchpad memories significantly improves the timing predictability of the entire system leading to tighter worst case execution time bounds **Error Correction Codes for Non-Volatile Memories** Rino Micheloni,A. Marelli,R. Ravasio,2008-06-03 Nowadays it is

hard to find an electronic device which does not use codes for example we listen to music via heavily encoded audio CD s and we watch movies via encoded DVD s There is at least one area where the use of encoding decoding is not so developed yet Flash non volatile memories Flash memory high density low power cost effectiveness and scalable design make it an ideal choice to fuel the explosion of multimedia products like USB keys MP3 players digital cameras and solid state disk In ECC for Non Volatile Memories the authors expose the basics of coding theory needed to understand the application to memories as well as the relevant design topics with reference to both NOR and NAND Flash architectures A collection of software routines is also included for better understanding The authors form a research group now at Qimonda which is the typical example of a fruitful collaboration between mathematicians and engineers *Nonvolatile Memory Design* Hai Li,Yiran Chen,2017-12-19 The manufacture of flash memory which is the dominant nonvolatile memory technology is facing severe technical barriers So much so that some emerging technologies have been proposed as alternatives to flash memory in the nano regime Nonvolatile Memory Design Magnetic Resistive and Phase Changing introduces three promising candidates phase change memory magnetic random access memory and resistive random access memory The text illustrates the fundamental storage mechanism of these technologies and examines their differences from flash memory techniques Based on the latest advances the authors discuss key design methodologies as well as the various functions and capabilities of the three nonvolatile memory technologies Nanoelectronics: Physics, Materials and Devices Angsuman Sarkar,Chandan Kumar Sarkar,Arpan Deyasi,Debashis De,Arezki Benfdila,2023-01-03 Approx 528 pagesApprox 528 pages

Semiconductor Technology (ISTC 2001) Ming Yang,2001 **Microprocessors and Microcomputer-Based System Design** Mohamed Rafiquzzaman,2021-11-01 Microprocessors and Microcomputer Based System Design Second Edition builds on the concepts of the first edition It discusses the basics of microprocessors various 32 bit microprocessors the 8085 microprocessor the fundamentals of peripheral interfacing and Intel and Motorola microprocessors This edition includes new topics such as floating point arithmetic Program Array Logic and flash memories It covers the popular Intel 80486 80960 and Motorola 68040 as well as the Pentium and PowerPC microprocessors The final chapter presents system design concepts applying the design principles covered in previous chapters to sample problems Federal Register ,1991-12-06 Encyclopedia of Polymer Applications, 3 Volume Set Munmaya Mishra,2018-12-17 Undoubtedly the applications of polymers are rapidly evolving Technology is continually changing and quickly advancing as polymers are needed to solve a variety of day to day challenges leading to improvements in quality of life The Encyclopedia of Polymer Applications presents state of the art research and development on the applications of polymers This groundbreaking work provides important overviews to help stimulate further advancements in all areas of polymers This comprehensive multi volume reference includes articles contributed from a diverse and global team of renowned researchers It offers a broad based perspective on a multitude of topics in a variety of applications as well as detailed research information figures tables

illustrations and references The encyclopedia provides introductions classifications properties selection types technologies shelf life recycling testing and applications for each of the entries where applicable It features critical content for both novices and experts including engineers scientists polymer scientists materials scientists biomedical engineers macromolecular chemists researchers and students as well as interested readers in academia industry and research institutions   *Semiconductor Memories and Systems* Andrea Redaelli,Fabio Pellizzer,2022-06-07 Semiconductor Memories and Systems provides a comprehensive overview of the current state of semiconductor memory at the technology and system levels After an introduction on market trends and memory applications the book focuses on mainstream technologies illustrating their current status challenges and opportunities with special attention paid to scalability paths Technologies discussed include static random access memory SRAM dynamic random access memory DRAM non volatile memory NVM and NAND flash memory Embedded memory and requirements and system level needs for storage class memory are also addressed Each chapter covers physical operating mechanisms fabrication technologies and the main challenges to scalability Finally the work reviews the emerging trends for storage class memory mainly focusing on the advantages and opportunities of phase change based memory technologies Features contributions from experts from leading companies in semiconductor memory Discusses physical operating mechanisms fabrication technologies and paths to scalability for current and emerging semiconductor memories Reviews primary memory technologies including SRAM DRAM NVM and NAND flash memory Includes emerging storage class memory technologies such as phase change memory   Inside Solid State Drives (SSDs) Rino Micheloni,Alessia Marelli,Kam Eshghi,2018-07-11 The revised second edition of this respected text provides a state of the art overview of the main topics relating to solid state drives SSDs covering NAND flash memories memory controllers including booth hardware and software I O interfaces PCIe SAS SATA reliability error correction codes BCH and LDPC encryption flash signal processing and hybrid storage Updated throughout to include all recent work in the field significant changes for the new edition include A new chapter on flash memory errors and data recovery procedures in SSDs for reliability and lifetime improvement Updated coverage of SSD Architecture and PCI Express Interfaces moving from PCIe Gen3 to PCIe Gen4 and including a section on NVMe over fabric NVMf An additional section on 3D flash memories An update on standard reliability procedures for SSDs Expanded coverage of BCH for SSDs with a specific section on detection A new section on non binary Low Density Parity Check LDPC codes the most recent advancement in the field A description of randomization in the protection of SSD data against attacks particularly relevant to 3D architectures The SSD market is booming with many industries placing a huge effort in this space spending billions of dollars in R D and product development Moreover flash manufacturers are now moving to 3D architectures thus enabling an even higher level of storage capacity This book takes the reader through the fundamentals and brings them up to speed with the most recent developments in the field and is suitable for advanced students researchers and engineers alike   *Memory, Microprocessor, and ASIC* Wai-Kai

Chen,2003-03-26 Timing memory power dissipation testing and testability are all crucial elements of VLSI circuit design In this volume culled from the popular VLSI Handbook experts from around the world provide in depth discussions on these and related topics Stacked gate embedded and flash memory all receive detailed treatment including their power cons

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