

Josephson computer technology

Itaru Kurosawa, Hiroshi Nakagawa, Masahiro Aoyagi, and Susumu Takada

A major obstacle to the further miniaturization and higher speed of computers is the dissipation on an integrated circuit chip. The use of Josephson junction devices is potentially a solution to this problem and much research was done in the 1980s. Results were disappointing, mainly because of technical problems in making stable junction electrodes. Recently, however, a new approach by Japanese workers shows how these difficulties may be overcome and computers based on the Josephson effect are again the subject of much research.

The rapid proliferation of information-processing machines - such as multi-scale personal computers, work stations, and mini and large scale computers - has been achieved on the basis of tremendous progress in integrated circuit (IC) technology. Miniaturization, reducing the dimensions of every component fabricated on an IC chip, is one of the most important techniques which have determined the progress of semiconductor technology during the past few decades. In the future, however, as the dimensions become smaller and smaller, semiconductor technology will face some fundamental problems around the submicrometer level. Among these is the scaling problem which arises from the power dissipated by a great number of transistors on an IC chip: this is serious and will set a limit to how closely and how many transistors can be packed together. The Josephson technology - which utilizes some advantageous characteristics of Josephson junction devices made from

superconductors - is one of the candidates for solving such scaling problems. Josephson junctions dissipate only one-thousandth as much power as semiconductor transistors do.

This article reviews the challenging technology involved in achieving an ultrahigh-speed superconducting computer in which Josephson devices are incorporated as logic and memory circuits.

Why Josephson computers?

Larger computing power is continuously demanded in various fields of scientific and technological computation such as weather forecasting, nuclear fusion research, speech recognition, image understanding, satellite land surveying, and so on. For these purposes supercomputers, which are computers specialized for high-speed work, have been developed and widely used. One measure of a machine's processing speed is the cycle time. All the signals flowing through the circuitry of a computer are periodically synchronized by an internal clock that has a characteristic period or cycle time. Modern large-scale computers, which are based on semiconductor technology, have cycle times of about 10 nanoseconds. These periods are not determined only by the switching time of circuit devices but also by the propagation delay time of electric signals traveling through the circuitry. In large-scale computers, the delay time of signal propagation now occupies more than 50 per cent of the cycle time. Even if we could use an ideal device whose switching time was short as to be negligible, the speed of a computer system is improved by a factor of only about 2. The delay time of signal propagation is caused by the velocity of light. Present computers have a main-frame volume about 1 cubic meter. The cycle time of about 10 nanoseconds of these machines is determined by the propagation delay of electric signals with light velocity (light velocity in the circuitry is about one-third of the light velocity in vacuum) through a path about 1 meter long. If we want a ten times faster supercomputer, we must decrease the

physical size of the computer assembly to a volume about 10⁻³ m³ cubed. However, switching devices generate heat during operation due to their power dissipation. The 10 to 100 milliwatts dissipation in the logic and memory circuits of a supercomputer made of semiconductors would emit so much heat that a machine of such small size would glow like a stove. Josephson junction devices offer ultra high speed operation simultaneously with very low power dissipation. A Josephson computer could, therefore, be installed in this small volume. Since the 1970s, therefore, much effort has been made to develop such a computer.

History

In 1962, H. D. Josephson of Cambridge University predicted theoretically that superconducting electron pairs could tunnel through a thin insulating barrier sandwiched by two superconductor electrodes [1]. The effect was experimentally verified in the following year by P. W. Anderson and J. M. Rowell of Bell Telephone Laboratories [2]. This device is called a Josephson tunneling junction. As shown in Figure 1(a), a Josephson junction is formed when a sufficiently thin layer of insulating material (an oxide, for example) is sandwiched between two layers of a superconductor such as niobium. Penetration of the oxide layer by the magnetic field functions that describe the superconducting state leads to electron pair tunneling, and a superconducting current flows across the junction without any applied voltage. Figure 1(b) shows a typical current-voltage relationship characteristic of a Josephson junction. If the current exceeds a critical threshold value, a voltage appears across the junction. This transition between a zero and a finite voltage at a particular current threshold (that is a critical current) can be exploited for logic circuits. In 1963, Mahan of IBM discovered, and a logic circuit with sub-nanosecond switching time which was very attractive at the time as a competitor to semiconductor devices [3]. On the basis of initially promising results,

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Editorial: "New Surface," Volume 18, No. 3, 1990.

0-905-3575-91/90/0000-0000.

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Fluxoid Josephson Computer Technology

Eiichi Goto, Willy Hioe



Fluxoid Josephson Computer Technology:

Fluxoid Josephson Computer Technology Rikagaku Kenkyūjo (Japan),1988 **Advances In Quantum Flux**

Parametron Computer Design: Studies In Josephson Supercomputers Eiichi Goto,Kia Fock Loe,Y Wada,1992-06-18

This volume reports new developments on work in the Quantum Flux Parametron QFP project It makes complete a series on Josephson Supercomputers which includes four earlier volumes also published by World Scientific QFP technology has great potential especially in the design of computer architecture It is regarded as being able to go beyond the horizon of current technology and is a leading direction for the advancement of computer technology in the next decade **Issues In**

Josephson Supercomputer Design - Proceedings Of The 6th And 7th Riken Symposia On Josephson Electronics

Eiichi Goto,Kia Fock Loe,1991-03-15 Quantum Flux Parametron QFP is a Josephson junction device which uses the polarities of a unit of quantum flux to represent a bit of binary information The low power consumption and the inductive nature of signal transfer of QFP promise the realizability of a highly integrated Josephson Supercomputer The miniaturization and the inductively connected circuits pose new challenges and problems to the designers of such a computer system This volume contains five papers which provide detailed illustrations of the essential problems in the design of Josephson Supercomputers The problems include i the three dimensional packaging problems in the highly integrated system ii the detection of trapped magnetic quanta iii the computation of three dimensional inductance and iv the elimination or alleviation of various internal and external operational problems of QFP arising from the limitation of the current fabrication technology as well as the inherent problems of direct coupling of QFP *Quantum Flux Parametron* Eiichi Goto,Willy Hioe,1991-03-30 This book

concerns a Josephson device for supercomputers which has extremely low heat dissipation about 106 times less than semiconductor devices and 103 times less than voltage based Josephson devices In the previous book on Quantum Flux Parametrons QFPs DC Flux Parametron the basic device operation are described This book deals in much greater depth on the problems which are faced by the QFP The device characteristics are worked out in detail showing clearly the analysis methods used A new logic gate using the QFP is described with respect to its basic scheme operation and ways for forming logic circuits The problems faced by the basic QFP are much reduced in the new logic gate As the QFP operates near the Heisenberg and Boltzmann limits for computing devices we also show the relationship between speed and stability The book contains the latest analytical results on QFPs The material presented in the book can be understood with very little mathematical training or knowledge about superconducting physics It is also self contained and does not require reading of other material Most of the device characteristics can be reproduced from the equations given using simple programs A circuit simulator is not needed except for high speeds when transient behavior becomes important **Scientific**

Information Bulletin ,1990 *Superconductivity* A. Bisarsh,1999 This book consists of over 600 selected descriptions and abstracts of books book chapters patents and journal articles from throughout the world dealing with this high profile topic

Each citation contains complete bibliographic data plus key words The entries are grouped under the headings of Theory of Superconductivity Superconducting Devices Superconducting Properties of Materials Applications of Superconductors

Author Index Subject Index 超伝導の基礎と応用 (Japan),1972 超伝導の基礎と応用 ,1992

Japanese Science and Technology ,1986 **Research and Development Consortia in Innovation in Japan** Gerald Jiro Hane,1992 *High-speed Integrated Circuit Technology* Mark J. W. Rodwell,2001 This book reviews the state of the art of very high speed digital integrated circuits Commercial applications are in fiber optic transmission systems operating at 10 40 and 100 Gb s while the military application is ADCs and DACs for microwave radar The book contains detailed descriptions of the design fabrication and performance of wideband Si SiGe GaAs and InP based bipolar transistors The analysis design and performance of high speed CMOS silicon bipolar and III V digital ICs are presented in detail with emphasis on application in optical fiber transmission and mixed signal ICs The underlying physics and circuit design of rapid single flux quantum RSFQ superconducting logic circuits are reviewed and there is extensive coverage of recent integrated circuit results in this technology Contents Preface M J W Rodwell High Speed and High Data Bandwidth Transmitter and Receiver for Multi Channel Serial Data Communication with CMOS Technology M Fukaishi et al High Performance Si and SiGe Bipolar Technologies and Circuits M Wurzer et al Self Aligned Si BJT SiGe HBT Technology and Its Application to High Speed Circuits K Washio Small Scale InGaP GaAs Heterojunction Bipolar Transistors for High Speed and Low Power Integrated Circuit Applications T Oka et al Prospects of InP Based IC Technologies for 100 Gbit S Class Lightwave Communications Systems T Enoki et al Scaling of InGaAs InAlAs HBTs for High Speed Mixed Signal and mm Wave ICs M J W Rodwell Progress Toward 100 GHz Logic in InP HBT IC Technology C H Fields et al Cantilevered Base InP DHBT for High Speed Digital Applications A L Gutierrez Aitken et al RSFQ Technology Physics and Devices P Bunyk et al RSFQ Technology Circuits and Systems D K Brock Readership Researchers industrialists and academics in electrical and electronic engineering

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Superconductivity Electronics ,1987 Predicasts Technology Update ,1987 *Forthcoming Books* Rose Army,1989-09 Books in Print Supplement ,1994 **Applications of Superconductivity** H. Weinstock,2013-03-09 This book in essence the proceedings of a NATO Advanced Study Institute with the same title is designed to provide in depth coverage of many but not all of the major current applications of superconductivity and of many that still are being developed It will be of value to scientists and engineers who have interests in the research and production aspects of the technology as well as in the applications themselves The first three chapters by Clarke Vrba and Wikswo are devoted to an understanding of the principles fabrication and uses of SQUID magnetometers and gradiometers with the greatest emphasis on biomagnetism and nondestructive evaluation NDE For the most part traditional low temperature superconductor LTS

SQUIDS are used but particularly for NDE high temperature superconductor HTS SQUIDS are proving useful and often more convenient The succeeding three chapters by Przybysz Likharev and Chaloupka cover broader aspects of superconducting electronics The first two of these deal primarily with digital L TS circuits while the third discusses in great detail passive component applications using HTS materials Currently HTS filters are undergoing intense J3 site testing at cellular telephone base stations While it is clear that HTS filters outperform conventional filters in reducing signal loss and allowing for more channels in a given bandwidth it isn't yet certain that the cellular telephone industry sees sufficient economic benefits to make a firm decision to use HTS filters universally in its systems If this application is generally adapted the market for these filters should be quite large

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