

# **High-Level Test Synthesis of Digital VLSI Circuits**

**Mike Tien-Chien Lee**

# Highlevel Test Synthesis Of Digital Vlsi Circuits

**Luciano Lavagno,Igor L. Markov,Grant  
Martin,Louis K. Scheffer**

## **Highlevel Test Synthesis Of Digital Vlsi Circuits:**

High-level Test Synthesis of Digital VLSI Circuits Mike Tien-Chien Lee,1997 Here is the first book to propose HTS as a complete more effective design approach the author explains how HTS unlike most existing high level synthesis techniques that optimize the circuit architecture for area and performance only is able to explore the synthesis freedom provided at high level to derive an inherently testable architecture at low or even no overhead By permitting testing from the earliest design stages to minimize or even eliminate serious testing problems HTS boosts design quality and shortens the development cycle

**Principles of Testing Electronic Systems** Samiha Mourad,Yervant Zorian,2000-07-25 A pragmatic approach to testing electronic systems As we move ahead in the electronic age rapid changes in technology pose an ever increasing number of challenges in testing electronic products Many practicing engineers are involved in this arena but few have a chance to study the field in a systematic way learning takes place on the job By covering the fundamental disciplines in detail Principles of Testing Electronic Systems provides design engineers with the much needed knowledge base Divided into five major parts this highly useful reference relates design and tests to the development of reliable electronic products shows the main vehicles for design verification examines designs that facilitate testing and investigates how testing is applied to random logic memories FPGAs and microprocessors Finally the last part offers coverage of advanced test solutions for today s very deep submicron designs The authors take a phenomenological approach to the subject matter while providing readers with plenty of opportunities to explore the foundation in detail Special features include An explanation of where a test belongs in the design flow Detailed discussion of scan path and ordering of scan chains BIST solutions for embedded logic and memory blocks Test methodologies for FPGAs A chapter on testing system on a chip Numerous references      **Proceedings of the ...**

**ACM Great Lakes Symposium on VLSI.** ,2007      **High-Level Synthesis** Philippe Coussy,Adam Morawiec,2008-08-01 This book presents an excellent collection of contributions addressing different aspects of high level synthesis from both industry and academia It includes an overview of available EDA tool solutions and their applicability to design problems

*Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits* M. Bushnell,Vishwani Agrawal,2006-04-11 The modern electronic testing has a forty year history Test professionals hold some fairly large conferences and numerous workshops have a journal and there are over one hundred books on testing Still a full course on testing is offered only at a few universities mostly by professors who have a research interest in this area Apparently most professors would not have taken a course on electronic testing when they were students Other than the computer engineering curriculum being too crowded the major reason cited for the absence of a course on electronic testing is the lack of a suitable textbook For VLSI the foundation was provided by semiconductor device techn ogy circuit design and electronic testing In a computer engineering curriculum therefore it is necessary that foundations should be taught before applications The field of VLSI has expanded to systems on a chip which include digital memory and mixed signalsubsystems To our

knowledge this is the first textbook to cover all three types of electronic circuits We have written this textbook for an undergraduate foundations course on electronic testing Obviously it is too voluminous for a one semester course and a teacher will have to select from the topics We did not restrict such freedom because the selection may depend upon the individual expertise and interests Besides there is merit in having a larger book that will retain its usefulness for the owner even after the completion of the course With equal tenacity we address the needs of three other groups of readers IEEE International Symposium on Circuits and Systems IEEE Circuits and Systems Society, 2003 These volumes relate to matters discussed during the 2003 IEEE International Symposium on Circuits and Systems such as analogue circuits and signal processing communications multimedia systems and applications general and nonlinear circuits and systems and neural networks and systems *Testing of Digital Systems* N. K. Jha, S. Gupta, 2003-05-08 Device testing represents the single largest manufacturing expense in the semiconductor industry costing over 40 billion a year The most comprehensive and wide ranging book of its kind Testing of Digital Systems covers everything you need to know about this vitally important subject Starting right from the basics the authors take the reader through automatic test pattern generation design for testability and built in self test of digital circuits before moving on to more advanced topics such as IDDQ testing functional testing delay fault testing memory testing and fault diagnosis The book includes detailed treatment of the latest techniques including test generation for various fault models discussion of testing techniques at different levels of integrated circuit hierarchy and a chapter on system on a chip test synthesis Written for students and engineers it is both an excellent senior graduate level textbook and a valuable reference **VLSI Custom Microelectronics** Stanley L. Hurst, 1998-11-05 Focuses on the design and production of integrated circuits specifically designed for a particular application from original equipment manufacturers The book outlines silicon and GaAs semiconductor fabrication techniques and circuit configurations compares custom design style discusses computer aided design tools and more VLSI Testing Stanley Leonard Hurst, 1998 Hurst an editor at the Microelectronics Journal analyzes common problems that electronics engineers and circuit designers encounter while testing integrated circuits and the systems in which they are used and explains a variety of solutions available for overcoming them in both digital and mixed circuits Among his topics are faults in digital circuits generating a digital test pattern signatures and self tests structured design for testability testing structured digital circuits and microprocessors and financial aspects of testing The self contained reference is also suitable as a textbook in a formal course on the subject Annotation copyrighted by Book News Inc Portland OR **EDA for IC System Design, Verification, and Testing** Louis Scheffer, Luciano Lavagno, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The first volume EDA for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logical verification and testing Chapters contributed by

leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for IC designs design and verification languages digital simulation hardware acceleration and emulation and much more Save on the complete set VLSI Test Principles and Architectures Laung-Terng Wang, Cheng-Wen Wu, Xiaoqing Wen, 2006-08-14 This book is a comprehensive guide to new DFT methods that will show the readers how to design a testable and quality product drive down test cost improve product quality and yield and speed up time to market and time to volume Most up to date coverage of design for testability Coverage of industry practices commonly found in commercial DFT tools but not discussed in other books Numerous practical examples in each chapter illustrating basic VLSI test principles and DFT architectures Advances in Silicon Carbide Processing and Applications Stephen E. Sadow, Anant K. Agarwal, 2004 Learn the latest advances in SiC Silicon Carbide technology from the leading experts in the field with this new cutting edge resource The book is your single source for in depth information on both SiC device fabrication and system level applications This comprehensive reference begins with an examination of how SiC is grown and how defects in SiC growth can affect working devices Key issues in selective doping of SiC via ion implantation are covered with special focus on implant conditions and electrical activation of implants SiC applications discussed include chemical sensors motor control components high temperature gas sensors and high temperature electronics By cutting through the arcane data and jargon surrounding the hype on SiC this book gives an honest assessment of today s SiC technology and shows you how SiC can be adopted in developing tomorrow s applications Program Solicitation ,1998

**Design and Testing of Digital Microfluidic Biochips** Yang Zhao, Krishnendu Chakrabarty, 2012-07-25 This book provides a comprehensive methodology for automated design test and diagnosis and use of robust low cost and manufacturable digital microfluidic systems It focuses on the development of a comprehensive CAD optimization framework for digital microfluidic biochips that unifies different design problems With the increase in system complexity and integration levels biochip designers can utilize the design methods described in this book to evaluate different design alternatives and carry out design space exploration to obtain the best design point **Advances in Computers** ,1993-09-14 *Advances in Computers* *Electronic Design Automation for IC System Design, Verification, and Testing* Luciano Lavagno, Igor L. Markov, Grant Martin, Louis K. Scheffer, 2017-12-19 The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook Second Edition Electronic Design Automation for IC System Design Verification and Testing thoroughly examines system level design microarchitectural design logic verification and testing Chapters contributed by leading experts authoritatively discuss processor modeling and design tools using performance metrics to select microprocessor cores for integrated circuit IC designs design and verification languages digital simulation hardware acceleration and emulation and much more New to This Edition Major updates appearing in the initial phases of the design flow where the level of abstraction keeps rising to support more functionality with lower non recurring engineering NRE

costs Significant revisions reflected in the final phases of the design flow where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting edge applications and approaches realized in the decade since publication of the previous edition these are illustrated by new chapters on high level synthesis system on chip SoC block based design and back annotating system level models Offering improved depth and modernity Electronic Design Automation for IC System Design Verification and Testing provides a valuable state of the art reference for electronic design automation EDA students researchers and professionals **Springer Handbook of Automation** Shimon Y. Nof, 2009-07-16 Automation is undergoing a major transformation in scope and dimension and plays an increasingly important role in the global economy and in our daily lives Engineers combine automated devices with mathematical and organizational tools to create complex systems for a rapidly expanding range of applications and human activities This handbook incorporates these new developments and presents a widespread and well structured conglomeration of new emerging application areas of automation Besides manufacturing as a primary application of automation the handbook contains new application areas such as medical systems and health transportation security and maintenance service construction and retail as well as production or logistics This Springer Handbook is not only an ideal resource for automation experts but also for people new to this expanding field such as engineers medical doctors computer scientists designers It is edited by an internationally renowned and experienced expert **Scientific and Technical Aerospace Reports**, 1994 **Readings in Hardware/Software Co-Design** Giovanni De Micheli, Rolf Ernst, Wayne Wolf, 2002 This title serves as an introduction and reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s **Digital Microfluidic Biochips** Krishnendu Chakrabarty, Tao Xu, 2010-05-03 Microfluidics based biochips combine electronics with biochemistry providing access to new application areas in a wide variety of fields Continued technological innovations are essential to assuring the future role of these chips in functional diversification in biotech pharmaceuticals and other industries Revolutionary guidance on design opti

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

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