

Jeffrey J. Joyce

Carl-Johan H. Seger (Eds.)

## Higher Order Logic Theorem Proving and Its Applications

6th International Workshop, HUG '93  
Vancouver, B. C., Canada, August 1993  
Proceedings



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# Higher Order Logic Theorem Proving Its

**Joe Hurd**



## Higher Order Logic Theorem Proving Its:

Higher Order Logic Theorem Proving and its Applications L.J.M. Claesen, M.J.C. Gordon, 2014-05-23 The HOL system is a higher order logic theorem proving system implemented at Edinburgh University Cambridge University and INRIA Its many applications from the verification of hardware designs at all levels to the verification of programs and communication protocols are considered in depth in this volume Other systems based on higher order logic namely Nuprl and LAMBDA are also discussed Features given particular consideration are novel developments in higher order logic and its implementations in HOL formal design and verification methodologies for hardware and software public domain availability of the HOL system Papers addressing these issues have been divided as follows Mathematical Logic Induction General Modelling and Proofs Formalizing and Modelling of Automata Program Verification Hardware Description Language Semantics Hardware Verification Methodologies Simulation in Higher Order Logic Extended Uses of Higher Order Logic Academic and industrial researchers involved in formal hardware and software design and verification methods should find the publication especially interesting and it is hoped it will also provide a useful reference tool for those working at software institutes and within the electronics industries *Higher Order Logic Theorem Proving and Its Applications* E. Thomas Schubert, Phillip J.

Windley, James Alves-Foss, 1995-08-23 This book constitutes the proceedings of the 8th International Conference on Higher Order Logic Theorem Proving and Its Applications held in Aspen Grove Utah USA in September 1995 The 26 papers selected by the program committee for inclusion in this volume document the advances in the field achieved since the predecessor conference The papers presented fall into three general categories representation of formalisms in higher order logic applications of mechanized higher order logic and enhancements to the HOL and other theorem proving systems **Higher**

**Order Logic Theorem Proving and Its Applications** Thomas F. Melham, 1994-09-07 This volume presents the proceedings of the 7th International Workshop on Higher Order Logic Theorem Proving and Its Applications held in Valetta Malta in September 1994 Besides 3 invited papers the proceedings contains 27 refereed papers selected from 42 submissions In total the book presents many new results by leading researchers working on the design and applications of theorem provers for higher order logic In particular this book gives a thorough state of the art report on applications of the HOL system one of the most widely used theorem provers for higher order logic Higher Order Logic Theorem Proving and Its Applications

Jeffrey J. Joyce, Carl-Johan H. Seger, 1994-04-28 This volume constitutes the refereed proceedings of the 1993 Higher Order Logic User's Group Workshop held at the University of British Columbia in August 1993 The workshop was sponsored by the Centre for Integrated Computer System Research It was the sixth in the series of annual international workshops dedicated to the topic of Higher Order Logic theorem proving its usage in the HOL system and its applications The volume contains 40 papers including an invited paper by David Parnas McMaster University Canada entitled Some theorems we should prove

*Theorem Proving in Higher Order Logics* Joe Hurd, 2005-08-08 This book constitutes the refereed proceedings of the 18th

International Conference on Theorem Proving in Higher Order Logics TPHOLs 2005 held in Oxford UK in August 2005 The 20 revised full papers presented together with 2 invited papers and 4 proof pearls concise and elegant presentations of interesting examples were carefully reviewed and selected from 49 submissions All current issues in HOL theorem proving and formal verification of software and hardware systems are addressed Among the topics of this volume are theorem proving verification recursion and induction mechanized proofs mathematical logic proof theory type systems program verification and proving systems like HOL Coq ACL2 Isabelle HOL and Isabelle HOLCF

*Higher Order Logic Theorem Proving and Its Applications* Luc J. M. Claesen, Michael J. C. Gordon, 1993 The HOL system is a higher order logic theorem proving system implemented at Edinburgh University Cambridge University and INRIA Its many applications from the verification of hardware designs at all levels to the verification of programs and communication protocols are considered in depth in this volume Other systems based on higher order logic namely Nuprl and LAMBDA are also discussed Features given particular consideration are novel developments in higher order logic and its implementations in HOL formal design and verification methodologies for hardware and software public domain availability of the HOL system Papers addressing these issues have been divided as follows Mathematical Logic Induction General Modelling and Proofs Formalizing and Modelling of Automata Program Verification Hardware Description Language Semantics Hardware Verification Methodologies Simulation in Higher Order Logic Extended Uses of Higher Order Logic Academic and industrial researchers involved in formal hardware and software design and verification methods should find the publication especially interesting and it is hoped it will also provide a useful reference tool for those working at software institutes and within the electronics industries

**Higher Order Logic Theorem Proving and Its Applications**, 1995 Theorem Proving in Higher Order Logics Richard J. Boulton, Paul B. Jackson, 2003-06-30 This volume constitutes the proceedings of the 14th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2001 held 3-6 September 2001 in Edinburgh Scotland TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and verification TPHOLs 2001 was colocated with the 11th Advanced Research Working Conference on Correct Hardware Design and Verification Methods CHARME 2001 This was held 4-7 September 2001 in nearby Livingston Scotland at the Institute for System Level Integration and a joint half day session of talks was arranged for the 5th September in Edinburgh An excursion to Traquair House and a banquet in the Playfair Library of Old College University of Edinburgh were also jointly organized The proceedings of CHARME 2001 have been published as volume 2144 of Springer Verlag's Lecture Notes in Computer Science series with Tiziana Margaria and Tom Melham as editors Each of the 47 papers submitted in the full research category was refereed by at least 3 reviewers who were selected by the Program Committee Of these submissions 23 were accepted for presentation at the conference and publication in this volume In keeping with tradition TPHOLs 2001 also offered a venue for the presentation of work in progress where researchers invite discussion by means of a brief preliminary

talk and then discuss their work at a poster session A supplementary proceedings containing associated papers for work in progress was published by the Division of Informatics at the University of Edinburgh Theorem Proving in Higher Order Logics David Basin,Burkhart Wolff,2003-08-21 This volume constitutes the proceedings of the16th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2003 held September 8 12 2003 in Rome Italy TPHOLs covers all aspects of theorem proving in higher order logics as well as related topics in theorem proving and veri cation TPHOLs 2003 was co located with TABLEAUX the International Con rence on Automated Reasoning with Analytic Tableaux and Related Methods and with Calculemus the Symposium on the Integration of Symbolic Compu tion and Mechanized Reasoning There were 50 papers submitted to TPHOLs in the full research category each of which was refereed by at least 3 reviewers selected by the program c mittee Ofthesesubmissions 21wereacceptedforpresentationattheconference and publication in this volume In keeping with tradition TPHOLs 2003 also o ered a venue for the presentation of work in progress where researchers vite discussion by means of a brief preliminary talk and then discuss their work at a poster session A supplementary proceedings containing associated papers for work in progress was published by the computer science department at the Universit at Freiburg The organizers are grateful to Jean Raymond Abrial Patrick Lincoln and Dale Miller for agreeing to give invited talks at TPHOLs 2003 The TPHOLs conference traditionally changes continent each year in order to maximize the chances that researchers from around the world can attend Theorem Proving in Higher Order Logics Yves Bertot,Gilles Dowek,Andre Hirschowitz,Christine Paulin,Laurent Thery,2003-07-31 This book constitutes the refereed proceedings of the 12th International Conference on Theorem Proving in Higher Order Logics TPHOLs 99 held in Nice France in September 1999 The 20 revised full papers presented together with three invited contributions were carefully reviewed and selected from 35 papers submitted All current aspects of higher order theorem proving formal verification and specification are discussed Among the theorem provers evaluated are COQ HOL Isabelle Isabelle ZF and OpenMath

**Theorem Proving in Higher Order Logics** Stefan Berghofer,Tobias Nipkow,Christian Urban,Makarius Wenzel,2009-08-20 This book constitutes the refereed proceedings of the 22nd International Conference on Theorem Proving in Higher Order Logics TPHOLs 200 held in Munich Germany in August 2009 The 26 revised full papers presented together with 1 proof pearl 4 tool presentations and 3 invited papers were carefully reviewed and selected from 55 submissions The papers cover all aspects of theorem proving in higher order logics as well as related topics in theorem proving and verification such as formal semantics of specification modeling and programming languages specification and verification of hardware and software formalization of mathematical theories advances in theorem prover technology as well as industrial application of theorem provers **Theorem Proving in Higher Order Logics** Jim Grundy,Malcolm Newey,1998-09-09 This book constitutes the refereed proceedings of the 11th International Conference on Theorem Proving in Higher Order Logics TPHOLs 98 held in Canberra Australia in September October 1998 The 26 revised full papers presented were carefully

reviewed and selected from a total of 52 submissions Also included are two invited papers The papers address all current aspects of theorem proving in higher order logics and formal verification and program analysis Besides the HOL system the theorem provers Coq Isabelle LAMBDA LEGO NuPrl and PVS are discussed Theorem Proving in Higher Order Logics Mark Aagaard, John Harrison, 2007-07-23 This volume is the proceedings of the 13th International Conference on Theorem Proving in Higher Order Logics TPHOLs 2000 held 14-18 August 2000 in Portland Oregon USA Each of the 55 papers submitted in the full research category was refereed by at least three reviewers who were selected by the program committee Because of the limited space available in the program and proceedings only 29 papers were accepted for presentation and publication in this volume In keeping with tradition TPHOLs 2000 also offered a venue for the presentation of work in progress where researchers invite discussion by means of a brief preliminary talk and then discuss their work at a poster session A supplementary proceedings containing associated papers for work in progress was published by the Oregon Graduate Institute OGI as technical report CSE 00-009 The organizers are grateful to Bob Colwell Robin Milner and Larry Wos for agreeing to give invited talks Bob Colwell was the lead architect on the Intel P6 microarchitecture which introduced a number of innovative techniques and achieved enormous commercial success As such he is ideally placed to offer an industrial perspective on the challenges for formal verification Robin Milner contributed many key ideas to computer theorem proving and to functional programming through his leadership of the influential Edinburgh LCF project Higher Order Logic Theorem Proving and Its Applications, 1992 **Automated Reasoning** Nicolas Peltier, Viorica Sofronie-Stokkermans, 2020-06-30 This two volume set LNAI 12166 and 12167 constitutes the refereed proceedings of the 10th International Joint Conference on Automated Reasoning IJCAR 2020 held in Paris France in July 2020 In 2020 IJCAR was a merger of the following leading events namely CADE International Conference on Automated Deduction FroCoS International Symposium on Frontiers of Combining Systems ITP International Conference on Interactive Theorem Proving and TABLEAUX International Conference on Analytic Tableaux and Related Methods The 46 full research papers 5 short papers and 11 system descriptions presented together with two invited talks were carefully reviewed and selected from 150 submissions The papers focus on the following topics Part I SAT SMT and QBF decision procedures and combination of theories superposition proof procedures non classical logics Part II interactive theorem proving HOL formalizations verification reasoning systems and tools The conference was held virtually due to the COVID 19 pandemic Chapter Constructive Hybrid Games is available open access under a Creative Commons Attribution 4.0 International License via link [springer.com](https://www.springer.com) *Verification of Reactive Systems* Klaus Schneider, 2013-03-09 Reactive systems are becoming more and more important for essentially all areas of technical and professional activities as well as for many areas of everyday life The design of these systems is a great challenge and requires sound compromises between safety and time to market To meet these needs early design phases nowadays include verification of given specifications against system descriptions to find

potential design errors as early as possible This book is devoted to the foundation of the most popular formal methods for the specification and verification of reactive systems In particular the calculus omega automata and temporal logics are covered in full detail their relationship and state of the art verification procedures based on these formal approaches are presented Furthermore the advantages and disadvantages of the formalisms from particular points of view are analyzed Most results are given with detailed proofs so that the presentation is almost self contained This book is targeted to advanced students lecturers and researchers in the area of formal methods      *Formal Methods and Software Engineering* Stephan Merz,Jun Pang,2014-10-15 This book constitutes the refereed proceedings of the 16th International Conference on Formal Engineering Methods ICFEM 2014 held in Luxembourg Luxembourg in November 2014 The 28 revised full papers presented were carefully reviewed and selected from 73 submissions The papers cover a wide range of topics in the area of formal methods and software engineering and are devoted to advancing the state of the art of applying formal methods in practice They focus in particular on combinations of conceptual and methodological aspects with their formal foundation and tool support

**Guide to Discrete Mathematics** Gerard O'Regan,2021-10-28 This stimulating textbook presents a broad and accessible guide to the fundamentals of discrete mathematics highlighting how the techniques may be applied to various exciting areas in computing The text is designed to motivate and inspire the reader encouraging further study in this important skill Features This book provides an introduction to the building blocks of discrete mathematics including sets relations and functions describes the basics of number theory the techniques of induction and recursion and the applications of mathematical sequences series permutations and combinations presents the essentials of algebra explains the fundamentals of automata theory matrices graph theory cryptography coding theory language theory and the concepts of computability and decidability reviews the history of logic discussing propositional and predicate logic as well as advanced topics such as the nature of theorem proving examines the field of software engineering including software reliability and dependability and describes formal methods investigates probability and statistics and presents an overview of operations research and financial mathematics      *Computer Hardware Description Languages and their Applications* D. Agnew,L. Claesen,R. Camposano,2014-05-21 Hardware description languages HDLs have established themselves as one of the principal means of designing electronic systems The interest in and usage of HDLs continues to spread rapidly driven by the increasing complexity of systems the growth of HDL driven synthesis the research on formal design methods and many other related advances This research oriented publication aims to make a strong contribution to further developments in the field The following topics are explored in depth BDD based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification VHDL HDL based design methods high level synthesis and text graphical HDLs There are short papers covering advanced design capture and recent work in high level synthesis and formal verification In addition several invited presentations on key issues discuss and summarize recent advances in real time

system design automatic verification of sequential circuits and languages for protocol specification      **Interactive**

**Theorem Proving** Lennart Beringer, Amy Felty, 2012-08-10 This book constitutes the thoroughly refereed proceedings of the Third International Conference on Interactive Theorem Proving ITP 2012 held in Princeton NJ USA in August 2012 The 21 revised full papers presented together with 4 rough diamond papers 3 invited talks and one invited tutorial were carefully reviewed and selected from 40 submissions Among the topics covered are formalization of mathematics program abstraction and logics data structures and synthesis security non termination and automata program verification theorem prover development reasoning about program execution and prover infrastructure and modeling styles



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