FUNCTION/ARCHITECTURE OPTIMIZATION AND CO-DESIGN OF EMBEDDED SYSTEMS

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Function And Architecture Optimization And Co Design Of Embedded Systems

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Function And Architecture Optimization And Co Design Of Embedded Systems:

Function/Architecture Optimization and Co-Design of Embedded Systems Bassam Tabbara,Abdallah Tabbara,Alberto L. Sangiovanni-Vincentelli,2012-12-06 Function Architecture Co Design is a new paradigm for the design and implementation of embedded systems Function Architecture Optimization and Co Design of Embedded Systems presents the authors work in developing a function architecture optimization and co design formal methodology and framework for control dominated embedded systems The approach incorporates both data flow and control optimizations performed on a suitable novel intermediate design task representation The aim is not only to enhance productivity of the designer and system developer but also to improve quality of the final synthesis outcome Function Architecture Optimization and Co Design of Embedded Systems discusses the proposed function architecture co design methodology focusing on design representation optimization validation and synthesis Throughout the text the difference between behavior specification and implementation is emphasized The current need in co design to move from synthesis based technology to compiler based technology is pointed out The authors describe and show how performing data flow and control optimizations at the high abstraction level can lead to significant size and performance improvements in both the synthesized hardware and software The work builds on bodies of research in the silicon and software compilation domains The aforementioned techniques are specialized to the embedded systems domain It is recognized that guided optimization can be applied on the internal design representation no matter what the abstraction level and need not be restricted to the final stages of software assembly code generation or hardware synthesis Function Architecture Optimization and Co Design of Embedded Systems will be of primary interest to researchers developers and professionals in the field of embedded systems design   **Embedded Systems – A Hardware-Software Co-Design Approach** Bashir I Morshed,2021-04-19 This textbook introduces the concept of embedded systems with exercises using Arduino Uno It is intended for advanced undergraduate and graduate students in computer science computer engineering and electrical engineering programs It contains a balanced discussion on both hardware and software related to embedded systems with a focus on co design aspects Embedded systems have applications in Internet of Things IoT wearables self driving cars smart devices cyberphysical systems drones and robotics The hardware chapter discusses various microcontrollers including popular microcontroller hardware examples sensors amplifiers filters actuators wired and wireless communication topologies schematic and PCB designs and much more The software chapter describes OS less programming bitmath polling interrupt timer sleep modes direct memory access shared memory mutex and smart algorithms with lots of C code examples for Arduino Uno Other topics discussed are prototyping testing verification reliability optimization and regulations Appropriate for courses on embedded systems microcontrollers and instrumentation this textbook teaches budding embedded system programmers practical skills with fun projects to prepare them for industry products Introduces embedded systems for wearables Internet of Things IoT robotics and other smart devices Offers a balanced focus on both

hardware and software co design of embedded systems Includes exercises tutorials and assignments **Hardware/Software Co-Design for Data Flow Dominated Embedded Systems** Ralf Niemann,1998-10-31 Introduces different tasks of hardware software co design including system specification hardware software partitioning co synthesis and co simulation Summarizes and classifies co design tools and methods for these tasks and presents the co design tool COOL useful for solving co design tasks for the class of data flow dominated embedded systems Primary emphasis is on hardware software partitioning and the co synthesis phase and their coupling A mathematical formulation of the hardware software partitioning problem is given and several novel approaches are presented and compared for solving the partitioning problem Annotation copyrighted by Book News Inc Portland OR **System-on-Chip** Bashir M. Al-Hashimi,2006-01-31 This book highlights both the key achievements of electronic systems design targeting SoC implementation style and the future challenges presented by the continuing scaling of CMOS technology **Analysis and Synthesis of Distributed Real-Time Embedded Systems** Paul Pop,Petru Eles,Zebo Peng,2013-03-19 Embedded computer systems are now everywhere from alarm clocks to PDAs from mobile phones to cars almost all the devices we use are controlled by embedded computers An important class of embedded computer systems is that of hard real time systems which have to fulfill strict timing requirements As real time systems become more complex they are often implemented using distributed heterogeneous architectures Analysis and Synthesis of Distributed Real Time Embedded Systems addresses the design of real time applications implemented using distributed heterogeneous architectures The systems are heterogeneous not only in terms of hardware components but also in terms of communication protocols and scheduling policies Regarding this last aspect time driven and event driven systems as well as a combination of the two are considered Such systems are used in many application areas like automotive electronics real time multimedia avionics medical equipment and factory systems The proposed analysis and synthesis techniques derive optimized implementations that fulfill the imposed design constraints An important part of the implementation process is the synthesis of the communication infrastructure which has a significant impact on the overall system performance and cost Analysis and Synthesis of Distributed Real Time Embedded Systems considers the mapping and scheduling tasks within an incremental design process To reduce the time to market of products the design of real time systems seldom starts from scratch Typically designers start from an already existing system running certain applications and the design problem is to implement new functionality on top of this system Supporting such an incremental design process provides a high degree of flexibility and can result in important reductions of design costs STRONGAnalysis and Synthesis of Distributed Real Time Embedded Systems will be of interest to advanced undergraduates graduate students researchers and designers involved in the field of embedded systems **Architecture and Design of Distributed Embedded Systems** Bernd Kleinjohann,2013-04-18 Due to the decreasing production costs of IT systems applications that had to be realised as expensive PCBs formerly can now be realised as a system on chip Furthermore low cost broadband communication media for wide area communication as well as

for the realisation of local distributed systems are available Typically the market requires IT systems that realise a set of specific features for the end user in a given environment so called embedded systems Some examples for such embedded systems are control systems in cars airplanes houses or plants information and communication devices like digital TV mobile phones or autonomous systems like service or edutainment robots For the design of embedded systems the designer has to tackle three major aspects The application itself including the man machine interface The target architecture of the system including all functional and non functional constraints and the design methodology including modelling specification synthesis test and validation The last two points are a major focus of this book This book documents the high quality approaches and results that were presented at the International Workshop on Distributed and Parallel Embedded Systems DIPES 2000 which was sponsored by the International Federation for Information Processing IFIP and organised by IFIP working groups WG10 3 WG10 4 and WG10 5 The workshop took place on October 18 19 2000 in Schlo Eringerfeld near Paderborn Germany Architecture and Design of Distributed Embedded Systems is organised similar to the workshop Chapters 1 and 4 Methodology I and II deal with different modelling and specification paradigms and the corresponding design methodologies Generic system architectures for different classes of embedded systems are presented in Chapter 2 In Chapter 3 several design environments for the support ofspecific design methodologies are presented Problems concerning test and validation are discussed in Chapter 5 The last two chapters include distribution and communication aspects Chapter 6 and synthesis techniques for embedded systems Chapter 7 This book is essential reading for computer science researchers and application developers   **Specification and Design Methodology for Real-Time Embedded Systems** Randall S. Janka,2012-12-06 Specification and design methodology has seen significant growth as a research area over the last decade tracking but lagging behind VLSI design technology in general and the CAD industry in particular The commercial rush to market tries to leverage existing technology which fuels CAD design tool development Paralleling this is very active basic and applied research to investigate and move forward rational and effective methodologies for accomplishing digital design especially in the field of hardware software codesign It is this close relationship between industry and academia that makes close cooperation between researchers and practitioners so important and monographs like this that combine both abstract concept and pragmatic implementation deftly bridge this often gaping chasm It was at the IEEE ACM Eighth International Symposium on Hardware Software Codesign where I met the author of this monograph Dr Randall Janka who was presenting some of his recent dissertation research results on specification and design methodology or as he has so succinctly defined this sometimes ambiguous concept the tools and rules Where so many codesign researchers are trying to prove out different aspects of codesign and using toy applications to do so Dr Janka had developed a complete specification and design methodology and prototyped the infrastructure and proven its viability utility and effectiveness using a demanding real world application of a real time synthetic aperture radar imaging processor that was implemented with embedded parallel

processors   **Computer Safety, Reliability, and Security** Frank Ortmeier,Peter Daniel,2012-09-07 This book constitutes the refereed proceedings of the 31st International Conference on Computer Safety Reliability and Security SAFECOMP 2012 held in Magdeburg Germany in September 2012 The 33 revised full papers presented were carefully reviewed and selected from more than 70 submissions The papers are organized in topical sections on tools risk analysis testing quantitative analysis security formal methods aeronautic automotive and process Also included are 4 case studies

Hardware-Software Co-Design of Embedded Systems F. Balarin,Paolo Giusto,Attila Jurecska,Claudio Passerone,Ellen Sentovich,Bassam Tabbara,M. Chiodo,Harry Hsieh,Luciano Lavagno,Alberto Sangiovanni-Vincentelli,Kei Suzuki,2012-12-06 Embedded systems are informally defined as a collection of programmable parts surrounded by ASICs and other standard components that interact continuously with an environment through sensors and actuators The programmable parts include micro controllers and Digital Signal Processors DSPs Embedded systems are often used in life critical situations where reliability and safety are more important criteria than performance Today embedded systems are designed with an ad hoc approach that is heavily based on earlier experience with similar products and on manual design Use of higher level languages such as C helps structure the design somewhat but with increasing complexity it is not sufficient Formal verification and automatic synthesis of implementations are the surest ways to guarantee safety Thus the POLIS system which is a co design environment for embedded systems is based on a formal model of computation POLIS was initiated in 1988 as a research project at the University of California at Berkeley and over the years grew into a full design methodology with a software system supporting it Hardware Software Co Design of Embedded Systems The POLIS Approach is intended to give a complete overview of the POLIS system including its formal and algorithmic aspects Hardware Software Co Design of Embedded Systems The POLIS Approach will be of interest to embedded system designers automotive electronics consumer electronics and telecommunications micro controller designers CAD developers and students   **Proceedings, 11th IEEE International Conference and Workshop on the Engineering of Computer-Based Systems** Vaclav Dvorak,Miroslav Sveda,2004   **Electronic System-Level HW/SW Co-Design of Heterogeneous Multi-Processor Embedded Systems** Luigi Pomante,2022-09-01 Modern electronic systems consist of a fairly heterogeneous set of components Today a single system can be constituted by a hardware platform frequently composed of a mix of analog and digital components and by several software application layers The hardware can include several heterogeneous microprocessors e g GPP DSP GPU etc dedicated ICs ASICs and or FPGAs memories a set of local connections between the system components and some interfaces between the system and the environment sensors actuators etc Therefore on the one hand multi processor embedded systems are capable of meeting the demand of processing power and flexibility of complex applications On the other hand such systems are very complex to design and optimize so that the design methodology plays a major role in determining the success of the products For these reasons to cope with the increasing system complexity the

approaches typically used today are oriented towards co design methodologies working at the higher levels of abstraction Unfortunately such methodologies are typically customized for the specific application suffer of a lack of generality and still need a considerable effort when real size project are envisioned Therefore there is still the need for a general methodology able to support the designer during the high level steps of a co design flow enabling an effective design space exploration before tackling the low level steps and thus committing to the final technology This should prevent costly redesign loops In such a context the work described in this book composed of two parts aims at providing models methodologies and tools to support each step of the co design flow of embedded systems implemented by exploiting heterogeneous multi processor architectures mapped on distributed systems as well as fully integrated onto a single chip **Handbook of Signal Processing Systems** Shuvra S. Bhattacharyya,Ed F. Deprettere,Rainer Leupers,Jarmo Takala,2013-06-20 Handbook of Signal Processing Systems is organized in three parts The first part motivates representative applications that drive and apply state of the art methods for design and implementation of signal processing systems the second part discusses architectures for implementing these applications the third part focuses on compilers and simulation tools describes models of computation and their associated design tools and methodologies This handbook is an essential tool for professionals in many fields and researchers of all levels **Readings in Hardware/Software Co-Design** Giovanni De Micheli,Rolf Ernst,Wayne Wolf,2002 This title serves as an introduction ans reference for the field with the papers that have shaped the hardware software co design since its inception in the early 90s *Time-Triggered Communication* Roman Obermaisser,2018-09-03 Time Triggered Communication helps readers build an understanding of the conceptual foundation operation and application of time triggered communication which is widely used for embedded systems in a diverse range of industries This book assembles contributions from experts that examine the differences and commonalities of the most significant protocols including TTP FlexRay TTEthernet SAFEbus TTCAN and LIN Covering the spectrum from low cost time triggered fieldbus networks to ultra reliable time triggered networks used for safety critical applications the authors illustrate the inherent benefits of time triggered communication in terms of predictability complexity management fault tolerance and analytical dependability modeling which are key aspects of safety critical systems Examples covered include FlexRay in cars TTP in railway and avionic systems and TTEthernet in aerospace applications Illustrating key concepts based on real world industrial applications this book Details the underlying concepts and principles of time triggered communication Explores the properties of a time triggered communication system contrasting its strengths and weaknesses Focuses on the core algorithms applied in many systems including those used for clock synchronization startup membership and fault isolation Describes the protocols that incorporate presented algorithms Covers tooling requirements and solutions for system integration including scheduling The information in this book is extremely useful to industry leaders who design and manufacture products with distributed embedded systems based on time triggered communication It also benefits suppliers

of embedded components or development tools used in this area As an educational tool this material can be used to teach students and working professionals in areas including embedded systems computer networks system architectures dependability real time systems and automotive avionics and industrial control systems **Embedded Software Design and Programming of Multiprocessor System-on-Chip** Katalin Popovici,Frédéric Rousseau,Ahmed A. Jerraya,Marilyn Wolf,2010-03-03 Current multimedia and telecom applications require complex heterogeneous multiprocessor system on chip MPSoC architectures with specific communication infrastructure in order to achieve the required performance Heterogeneous MPSoC includes different types of processing units DSP microcontroller ASIP and different communication schemes fast links non standard memory organization and access Programming an MPSoC requires the generation of efficient software running on MPSoC from a high level environment by using the characteristics of the architecture This task is known to be tedious and error prone because it requires a combination of high level programming environments with low level software design This book gives an overview of concepts related to embedded software design for MPSoC It details a full software design approach allowing systematic high level mapping of software applications on heterogeneous MPSoC This approach is based on gradual refinement of hardware software interfaces and simulation models allowing to validate the software at different abstraction levels This book combines Simulink for high level programming and SystemC for the low level software development This approach is illustrated with multiple examples of application software and MPSoC architectures that can be used for deep understanding of software design for MPSoC **Model-Based Design for Embedded Systems** Gabriela Nicolescu,Pieter J. Mosterman,2018-09-03 The demands of increasingly complex embedded systems and associated performance computations have resulted in the development of heterogeneous computing architectures that often integrate several types of processors analog and digital electronic components and mechanical and optical components all on a single chip As a result now the most prominent challenge for the design automation community is to efficiently plan for such heterogeneity and to fully exploit its capabilities A compilation of work from internationally renowned authors Model Based Design for Embedded Systems elaborates on related practices and addresses the main facets of heterogeneous model based design for embedded systems including the current state of the art important challenges and the latest trends Focusing on computational models as the core design artifact this book presents the cutting edge results that have helped establish model based design and continue to expand its parameters The book is organized into three sections Real Time and Performance Analysis in Heterogeneous Embedded Systems Design Tools and Methodology for Multiprocessor System on Chip and Design Tools and Methodology for Multidomain Embedded Systems The respective contributors share their considerable expertise on the automation of design refinement and how to relate properties throughout this refinement while enabling analytic and synthetic qualities They focus on multi core methodological issues real time analysis and modeling and validation taking into account how optical electronic and mechanical components often

interface Model based design is emerging as a solution to bridge the gap between the availability of computational capabilities and our inability to make full use of them yet This approach enables teams to start the design process using a high level model that is gradually refined through abstraction levels to ultimately yield a prototype When executed well model based design encourages enhanced performance and quicker time to market for a product Illustrating a broad and diverse spectrum of applications such as in the automotive aerospace health care consumer electronics this volume provides designers with practical readily adaptable modeling solutions for their own practice **Dissertation Abstracts International** ,2007 **Hardware/Software Co-Design** Jørgen Staunstrup,Wayne Wolf,2013-04-17 Introduction to Hardware Software Co Design presents a number of issues of fundamental importance for the design of integrated hardware software products such as embedded communication and multimedia systems This book is a comprehensive introduction to the fundamentals of hardware software co design Co design is still a new field but one which has substantially matured over the past few years This book written by leading international experts covers all the major topics including fundamental issues in co design hardware software co synthesis algorithms prototyping and emulation target architectures compiler techniques specification and verification system level specification Special chapters describe in detail several leading edge co design systems including Cosyma LYCOS and Cosmos Introduction to Hardware Software Co Design contains sufficient material for use by teachers and students in an advanced course of hardware software co design It also contains extensive explanation of the fundamental concepts of the subject and the necessary background to bring practitioners up to date on this increasingly important topic **High Performance Embedded Computing Handbook** David R. Martinez,Robert A. Bond,M. Michael Vai,2018-10-03 Over the past several decades applications permeated by advances in digital signal processing have undergone unprecedented growth in capabilities The editors and authors of High Performance Embedded Computing Handbook A Systems Perspective have been significant contributors to this field and the principles and techniques presented in the handbook are reinforced by examples drawn from their work The chapters cover system components found in today s HPEC systems by addressing design trade offs implementation options and techniques of the trade then solidifying the concepts with specific HPEC system examples This approach provides a more valuable learning tool Because readers learn about these subject areas through factual implementation cases drawn from the contributing authors own experiences Discussions include Key subsystems and components Computational characteristics of high performance embedded algorithms and applications Front end real time processor technologies such as analog to digital conversion application specific integrated circuits field programmable gate arrays and intellectual property based design Programmable HPEC systems technology including interconnection fabrics parallel and distributed processing performance metrics and software architecture and automatic code parallelization and optimization Examples of complex HPEC systems representative of actual prototype developments Application examples including radar communications electro optical and sonar applications

The handbook is organized around a canonical framework that helps readers navigate through the chapters and it concludes with a discussion of future trends in HPEC systems The material is covered at a level suitable for practicing engineers and HPEC computational practitioners and is easily adaptable to their own implementation requirements   Embedded Machine Learning for Cyber-Physical, IoT, and Edge Computing Sudeep Pasricha,Muhammad Shafique,2023-10-09 This book presents recent advances towards the goal of enabling efficient implementation of machine learning models on resource constrained systems covering different application domains The focus is on presenting interesting and new use cases of applying machine learning to innovative application domains exploring the efficient hardware design of efficient machine learning accelerators memory optimization techniques illustrating model compression and neural architecture search techniques for energy efficient and fast execution on resource constrained hardware platforms and understanding hardware software codesign techniques for achieving even greater energy reliability and performance benefits Discusses efficient implementation of machine learning in embedded CPS IoT and edge computing Offers comprehensive coverage of hardware design software design and hardware software co design and co optimization Describes real applications to demonstrate how embedded CPS IoT and edge applications benefit from machine learning

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