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Brock J. LaMeres



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Introduction to Digital Logic Design John Patrick Hayes, 1993 Textbook

Electrical and Computer Engineering Rajiv Kapadia, 2015-06-19 An excellent introduction to the digital world in engineering Introduction to Digital Logic Design explains the simple concepts behind digital logic design from logic gates all the way to the design of sequential machines Over the course of the eight chapters of the book students explore number systems and codes simple logic states boolean algebra working with logic equations and simplifying logic functions They also work with arithmetic in binary systems common combinational logic functions counters and sequential logic Each chapter includes practical problems that allow for immediate application of the skills and concepts All material is based on extensive class testing Simple yet rigorous Introduction to Digital Logic Design helps first semester students see the big picture in logic design and doesn't overwhelm them with extraneous details The text is suitable for first year engineering computer science and information science courses Rajiv Kapadia earned his Ph D at the University of Oklahoma Dr Kapadia is an associate professor of electrical and computer engineering and technology at Minnesota State University Mankato

Introduction to Logic Circuits & Logic Design with Verilog Brock J. LaMeres, 2019-04-10 This textbook for courses in Digital Systems Design introduces students to the

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Introduction to Logic Design Svetlana N. Yanushkevich, Vlad P. Shmerko, 2008-01-25 With an abundance of insightful examples problems and computer experiments Introduction to Logic Design provides a balanced easy to read treatment of the fundamental theory of logic functions and applications to the design of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the

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Introduction to Logic and Computer Design Alan B. Marcovitz, 2008

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Logic Design Tariq Jamil,Iftaquaruddin Mohammed,2011-09 Various books on the topic of digital logic design are available in the market today which tend to focus on presenting detailed description of the topics associated with this subject area Unfortunately because of limited time during the semester most students of digital logic design are unable to read each and every page of these books to ascertain the main objective of each topic and therefore require a more succinct and to the point approach towards learning about these fundamental concepts This book is a compilation of lectures in digital logic design resulting from teaching of this subject by the authors during the past several years and is intended to provide concise description of the topic commonly covered in a one semester course Typical questions or problems which may be asked in examinations from the students are indicated at the conclusion of each chapter to assist students with the formulation of their study plans It is hoped that this book will assist both students and practicing computer engineers in understanding of the fundamental concepts of digital logic design and can be used as a reference book throughout their professional career

Introduction to Logic and Computer Design Alan Marcovitz,2007 Introduction to Logic and Computer Design by Alan Marcovitz takes the successful formula realized in the author s previous books and makes it even better With the inclusion of several chapters on computer design Marcovitz now offers everything a fundamentals oriented logic design course might include Further this new book is supported by an ARIS site and a host of new media supplements to make both the instructor s and the student s job easier As with Marcovitz s previous books the clear presentation of concepts and well paced writing style make Introduction to Logic and Computer Design **Efficient Branch and Bound Search with Application to**

Computer-Aided Design Xinghao Chen,Michael L. Bushnell,2012-12-06 Branch and bound search has been known for a long time and has been widely used in solving a variety of problems in computer aided design CAD and many important optimization problems In many applications the classic branch and bound search methods perform duplications of computations or rely on the search decision trees which keep track of the branch and bound search processes In CAD and many other technical fields the computational cost of constructing branch and bound search decision trees in solving large scale problems is prohibitive and duplications of computations are intolerable Efficient branch and bound methods are needed to deal with today s computational challenges Efficient branch and bound methods must not duplicate computations Efficient Branch and Bound Search with Application to Computer Aided Design describes an efficient branch and bound method for logic justification which is fundamental to automatic test pattern generation ATPG redundancy identification logic synthesis minimization verification and other problems in CAD The method is called justification equivalence based on the observation that justification processes may share identical subsequent search decision sequences With justification equivalence duplication of computations is avoided in the dynamic branch and bound search process without using search decision trees Efficient Branch and Bound Search with Application to Computer Aided Design consists of two parts The first

part containing the first three chapters provides the theoretical work The second part deals with applications particularly ATPG for sequential circuits This book is particularly useful to readers who are interested in the design and test of digital circuits Digital Circuit Analysis and Design with Simulink Modeling and Introduction to CPLDs and FPGAs Steven T. Karris,2007 This book is an undergraduate level textbook presenting a thorough discussion of state of the art digital devices and circuits It is self contained AI Technology Fumio Mizoguchi,1990

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