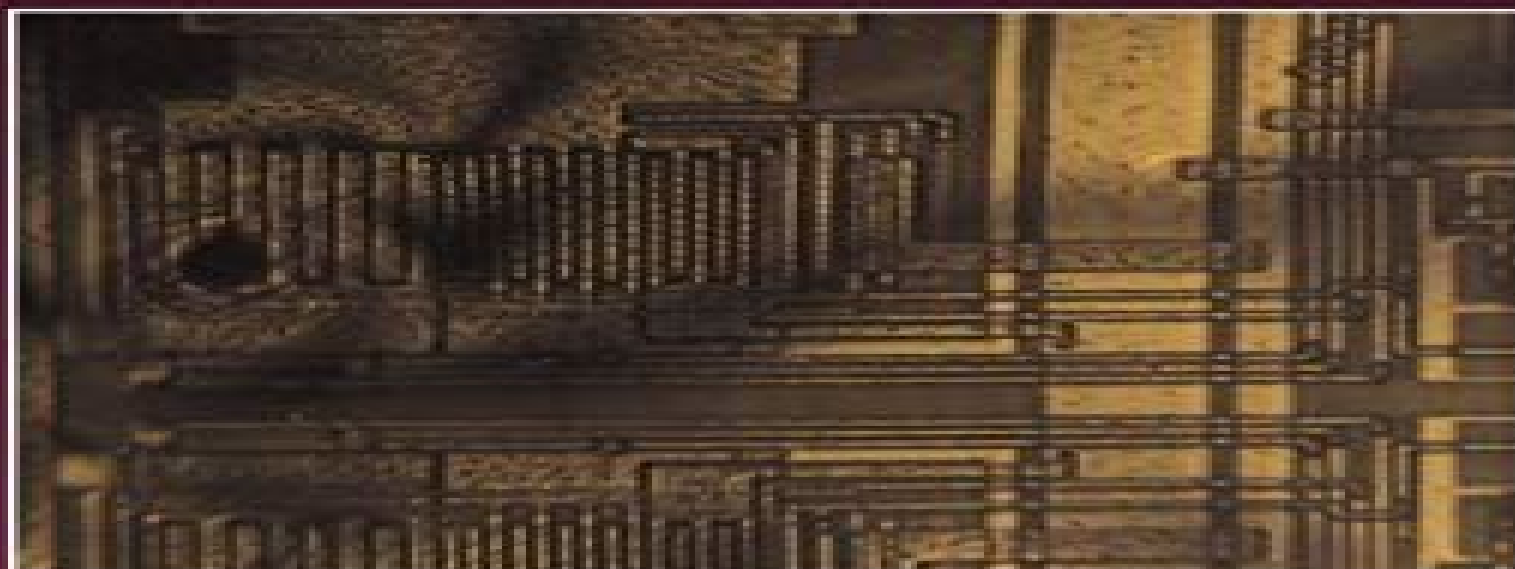


FAILURE MECHANISMS IN SEMICONDUCTOR DEVICES

Second Edition



E. Ajith Amerasekera
Farid N. Najm

Failure Mechanisms In Semiconductor Devices

SJ Ball



Failure Mechanisms In Semiconductor Devices:

Failure Mechanisms in Semiconductor Devices E. Ajith Amerasekera, Farid N. Najm, 1997-08-04 Failure Mechanisms in Semiconductor Devices Second Edition E Ajith Amerasekera Texas Instruments Inc Dallas USA Farid N Najm University of Illinois at Urbana Champaign USA Since the successful first edition of Failure Mechanisms in Semiconductor Devices semiconductor technology has become increasingly important The high complexity of today s integrated circuits has engendered a demand for greater component reliability Reflecting the need for guaranteed performance in consumer applications this thoroughly updated edition includes more detailed material on reliability modelling and prediction The book analyses the main failure mechanisms in terms of cause effects and prevention and explains the mathematics behind reliability analysis The authors detail methodologies for the identification of failures and describe the approaches for building reliability into semiconductor devices Their thorough yet accessible text covers the physics of failure mechanisms from the semiconductor die itself to the packaging and interconnections Incorporating recent advances this comprehensive survey of semiconductor reliability will be an asset to both engineers and graduate students in the field

Failure Mechanisms in Semiconductor Devices E. Ajith Amerasekera, D. S. Campbell, 1987-12-28 Thoroughly surveys the physics of failure mechanisms in semiconductor devices from the semiconductor die itself to the packaging and interconnections Its specific intention is to identify the processes leading to damage and the techniques used to repair or detect it Discusses and critiques accelerated lifetesting and how the various tests apply to different failure mechanisms Also provides a critical review of reliability modelling and estimation and techniques and quality assurance and screening techniques emphasizing the complexity of present generation integrated circuits Throughout suggestions are offered on ways to improve the quality of devices

Semiconductor Device Reliability A. Christou, B.A. Unger, 2012-12-06 This publication is a compilation of papers presented at the Semiconductor Device Reliability Workshop sponsored by the NATO International Scientific Exchange Program The Workshop was held in Crete Greece from June 4 to June 9 1989 The objective of the Workshop was to review and to further explore advances in the field of semiconductor reliability through invited paper presentations and discussions The technical emphasis was on quality assurance and reliability of optoelectronic and high speed semiconductor devices The primary support for the meeting was provided by the Scientific Affairs Division of NATO We are indebted to NATO for their support and to Dr Craig Sinclair who administers this program The chapters of this book follow the format and order of the sessions of the meeting Thirty six papers were presented and discussed during the five day Workshop In addition two panel sessions were held with audience participation where the particularly controversial topics of burn in and reliability modeling and prediction methods were discussed A brief review of these sessions is presented in this book

Failure Mechanisms and Models for Silicon Semiconductor Devices Electronic Industries Association, Joint Electron Device Engineering Council, 1996

Microelectronic Failure Analysis Desk Reference, 2001-01-01 Developed

by the Electronic Device Failure Analysis Society EDFAS Publications Committee **Defects in Organic Semiconductors and Devices** Thien-Phap Nguyen, 2023-07-27 Defects play a key role in the physical properties of semiconductors and devices and their identification is essential in assessing the reliability of electronic devices Defects in Organic Semiconductors and Devices introduces the fundamental aspects of defects in organic semiconductors and devices in relation to the structure of materials and architecture of electronic components It covers the topics of defect formation and evolution defect measurement techniques and their adaption to organic devices the effects of defects on the physical properties of materials and their effects on the performance and lifetime of organic devices Identifying defects and determining their characteristics in the structure of organic devices such as OLEDs OFETs and OPVs make it possible to better understand degradation processes and develop solutions to improve the reliability of such devices This book is intended for researchers and students in university programs or engineering schools who are specializing in electronics energy and materials **NBS Special Publication** ,1978 **Microelectronic Failure Analysis** ,2002-01-01 Provides new or expanded coverage on the latest techniques for microelectronic failure analysis The CD ROM includes the complete content of the book in fully searchable Adobe Acrobat format Developed by the Electronic Device Failure Analysis Society EDFAS Publications Committee *Microelectronics Failure Analysis* ,2004-01-01 For newcomers cast into the waters to sink or swim as well as seasoned professionals who want authoritative guidance desk side this hefty volume updates the previous 1999 edition It contains the work of expert contributors who rallied to the job in response to a committee s call for help the committee was assigned to the update by the Electron *Physics of Failure in Electronics* M. E. Goldberg, M. F. Goldberg, Joseph Vaccaro, 1963 **Catalog of National Bureau of Standards Publications, 1966-1976** United States. National Bureau of Standards. Technical Information and Publications Division, Betty L. Burris, Rebecca J. Morehouse, 1978 **ESD** Steven H. Voldman, 2015-04-24 ESD Circuits and Devices 2nd Edition provides a clear picture of layout and design of digital analog radio frequency RF and power applications for protection from electrostatic discharge ESD electrical overstress EOS and latchup phenomena from a generalist perspective and design synthesis practices providing optimum solutions in advanced technologies New features in the 2nd edition Expanded treatment of ESD and analog design of passive devices of resistors capacitors inductors and active devices of diodes bipolar junction transistors MOSFETs and FINFETs Increased focus on ESD power clamps for power rails for CMOS Bipolar and BiCMOS Co synthesizing of semiconductor chip architecture and floor planning with ESD design practices for analog and mixed signal applications Illustrates the influence of analog design practices on ESD design circuitry from integration synthesis and layout to symmetry matching inter digitation and common centroid techniques Increased emphasis on system level testing conforming to IEC 61000 4 2 and IEC 61000 4 5 Improved coverage of low capacitance ESD scaling of devices and oxide scaling challenges ESD Circuits and Devices 2nd Edition is an essential reference to ESD circuit semiconductor engineers and quality reliability analysis engineers It is also useful for

graduate and undergraduate students in electrical engineering semiconductor sciences microelectronics and IC design

Materials and Reliability Handbook for Semiconductor Optical and Electron Devices Osamu Ueda, Stephen J. Pearton, 2012-09-22 Materials and Reliability Handbook for Semiconductor Optical and Electron Devices provides comprehensive coverage of reliability procedures and approaches for electron and photonic devices These include lasers and high speed electronics used in cell phones satellites data transmission systems and displays Lifetime predictions for compound semiconductor devices are notoriously inaccurate due to the absence of standard protocols Manufacturers have relied on extrapolation back to room temperature of accelerated testing at elevated temperature This technique fails for scaled high current density devices Device failure is driven by electric field or current mechanisms or low activation energy processes that are masked by other mechanisms at high temperature The Handbook addresses reliability engineering for III V devices including materials and electrical characterization reliability testing and electronic characterization These are used to develop new simulation technologies for device operation and reliability which allow accurate prediction of reliability as well as the design specifically for improved reliability The Handbook emphasizes physical mechanisms rather than an electrical definition of reliability Accelerated aging is useful only if the failure mechanism is known The Handbook also focuses on voltage and current acceleration stress mechanisms

Catalog of National Bureau of Standards

Publications, 1966-1976 United States. National Bureau of Standards, 1978 *Reliability of Electronic Packages and Semiconductor Devices* Giulio Di Giacomo, 1997 This text looks at predicting and extending the functional life of semiconductor components Using empirical modelling the author covers major types of failure mechanisms that can greatly reduce the active life of semiconductor components including interconnection fatigue and electromigration *Failure Analysis* Marius Bazu, Titu Bajenescu, 2011-03-08 Failure analysis is the preferred method to investigate product or process reliability and to ensure optimum performance of electrical components and systems The physics of failure approach is the only internationally accepted solution for continuously improving the reliability of materials devices and processes The models have been developed from the physical and chemical phenomena that are responsible for degradation or failure of electronic components and materials and now replace popular distribution models for failure mechanisms such as Weibull or lognormal Reliability engineers need practical orientation around the complex procedures involved in failure analysis This guide acts as a tool for all advanced techniques their benefits and vital aspects of their use in a reliability programme Using twelve complex case studies the authors explain why failure analysis should be used with electronic components when implementation is appropriate and methods for its successful use Inside you will find detailed coverage on a synergistic approach to failure modes and mechanisms along with reliability physics and the failure analysis of materials emphasizing the vital importance of cooperation between a product development team involved the reasons why failure analysis is an important tool for improving yield and reliability by corrective actions the design stage highlighting the concurrent

engineering approach and DfR Design for Reliability failure analysis during fabrication covering reliability monitoring process monitors and package reliability reliability testing after fabrication including reliability assessment at this stage and corrective actions a large variety of methods such as electrical methods thermal methods optical methods electron microscopy mechanical methods X Ray methods spectroscopic acoustical and laser methods new challenges in reliability testing such as its use in microsystems and nanostructures This practical yet comprehensive reference is useful for manufacturers and engineers involved in the design fabrication and testing of electronic components devices ICs and electronic systems as well as for users of components in complex systems wanting to discover the roots of the reliability flaws for their products

Handbook of Electronic Package Design Michael Pecht, 2018-10-24 Both a handbook for practitioners and a text for use in teaching electronic packaging concepts guidelines and techniques The treatment begins with an overview of the electronics design process and proceeds to examine the levels of electronic packaging and the fundamental issues in the development

Reliability Abstracts and Technical Reviews, 1970 Scientific and Technical Aerospace Reports, 1967

Digital Avionics Handbook Cary Spitzer, Uma Ferrell, Thomas Ferrell, 2017-11-22 A perennial bestseller the Digital Avionics Handbook offers a comprehensive view of avionics Complete with case studies of avionics architectures as well as examples of modern systems flying on current military and civil aircraft this Third Edition includes Ten brand new chapters covering new topics and emerging trends Significant restructuring to deliver a more coherent and cohesive story Updates to all existing chapters to reflect the latest software and technologies Featuring discussions of new data bus and display concepts involving retina scanning speech interaction and synthetic vision the Digital Avionics Handbook Third Edition provides practicing and aspiring electrical aerospace avionics and control systems engineers with a pragmatic look at the present state of the art of avionics

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