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Gunn-effect logic devices

Hartnagel, Hans

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Gunn Effect Logic Devices

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Gunn Effect Logic Devices:

Gunn-effect Logic Devices Hans Hartnagel, 1973 **Gunn Effect Pulse and Logic Devices** Seid Hossein Izadpanah, 1969 GaAs Devices and Circuits Michael S. Shur, 2013-11-21 GaAs devices and integrated circuits have emerged as leading contenders for ultra high speed applications This book is intended to be a reference for a rapidly growing GaAs community of researchers and graduate students It was written over several years and parts of it were used for courses on GaAs devices and integrated circuits and on heterojunction GaAs devices developed and taught at the University of Minnesota Many people helped me in writing this book I would like to express my deep gratitude to Professor Lester Eastman of Cornell University whose ideas and thoughts inspired me and helped to determine the direction of my research work for many years I also benefited from numerous discussions with his students and associates and from the very atmosphere of the pursuit of excellence which exists in his group I would like to thank my former and present co workers and colleagues Drs Levinstein and Gelmont of the A F Ioffe Institute of Physics and Technology Professor Melvin Shaw of Wayne State University Dr Kastalsky of Bell Communications Professor Gary Robinson of Colorado State University Professor Tony Valois and Dr Tim Drummond of Sandia Labs for their contributions to our joint research and for valuable discussions My special thanks to Professor Morko for his help his ideas and the example set by his pioneering work Since 1978 I have been working with engineers from Honeywell Inc Drs **Semiconductor Devices and Integrated Electronics** A. G. Milnes, 2012-12-06 For some time there has been a need for a semiconductor device book that carries diode and transistor theory beyond an introductory level and yet has space to touch on a wider range of semiconductor device principles and applications Such topics are covered in specialized monographs numbering many hundreds but the voluminous nature of this literature limits access for students This book is the outcome of attempts to develop a broad course on devices and integrated electronics for university students at about senior year level The educational prerequisites are an introductory course in semiconductor junction and transistor concepts and a course on analog and digital circuits that has introduced the concepts of rectification amplification oscillators modulation and logic and Switching circuits The book should also be of value to professional engineers and physicists because of both the information included and the detailed guide to the literature given by the references The aim has been to bring some measure of order into the subject area examined and to provide a basic structure from which teachers may develop themes that are of most interest to students and themselves Semiconductor devices and integrated circuits are reviewed and fundamental factors that control power levels frequency speed size and cost are discussed The text also briefly mentions how devices are used and presents circuits and comments on representative applications Thus the book seeks a balance between the extremes of device physics and circuit design

Microelectronics, II. , 1983 **III-V Semiconductor Materials and Devices** R.J. Malik, 2012-12-02 The main emphasis of this volume is on III V semiconductor epitaxial and bulk crystal growth techniques Chapters are also included on

material characterization and ion implantation In order to put these growth techniques into perspective a thorough review of the physics and technology of III V devices is presented This is the first book of its kind to discuss the theory of the various crystal growth techniques in relation to their advantages and limitations for use in III V semiconductor devices

Compound and Josephson High-Speed Devices Takahiko Misugi,Akihiro Shibatomi,2013-06-29 In recent years III V devices integrated circuits and superconducting integrated circuits have emerged as leading contenders for high frequency and ultrahigh speed applications GaAs MESFETs have been applied in microwave systems as low noise and high power amplifiers since the early 1970s replacing silicon devices The heterojunction high electron mobility transistor HEMT invented in 1980 has become a key component for satellite broadcasting receiver systems serving as the ultra low noise device at 12 GHz Furthermore the heterojunction bipolar transistor HBT has been considered as having the highest switching speed and cutoff frequency in the semiconductor device field Initially most of these devices were used for analog high frequency applications but there is also a strong need to develop high speed III V digital devices for computer telecom munication and instrumentation systems to replace silicon high speed devices because of the switching speed and power dissipation limitations of silicon The potential high speed and low power dissipation of digital integrated circuits using GaAs MESFET HEMT HBT and superconducting Josephson junction devices has evoked tremendous competition in the race to develop such technology A technology review shows that Japanese research institutes and companies have taken the lead in the development of these devices and some integrated circuits have already been applied to supercomputers in Japan The activities of Japanese research institutes and companies in the III V and superconducting device fields have been superior for three reasons First bulk crystal growth epitaxial growth process and design technology were developed at the same time

Microelectronics, I. ,1981 Electronic Processes in Unipolar Solid-state Devices Dan Dascălu,1977 Transit-time Effects in Unipolar Solid-state Devices Dan Dascălu,1974 GaAs Microelectronics Norman G. Einspruch,William R. Wisseman,2014-12-01 VLSI Electronics Microstructure Science Volume 11 GaAs Microelectronics presents the important aspects of GaAs Gallium Arsenide IC technology development ranging from materials preparation and IC fabrication to wafer evaluation and chip packaging The volume is comprised of eleven chapters Chapter 1 traces the historical development of GaAs technology for high speed and high frequency applications This chapter summarizes the important properties of GaAs that serve to make this material and its related compounds technologically important Chapter 2 covers GaAs substrate growth ion implantation and annealing and materials characterization technologies that are essential for IC development Chapters 3 6 describe the various IC technologies that are currently under development These include microwave and digital MESFET ICs the most mature technologies and bipolar and field effect heterostructure transistor ICs The high speed capability of GaAs ICs introduces new problems on wafer testing and packaging These topics are discussed in Chapters 7 and 8 Applications for GaAs ICs are covered in Chapters 9 and 10 The first of these chapters is concerned with high speed

computer applications the second addresses military applications The book concludes with a chapter on radiation effects in GaAs ICs Scientists engineers researchers device designers and systems architects will find the book useful *Gallium Arsenide and Related Compounds* ,1981 **Official Gazette of the United States Patent Office** United States. Patent Office,1972 *Index of Patents Issued from the United States Patent Office* United States. Patent Office,1973 **The Transactions of the Institute of Electronics and Communication Engineers of Japan** Denshi Tsūshin Gakkai,1978

Official Gazette of the United States Patent and Trademark Office ,1982 **Microwave Semiconductor Devices and Circuit Applications** University of Michigan. Engineering Summer Conferences,1980 **Scientific and Technical Aerospace Reports** ,1985 *High-Speed Pulse Techniques* J. A. Coekin,2013-09-24 High Speed Pulse Techniques covers the many aspects of technique in digital electronics and encompass some of the more fundamental factors that apply to all digital systems The book describes the nature of pulse signals and their deliberate or inadvertent processing in networks transmission lines and transformers and then examines the characteristics and transient performance of semiconductor devices and integrated circuits Some of the problems associated with the assembly of these into viable systems operating at ultra high speed are also looked at The book examines the transients and waveshaping in linear circuits the steady state and transient characteristics of the diode switch and the two most useful diode waveshaping functions clipping and clamping circuits The characteristics of distributed parameter transmission lines with and without losses and their implications in digital systems are also considered The book then tackles transformer pulse response bipolar and unipolar transistor transient response and the characteristics of subnanosecond switching diodes and of high speed logic The text describes the implementation of high speed systems as well Students and practicing electronics and computer systems engineers will find the book useful **Nuclear Science Abstracts** ,1975 NSA is a comprehensive collection of international nuclear science and technology literature for the period 1948 through 1976 pre dating the prestigious INIS database which began in 1970 NSA existed as a printed product Volumes 1 33 initially created by DOE s predecessor the U S Atomic Energy Commission AEC NSA includes citations to scientific and technical reports from the AEC the U S Energy Research and Development Administration and its contractors plus other agencies and international organizations universities and industrial and research organizations References to books conference proceedings papers patents dissertations engineering drawings and journal articles from worldwide sources are also included Abstracts and full text are provided if available

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