

Field-Programmable Custom Computing Technology

Architectures, Tools, and Applications

edited by

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Kluwer Academic Publishers

Field Programmable Custom Computing Technology Architecture Tools And Applications

Dirk Koch, Frank Hannig, Daniel Ziener



Field Programmable Custom Computing Technology Architecture Tools And Applications:

Field-Programmable Custom Computing Technology: Architectures, Tools, and Applications Jeffrey Arnold, Wayne Luk, Ken Pocek, 2012-12-06 Field Programmable Custom Computing Technology Architectures Tools and Applications brings together in one place important contributions and up to date research results in this fast moving area In seven selected chapters the book describes the latest advances in architectures design methods and applications of field programmable devices for high performance reconfigurable systems The contributors to this work were selected from the leading researchers and practitioners in the field It will be valuable to anyone working or researching in the field of custom computing technology It serves as an excellent reference providing insight into some of the most challenging issues being examined today *Applied Reconfigurable Computing. Architectures, Tools, and Applications* Iouliia Skliarova, Piedad Brox Jiménez, Mário Véstias, Pedro C. Diniz, 2024-03-11 This LNCS conference volume constitutes the proceedings of the 20th International Symposium ARC 2024 in Aveiro Portugal in March 2024 The 16 full papers together with 5 papers from the technical program included in this volume were carefully reviewed and selected from 24 submissions The conference focuses on the application and development of reconfigurable computing techniques fault tolerance data and graph processing acceleration to computer security *Applied Reconfigurable Computing. Architectures, Tools, and Applications* Nikolaos Voros, Michael Huebner, Georgios Keramidas, Diana Goehringer, Christos Antonopoulos, Pedro C. Diniz, 2018-04-25 This book constitutes the proceedings of the 14th International Conference on Applied Reconfigurable Computing ARC 2018 held in Santorini Greece in May 2018 The 29 full papers and 22 short presented in this volume were carefully reviewed and selected from 78 submissions In addition the volume contains 9 contributions from research projects The papers were organized in topical sections named machine learning and neural networks FPGA based design and CGRA optimizations applications and surveys fault tolerance security and communication architectures reconfigurable and adaptive architectures design methods and fast prototyping FPGA based design and applications and special session research projects **Field Programmable Logic and Application** Jürgen Becker, Marco Platzner, Serge Vernalde, 2004-08-11 This book contains the papers presented at the 14th International Conference on Field Programmable Logic and Applications FPL held during August 30th September 1st 2004 The conference was hosted by the Interuniversity Micro Electronics Center IMEC in Leuven Belgium The FPL series of conferences was founded in 1991 at Oxford University UK and has been held annually since in Oxford 3 times Vienna Prague Darmstadt London Tallinn Glasgow Villach Belfast Montpellier and Lisbon It is the largest and oldest conference in reconfigurable computing and brings together academic researchers industry experts users and newcomers in an informal welcoming atmosphere that encourages productive exchange of ideas and knowledge between the delegates The fast and exciting advances in field programmable logic are increasing steadily with more and more application potential and need New ground has been broken in architectures design techniques partial run time reconfiguration and applications of field

programmable devices in several different areas. Many of these recent innovations are reported in this volume. The size of the FPL conferences has grown significantly over the years. FPL in 2003 saw 216 papers submitted. The interest and support for FPL in the programmable logic community continued this year with 285 scientific papers submitted, demonstrating a 32% increase when compared to the year before. The technical program was assembled from 78 selected regular papers, 45 additional short papers, and 29 posters, resulting in this volume of proceedings. The program also included three invited plenary keynote presentations from Xilinx, Gilder Technology Report, and Altera, and three embedded tutorials from Xilinx, the University of Karlsruhe, TH, and the University of Oslo.

Reconfigurable Computing: Architectures, Tools, and Applications Roger Woods, 2008-03-18. This book constitutes the refereed proceedings of the 4th International Workshop on Applied Reconfigurable Computing (ARC 2008) held in London, UK, in March 2008. The 21 full papers and 14 short papers presented together with the abstracts of 3 keynote lectures were carefully reviewed and selected from 56 submissions. The papers are organized in topical sections on programming and compilation, DNA and string processing applications, scientific applications, reconfigurable computing hardware and systems, image processing, run-time behavior, instruction set extension, as well as random number generation and financial computation.

Reconfigurable Computing: Architectures, Tools and Applications Oliver Choy, Ray Cheung, Peter Athanas, Kentaro Sano, 2012-03-02. This book constitutes the refereed proceedings of the 8th International Symposium on Reconfigurable Computing Architectures, Tools, and Applications (ARC 2012) held in Hong Kong, China, in March 2012. The 35 revised papers presented, consisting of 25 full papers and 10 poster papers, were carefully reviewed and selected from 44 submissions. The topics covered are applied RC design methods and tools, applied RC architectures, applied RC applications, and critical issues in applied RC.

Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm Reiner W. Hartenstein, Andres Keevallik, 2003-06-29. This book constitutes the refereed proceedings of the 8th International Workshop on Field Programmable Logics and Applications (FPL 98) held in Tallinn, Estonia, in August/September 1998. The 39 revised full papers presented were carefully selected for inclusion in the book from a total of 86 submissions. Also included are 30 refereed high quality posters. The papers are organized in topical sections on design methods, general aspects, prototyping and simulation, development methods, accelerators, system architectures, hardware, software, codesign, system development, algorithms on FPGAs, and applications.

Field-Programmable Logic and Applications: The Roadmap to Reconfigurable Computing Reiner W. Hartenstein, Herbert Grünbacher, 2003-06-29. This book is the proceedings volume of the 10th International Conference on Field Programmable Logic and its Applications (FPL) held August 27-30, 2000, in Villach, Austria, which covered areas like reconfigurable logic, RL, reconfigurable computing, RC, and its applications, and all other aspects. Its subtitle, *The Roadmap to Reconfigurable Computing*, reminds us that we are currently witnessing the runaway of a breakthrough. The annual FPL series is the eldest international conference in the world covering configware and all its aspects. It was founded 1991 at Oxford University, UK.

and is 2 years older than its two most important competitors usually taking place at Monterey and Napa FPL has been held at Oxford Vienna Prague Darmstadt London Tallinn and Glasgow also see <http://www.fpl.uni-kl.de> FPL The New Case for Reconfigurable Platforms Converging Media Indicated by palmtops smart mobile phones many other portables and consumer electronics media such as voice sound video TV wireless cable telephone and Internet continue to converge This creates new opportunities and even necessities for reconfigurable platform usage The new converged media require high volume flexible multi purpose multi standard low power products adaptable to support evolving standards emerging new standards field upgrades bug fixes and to meet the needs of a growing number of different kinds of services offered to zillions of individual subscribers preferring different media mixes

FPGAs for Software Programmers Dirk Koch, Frank Hannig, Daniel Ziener, 2016-06-17 This book makes powerful Field Programmable Gate Array FPGA and reconfigurable technology accessible to software engineers by covering different state of the art high level synthesis approaches e g OpenCL and several C to gates compilers It introduces FPGA technology its programming model and how various applications can be implemented on FPGAs without going through low level hardware design phases Readers will get a realistic sense for problems that are suited for FPGAs and how to implement them from a software designer s point of view The authors demonstrate that FPGAs and their programming model reflect the needs of stream processing problems much better than traditional CPU or GPU architectures making them well suited for a wide variety of systems from embedded systems performing sensor processing to large setups for Big Data number crunching This book serves as an invaluable tool for software designers and FPGA design engineers who are interested in high design productivity through behavioural synthesis domain specific compilation and FPGA overlays Introduces FPGA technology to software developers by giving an overview of FPGA programming models and design tools as well as various application examples Provides a holistic analysis of the topic and enables developers to tackle the architectural needs for Big Data processing with FPGAs Explains the reasons for the energy efficiency and performance benefits of FPGA processing Provides a user oriented approach and a sense for where and how to apply FPGA technology

ICCCE 2020 Amit Kumar, Stefan Mozar, 2020-10-11 This book is a collection of research papers and articles presented at the 3rd International Conference on Communications and Cyber Physical Engineering ICCCE 2020 held on 1 2 February 2020 at CMR Engineering College Hyderabad Telangana India Discussing the latest developments in voice and data communication engineering cyber physical systems network science communication software image and multimedia processing research and applications as well as communication technologies and other related technologies it includes contributions from both academia and industry This book is a valuable resource for scientists research scholars and PG students working to formulate their research ideas and find the future directions in these areas Further it may serve as a reference work to understand the latest engineering and technologies used by practicing engineers in the field of communication engineering

Software Defined Chips Shaojun Wei, Leibo Liu, Jianfeng Zhu, Chenchen

Deng,2022-10-20 This is the first book of a two volume book set which introduces software defined chips In this book it introduces the conceptual evolution of software defined chips from the development of integrated circuits and computing architectures Technical principles characteristics and key issues of software defined chips are systematically analyzed The hardware architecture design methods are described involving architecture design primitives hardware design spaces and agile design methods From the perspective of the compilation system the complete process from high level language to configuration contexts is introduced in detail This book is suitable for scientists and researchers in the areas of electrical and electronic engineering and computer science Postgraduate students practitioners and professionals in related areas are also potentially interested in the topic of this book **Applied Reconfigurable Computing** Christian Hochberger,Brent Nelson,Andreas Koch,Roger Woods,Pedro Diniz,2019-04-02 This book constitutes the proceedings of the 15th International Symposium on Applied Reconfigurable Computing ARC 2019 held in Darmstadt Germany in April 2019 The 20 full papers and 7 short papers presented in this volume were carefully reviewed and selected from 52 submissions In addition the volume contains 1 invited paper The papers were organized in topical sections named Applications partial reconfiguration and security image video processing high level synthesis CGRAs and vector processing architectures design frameworks and methodology convolutional neural networks **Field Programmable Logic and Applications** Wayne Luk,Peter Y.K. Cheung,Manfred Glesner,1997-08-20 This book constitutes the refereed proceedings of the 7th International Workshop on Field Programmable Logic and Applications FPL 97 held in London UK in September 1997 The 51 revised full papers in the volume were carefully selected from a large number of high quality papers The book is divided into sections on devices and architectures devices and systems reconfiguration design tools custom computing and codesign signal processing image and video processing sensors and graphics color and robotics and applications *Partial Reconfiguration on FPGAs* Dirk Koch,2012-07-25 This is the first book to focus on designing run time reconfigurable systems on FPGAs in order to gain resource and power efficiency as well as to improve speed Case studies in partial reconfiguration guide readers through the FPGA jungle straight toward a working system The discussion of partial reconfiguration is comprehensive and practical with models introduced together with methods to implement efficiently the corresponding systems Coverage includes concepts for partial module integration and corresponding communication architectures floorplanning of the on FPGA resources physical implementation aspects starting from constraining primitive placement and routing all the way down to the bitstream required to configure the FPGA and verification of reconfigurable systems **Computer System Design** Michael J. Flynn,Wayne Luk,2011-08-08 The next generation of computer system designers will be less concerned about details of processors and memories and more concerned about the elements of a system tailored to particular applications These designers will have a fundamental knowledge of processors and other elements in the system but the success of their design will depend on the skills in making system level tradeoffs that optimize the cost performance and other attributes to meet

application requirements This book provides a new treatment of computer system design particularly for System on Chip SOC which addresses the issues mentioned above It begins with a global introduction from the high level view to the lowest common denominator the chip itself then moves on to the three main building blocks of an SOC processor memory and interconnect Next is an overview of what makes SOC unique its customization ability and the applications that drive it The final chapter presents future challenges for system design and SOC possibilities

Design for Embedded Image Processing on FPGAs Donald G. Bailey, 2011-06-13 Dr Donald Bailey starts with introductory material considering the problem of embedded image processing and how some of the issues may be solved using parallel hardware solutions Field programmable gate arrays FPGAs are introduced as a technology that provides flexible fine grained hardware that can readily exploit parallelism within many image processing algorithms A brief review of FPGA programming languages provides the link between a software mindset normally associated with image processing algorithms and the hardware mindset required for efficient utilization of a parallel hardware design The design process for implementing an image processing algorithm on an FPGA is compared with that for a conventional software implementation with the key differences highlighted Particular attention is given to the techniques for mapping an algorithm onto an FPGA implementation considering timing memory bandwidth and resource constraints and efficient hardware computational techniques Extensive coverage is given of a range of low and intermediate level image processing operations discussing efficient implementations and how these may vary according to the application The techniques are illustrated with several example applications or case studies from projects or applications he has been involved with Issues such as interfacing between the FPGA and peripheral devices are covered briefly as is designing the system in such a way that it can be more readily debugged and tuned Provides a bridge between algorithms and hardware Demonstrates how to avoid many of the potential pitfalls Offers practical recommendations and solutions Illustrates several real world applications and case studies Allows those with software backgrounds to understand efficient hardware implementation Design for Embedded Image Processing on FPGAs is ideal for researchers and engineers in the vision or image processing industry who are looking at smart sensors machine vision and robotic vision as well as FPGA developers and application engineers The book can also be used by graduate students studying imaging systems computer engineering digital design circuit design or computer science It can also be used as supplementary text for courses in advanced digital design algorithm and hardware implementation and digital signal processing and applications Companion website for the book www.wiley.com/go/bailey/fpga

Encyclopedia of Software Engineering Three-Volume Set (Print) Phillip A. Laplante, 2010-11-22 Software engineering requires specialized knowledge of a broad spectrum of topics including the construction of software and the platforms applications and environments in which the software operates as well as an understanding of the people who build and use the software Offering an authoritative perspective the two volumes of the Encyclopedia of Software Engineering cover the entire

multidisciplinary scope of this important field More than 200 expert contributors and reviewers from industry and academia across 21 countries provide easy to read entries that cover software requirements design construction testing maintenance configuration management quality control and software engineering management tools and methods Editor Phillip A Laplante uses the most universally recognized definition of the areas of relevance to software engineering the Software Engineering Body of Knowledge SWEBOOK as a template for organizing the material Also available in an electronic format this encyclopedia supplies software engineering students IT professionals researchers managers and scholars with unrivaled coverage of the topics that encompass this ever changing field Also Available Online This Taylor E mail e reference taylorandfrancis com International Tel 44 0 20 7017 6062 E mail online sales tandf co uk [Euro-Par 2004 Parallel Processing](#) Marco Danelutto,Marco Vanneschi,Domenico Laforenza,2004-12-27 Euro Par Conference Series Euro Par is an annual series of international conferences dedicated to the p motion and advancement of all aspectsof parallelcomputing The major themes can be divided into the broad categories of hardware software algorithms and applications for parallel computing The objective of Euro Par is to provide a forum within which to promote the development of parallel computing both as an industrial technique and an academic discipline extending the frontier of both the state of the art and the state of the practice This is particularly portant at a time when parallel computing is undergoing strong and sustained development and experiencing real industrial take up The main audience for and participants at Euro Par are seen as researchers in academic departments government laboratories and industrial organizations Euro Par s objective is to be the primary choice of such professionals for the presentation of new sults in their speci c areas Euro Par also targets applications demonstrating the e ectiveness of parallelism This year s Euro Par conference was the tenth in the conference series The previous Euro Par conferences took place in Sto holm Lyon Passau Southampton Toulouse Munich Manchester Paderborn and Klagenfurt Next year the conference will take place in Lisbon Euro Par has a permanent Web site hosting the aims the organization structure details as well as all the conference history <http://www.europar.org> **FPGAs** Juan José Rodríguez Andina,Eduardo de la Torre Arnanz,Maria Dolores Valdés Peña,2017-07-28 Field Programmable Gate Arrays FPGAs are currently recognized as the most suitable platform for the implementation of complex digital systems targeting an increasing number of industrial electronics applications They cover a huge variety of application areas such as aerospace food industry art industrial automation automotive biomedicine process control military logistics power electronics chemistry sensor networks robotics ultrasound security and artificial vision This book first presents the basic architectures of the devices to familiarize the reader with the fundamentals of FPGAs before identifying and discussing new resources that extend the ability of the devices to solve problems in new application domains Design methodologies are discussed and application examples are included for some of these domains e g mechatronics robotics and power systems **Field-Programmable Logic and Applications** Gordon Brebner,2001-08-15 This book constitutes the refereed proceedings of the 11th International Conference on Field

Programmable Logic and Application FPL 2001 held in Belfast Northern Ireland UK in August 2001 The 56 revised full papers and 15 short papers presented were carefully reviewed and selected from a total of 117 submissions The book offers topical sections on architectural framework place and route architecture DSP synthesis encryption runtime reconfiguration graphics and vision networking processor interaction applications methodology loops and systolic image processing faults and arithmetic

Field Programmable Custom Computing Technology Architecture Tools And Applications: Bestsellers in 2023 The year 2023 has witnessed a remarkable surge in literary brilliance, with numerous captivating novels captivating the hearts of readers worldwide. Lets delve into the realm of bestselling books, exploring the engaging narratives that have enthralled audiences this year. Field Programmable Custom Computing Technology Architecture Tools And Applications : Colleen Hoover's "It Ends with Us" This poignant tale of love, loss, and resilience has captivated readers with its raw and emotional exploration of domestic abuse. Hoover expertly weaves a story of hope and healing, reminding us that even in the darkest of times, the human spirit can prevail. Field Programmable Custom Computing Technology Architecture Tools And Applications : Taylor Jenkins Reid's "The Seven Husbands of Evelyn Hugo" This captivating historical fiction novel unravels the life of Evelyn Hugo, a Hollywood icon who defies expectations and societal norms to pursue her dreams. Reid's compelling storytelling and compelling characters transport readers to a bygone era, immersing them in a world of glamour, ambition, and self-discovery. Discover the Magic : Delia Owens' "Where the Crawdads Sing" This mesmerizing coming-of-age story follows Kya Clark, a young woman who grows up alone in the marshes of North Carolina. Owens spins a tale of resilience, survival, and the transformative power of nature, entrancing readers with its evocative prose and mesmerizing setting. These popular novels represent just a fraction of the literary treasures that have emerged in 2023. Whether you seek tales of romance, adventure, or personal growth, the world of literature offers an abundance of captivating stories waiting to be discovered. The novel begins with Richard Papen, a bright but troubled young man, arriving at Hampden College. Richard is immediately drawn to the group of students who call themselves the Classics Club. The club is led by Henry Winter, a brilliant and charismatic young man. Henry is obsessed with Greek mythology and philosophy, and he quickly draws Richard into his world. The other members of the Classics Club are equally as fascinating. Bunny Corcoran is a wealthy and spoiled young man who is always looking for a good time. Charles Tavis is a quiet and reserved young man who is deeply in love with Henry. Camilla Macaulay is a beautiful and intelligent young woman who is drawn to the power and danger of the Classics Club. The students are all deeply in love with Morrow, and they are willing to do anything to please him. Morrow is a complex and mysterious figure, and he seems to be manipulating the students for his own purposes. As the students become more involved with Morrow, they begin to commit increasingly dangerous acts. The Secret History is a masterful and gripping novel that will keep you wondering until the very end. The novel is a warning tale about the dangers of obsession and the power of evil.

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