

# **Logic Ic Master Reference**

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## **Logic Ic Master Reference:**

Logic IC Master Reference David Heiserman, 2012-12-02 Logic IC Master Reference summarizes the identification codes functions pinouts and package styles for a broad range of digital IC devices The main body of this book is divided into three sections Section I 54 7400 TTL and CMOS devices Section II 4000 series CMOS devices Section III ECL devices The selections regarding functions are generally limited to small and medium scale devices logic functions buffers decoders multiplexers counters and shift registers A few large scale digital devices and devices provided by two or more major IC manufacturers are included The two indexes are organized by device identification code and by device function This reference text will prove useful to digital design engineers and technicians

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