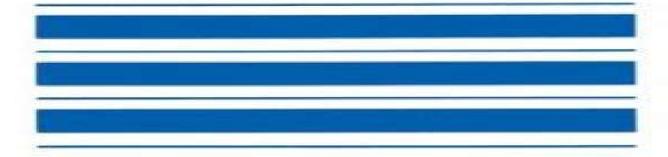
Logic Minimization Algorithms for VLSI Synthesis

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Laurence Pierre, Thomas Kropf

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Logic Minimization Algorithms for VLSI Synthesis Robert K. Brayton, Gary D. Hachtel, C. McMullen, Alberto L. Sangiovanni-Vincentelli, 2012-12-06 The roots of the project which culminates with the writing of this book can be traced to the work on logic synthesis started in 1979 at the IBM Watson Research Center and at University of California Berkeley During the preliminary phases of these projects the importance of logic minimization for the synthesis of area and performance effective circuits clearly emerged In 1980 Richard Newton stirred our interest by pointing out new heuristic algorithms for two level logic minimization and the potential for improving upon existing approaches In the summer of 1981 the authors organized and participated in a seminar on logic manipulation at IBM Research One of the goals of the seminar was to study the literature on logic minimization and to look at heuristic algorithms from a fundamental and comparative point of view The fruits of this investigation were surprisingly abundant it was apparent from an initial implementation of recursive logic minimization ESPRESSO I that if we merged our new results into a two level minimization program an important step forward in automatic logic synthesis could result ESPRESSO II was born and an APL implemen tation was created in the summer of 1982 The results of preliminary tests on a fairly large set of industrial examples were good enough to justify the publication of our algorithms It is hoped that the strength and speed of our minimizer warrant its Italian name which denotes both express delivery and a specially brewed black coffee Logic Minimization Algorithms for VLSI Synthesis Robert K Brayton, Gary D Hachtel, C McMullen, 1984-08-31 Logic Minimization Algorithms for VLSI synthesis .1985 EDA for IC Implementation, Circuit Design, and Process Technology Luciano Lavagno, Louis Scheffer, Grant Martin, 2018-10-03 Presenting a comprehensive overview of the design automation algorithms tools and methodologies used to design integrated circuits the Electronic Design Automation for Integrated Circuits Handbook is available in two volumes The second volume EDA for IC Implementation Circuit Design and Process Technology thoroughly examines real time logic to GDSII a file format used to transfer data of semiconductor physical layout analog mixed signal design physical verification and technology CAD TCAD Chapters contributed by leading experts authoritatively discuss design for manufacturability at the nanoscale power supply network design and analysis design modeling and much more Save on the complete set

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each chapter to revise the concepts Recent trends and references are listed at the end of each chapter for further reading Speech Time-Frequency Representations Michael D. Riley, 2012-12-06 1 1 Steps in the initial auditory processing 4 2 THE TIME FREQUENCY ENERGY REPRESENTATION 2 1 Short time spectrum of a steady state Iii 9 2 2 Smoothed short time spectra 9 2 3 Short time spectra of linear chirps 13 2 4 Short time spectra of w s 15 2 5 Wide band spectrograms of w s 16 Spectrograms of rapid formant motion 2 6 17 2 7 Wigner distribution and spectrogram 21 2 8 Wigner distribution and spectrogram of cos wot 23 2 9 Concentration ellipses for transform kernels 28 2 10 Concentration ellipses for complementary kernels 42 42 2 11 Directional transforms for a linear chirp 47 2 12 Spectrograms of wioi with different window sizes 2 13 Wigner distribution of wioi 49 2 14 Time frequency autocorrelation function of wioi 49 2 15 Gaussian transform of Iwioi 50 2 16 Directional transforms of lwioi 52 3 TIME FREQUENCY FILTERING 3 1 Recovering the transfer function by filtering 57 3 2 Estimating aliased transfer function 61 3 3 T F autocorrelation function of an impulse train 70 3 4 T F autocorrelation function of LTI filter output 70 Windowing recovers transfer function 3 5 72 3 6 Shearing the time frequency autocorrelation function 75 3 7 T F autocorrelation function for FM filter 76 3 8 T F autocorrelation function of FM filter output 77 3 9 Windowing recovers transfer function 79 4 THE SCHEMATIC SPECTROGRAM Problems with pole fitting approach **Systolic Array Optimizing Compiler** Monica S. Lam, 2012-12-06 This book is a revision of my Ph D thesis dissertation submitted to Carnegie Mellon University in 1987 It documents the research and results of the compiler technology developed for the Warp machine Warp is a systolic array built out of custom high performance processors each of which can execute up to 10 million floating point operations per second 10 MFLOPS Under the direction of H T Kung the Warp machine matured from an academic experimental prototype to a commercial product of General Electric The Warp machine demonstrated that the scalable architecture of high peiformance programmable systolic arrays represents a practical cost effective solution to the present and future computation intensive applications. The success of Warp led to the follow on iWarp project a joint project with Intel to develop a single chip 20 MFLOPS processor The availability of the highly integrated iWarp processor will have a significant impact on parallel computing One of the major challenges in the development of Warp was to build an optimizing compiler for the machine First the processors in the xx A Systolic Array Optimizing Compiler array cooperate at a fine granularity of parallelism interaction between processors must be considered in the generation of code for individual processors Second the individual processors themselves derive their performance from a VLIW Very Long Instruction Word instruction set and a high degree of internal pipelining and parallelism The compiler contains optimizations pertaining to the array level of parallelism as well as optimizations for the individual VLIW processors Applications Of Multi-objective Evolutionary Algorithms Carlos A Coello Coello, Gary B Lamont, 2004-12-08 This book presents an extensive variety of multi objective problems across diverse disciplines along with statistical solutions using multi objective evolutionary algorithms MOEAs The topics discussed serve to promote a wider understanding as well as the use of MOEAs the aim being to find good

solutions for high dimensional real world design applications. The book contains a large collection of MOEA applications from many researchers and thus provides the practitioner with detailed algorithmic direction to achieve good results in their selected problem domain ASIC System Design with VHDL: A Paradigm Steven S. Leung, Michael A. Shanblatt, 2012-12-06 Beginning in the mid 1980 s VLSI technology had begun to advance in two directions Pushing the limit of integration ULSI Ultra Large Scale Integration represents the frontier of the semiconductor processing technology in the campaign to conquer the submicron realm The application of ULSI however is at present largely confined in the area of memory designs and as such its impact on traditional microprocessor based system design is modest If advancement in this direction is merely a natural extrapolation from the previous integration generations then the rise of ASIC Application Specific Integrated Circuit is an unequivocal signal that a directional change in the discipline of system design is in effect In contrast to ULSI ASIC employs only well proven technology and hence is usually at least one generation behind the most advanced processing technology In spite of this apparent disadvantage ASIC has become the mainstream of VLSI design and the technology base of numerous entrepreneurial opportunities ranging from PC clones to supercomputers Unlike ULSI whose complexity can be hidden inside a memory chip or a standard component and thus can be accommodated by traditional system design methods ASIC requires system designers to master a much larger body of knowledge spanning from processing technology and circuit techniques to architecture principles and algorithm characteristics Integrating knowledge in these various areas has become the precondition for integrating devices and functions into an ASIC chip in a Subband Image Coding John W. Woods, 2013-06-29 This book market oriented environment But knowledge is of two kinds concerns a new method of image data compression which weil may supplant the well established block transforn methods that have been state of the art for the last 15 years Subband image coding or SBC was first perfonned as such in 1985 and as the results became known at first through conference proceedings and later through journal papers the research community became excited about both the theoretical and practical aspects of this new approach This excitement is continuing today with many major research laboratories and research universities around the world investigating the subband approach to coding of color images high resolution images video including video conferencing and advanced tele vision and the medical application of picture archiving systems Much of the fruits of this work is summarized in the eight chapters of this book which were written by leading practitioners in this field The subband approach to image coding starts by passing the image through a two or three dimensional filter bank The two dimensional 2 D case usually is hierarchical consisting of two stages of four filters each Thus the original image is split into 16 subband images with each one decimated or subsampled by 4x4 resulting in a data conservation The individual channel data is then quantized for digital transmission In an attractive variation an octave like approach herein tenned subband pyramid is taken for the decomposition resulting in a total of just eleven subbands The Best of ICCAD Andreas Kuehlmann, 2012-12-06 In 2002 the International Conference on Computer

Aided Design ICCAD celebrates its 20th anniversary This book commemorates contributions made by ICCAD to the broad field of design automation during that time The foundation of ICCAD in 1982 coincided with the growth of Large Scale Integration The sharply increased functionality of board level circuits led to a major demand for more powerful Electronic Design Automation EDA tools At the same time LSI grew quickly and advanced circuit integration became widely avail able This in turn required new tools using sophisticated modeling analysis and optimization algorithms in order to manage the evermore complex design processes Not surprisingly during the same period a number of start up com panies began to commercialize EDA solutions complementing various existing in house efforts The overall increased interest in Design Automation DA re guired a new forum for the emerging community of EDA professionals one which would be focused on the publication of high quality research results and provide a structure for the exchange of ideas on a broad scale Many of the original ICCAD volunteers were also members of CANDE Computer Aided Network Design a workshop of the IEEE Circuits and Sys tem Society In fact it was at a CANDE workshop that Bill McCalla suggested the creation of a conference for the EDA professional Bill later developed the name Gallium Arsenide Digital Circuits Omar Wing, 2012-12-06 Gallium Arsenide technology has come of age GaAs integrated circuits are available today as gate arrays with an operating speed in excess of one Gigabits per second Special purpose GaAs circuits are used in optical fiber digital communications systems for the purpose of regeneration multiplexing and switching of the optical signals As advances in fabrication and packaging techniques are made the operating speed will further increase and the cost of production will reach a point where large scale application of GaAs circuits will be economical in these and other systems where speed is paramount This book is written for students and engineers who wish to enter into this new field of electronics for the first time and who wish to embark on a serious study of the subject of GaAs circuit design No prior knowledge of GaAs technology is assumed though some previous experience with MOS circuit design will be helpful A good part of the book is devoted to circuit analysis to the extent that is possible for non linear circuits The circuit model of the GaAs transistor is derived from first principles and analytic formulas useful in predicting the approxi mate circuit performance are also derived Computer simulation is used throughout the book to show the expected performance and to study the effects of parameter variations Hardware Design and Verification Methods Laurence Pierre, Thomas Kropf, 2003-07-31 CHARME 99 is the tenth in a series of working conferences devoted to the dev opment and use of leading edge formal techniques and tools for the design and veri cation of hardware and systems Previous conferences have been held in Darmstadt 1984 Edinburgh 1985 Grenoble 1986 Glasgow 1988 Leuven 1989 Torino 1991 Arles 1993 Frankfurt 1995 and Montreal 1997 This workshop and conference series has been organized in cooperation with IFIP WG 10 5 It is now the biannual counterpart of FMCAD which takes place every even numbered year in the USA The 1999 event took place in Bad Her nalb a resort village located in the Black Forest close to the city of Karlsruhe The validation of functional and timing behavior is a major bottleneck in current VLSI design

systems A predominantly academic area of study until a few years ago formal design and veri cation techniques are now migrating into industrial use The aim of CHARME 99 is to bring together researchers and users from academia and industry working in this active area of research Two invited talks illustrate major current trends the presentation by G erard Berry Ecole des Mines de Paris Sophia Antipolis France is concerned with the use of synchronous languages in circuit design and the talk given by Peter Jansen BMW Munich Germany demonstrates an application of formal methods in an industrial environment The program also includes 20 regular presentations and 12 short presentations poster exhibitions that have been selected from the 48 submitted papers Computer Aided Systems Theory -- EUROCAST 2011 Roberto Moreno Díaz, Franz Pichler, Alexis Quesada Arencibia, 2012-02-02 The two volume proceedings LNCS 6927 and LNCS 6928 constitute the papers presented at the 13th International Conference on Computer Aided Systems Theory EUROCAST 2011 held in February 2011 in Las Palmas de Gran Canaria Spain The total of 160 papers presented were carefully reviewed and selected for inclusion in the books The contributions are organized in topical sections on concepts and formal tools software applications computation and simulation in modelling biological systems intelligent information processing heurist problem solving computer aided systems optimization model based system design simulation and verification computer vision and image processing modelling and control of mechatronic systems biomimetic software systems computer based methods for clinical and academic medicine modeling and design of complex digital systems mobile and autonomous transportation systems traffic behaviour modelling and optimization mobile computing platforms and technologies and engineering systems Decision Diagram Techniques for Micro- and Nanoelectronic Design Handbook Svetlana N. applications Yanushkevich, D. Michael Miller, Vlad P. Shmerko, Radomir S. Stankovic, 2018-10-03 Decision diagram DD techniques are very popular in the electronic design automation EDA of integrated circuits and for good reason They can accurately simulate logic design can show where to make reductions in complexity and can be easily modified to model different scenarios Presenting DD techniques from an applied perspective Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook provides a comprehensive up to date collection of DD techniques Experts with more than forty years of combined experience in both industrial and academic settings demonstrate how to apply the techniques to full advantage with more than 400 examples and illustrations Beginning with the fundamental theory data structures and logic underlying DD techniques they explore a breadth of topics from arithmetic and word level representations to spectral techniques and event driven analysis The book also includes abundant references to more detailed information and additional applications Decision Diagram Techniques for Micro and Nanoelectronic Design Handbook collects the theory methods and practical knowledge necessary to design more advanced circuits and places it at your fingertips in a single concise reference Representations of Discrete Functions Tsutomu Sasao, Masahira Fujita, 2012-12-06 Representations of Discrete Functions is an edited volume containing 13 chapter contributions from leading researchers with a focus on the latest research results. The first three

chapters are introductions and contain many illustrations to clarify concepts presented in the text It is recommended that these chapters are read first The book then deals with the following topics binary decision diagrams BDDs multi terminal binary decision diagrams MTBDDs edge valued binary decision diagrams EVBDDs functional decision diagrams FDDs Kronecker decision diagrams KDDs binary moment diagrams BMDs spectral transform decision diagrams STDDs ternary decision diagrams TDDs spectral transformation of logic functions other transformations oflogic functions EXOR based two level expressions FPRM minimization with TDDs and MTBDDs complexity theories on FDDs multi level logic synthesis and complexity of three level logic networks Representations of Discrete Functions is designed for CAD researchers and engineers and will also be of interest to computer scientists who are interested in combinatorial problems Exercises prepared by the editors help make this book useful as a graduate level textbook Computer Aided Systems Theory - EUROCAST '91 Franz Pichler, Roberto Moreno Diaz, 1992-04-08 This volume contains a selection of papers presented at the second European workshop EUROCAST 91 held in Krems Austria in April 1991 It gives an overview of the current state of Computer Aided Systems Theory research and its relation to CAD applications in the engineering fields CAST research requires the application of the most advanced information processing technology in software and hardware for the implementation of CAST method base systems Engineers in the field of information and control engineering have the opportunity in CAST to present the state of the art in modeling tools to computer scientists EUROCAST 91 proved that CAST research is still in an early state of development The papers in the volume are organized into sections on systems theory and CAST methodology modeling environments CAST method base systems and artificial vision and information and control systems Programming Gisele Pappa, Mario Giacobini, Zdenek Vasicek, 2023-03-28 This book constitutes the refereed proceedings of the 26th European Conference on Genetic Programming EuroGP 2023 held as part of EvoStar 2023 in Brno Czech Republic during April 12 14 2023 and co located with the EvoStar events EvoCOP EvoMUSART and EvoApplications The 14 revised full papers and 8 short papers presented in this book were carefully reviewed and selected from 38 submissions The wide range of topics in this volume reflects the current state of research in the field The collection of papers cover topics including developing new variants of GP algorithms for both optimization and machine learning problems as well as exploring GP to address complex real world problems The Electrical Engineering Handbook, Second Edition Richard C. Dorf, 1997-09-26 In 1993 the first edition of The Electrical Engineering Handbook set a new standard for breadth and depth of coverage in an engineering reference work Now this classic has been substantially revised and updated to include the latest information on all the important topics in electrical engineering today Every electrical engineer should have an opportunity to expand his expertise with this definitive guide In a single volume this handbook provides a complete reference to answer the guestions encountered by practicing engineers in industry government or academia This well organized book is divided into 12 major sections that encompass the entire field of electrical engineering including circuits signal processing electronics

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