



Image Processing System Architectures

A.J. Tallón-Ballesteros



Image Processing System Architectures:

Image Processing System Architectures Josef Kittler, Michael Duff, 1985-05 A team of experts survey the various approaches to the problem of reconfiguring computing structures for efficient image processing including single instruction multiple data architectures serial architectures multiple instruction multiple data architectures and special architectures Treats issues involved in the development and use of VLSI modules in image processing systems **Morphological Image Processing: Architecture and VLSI design** P.P. Jonker, 2012-12-06 Summary Based on the experiences of past designs and the outcome of recent studies in the comparisons of low level image processing architectures a pipelined system for real time low image processing has been designed and realized in CMOS technology To minimize design pitfalls a study was performed to the details of the design solutions that have been found in embodiments of the three main architectural groups of image processing the Square Processor Arrays the Linear Processor Arrays and the Pipelines This is reflected in a theoretical model As the design is based on bitplane wise processing of images research was performed on the principles of Cellular Logic Processing of two dimensional images of binary A methodology has been developed that is based on the transformation images using sets of Hit or Miss masks This method appeared to be extendable to higher dimensional images A theoretical model for the generation of break point conditions in high dimensional images has been developed and applied up to dimension three *Algorithm-Architecture Matching for Signal and Image Processing* Guy Gogniat, Dragomir Milojevic, Adam Morawiec, Ahmet Erdogan, 2010-10-20 Advances in signal and image processing together with increasing computing power are bringing mobile technology closer to applications in a variety of domains like automotive health telecommunication multimedia entertainment and many others The development of these leading applications involving a large diversity of algorithms e.g. signal image video 3D communication cryptography is classically divided into three consecutive steps a theoretical study of the algorithms a study of the target architecture and finally the implementation Such a linear design flow is reaching its limits due to intense pressure on design cycle and strict performance constraints The approach called Algorithm Architecture Matching aims to leverage design flows with a simultaneous study of both algorithmic and architectural issues taking into account multiple design constraints as well as algorithm and architecture optimizations that couldn't be achieved otherwise if considered separately Introducing new design methodologies is mandatory when facing the new emerging applications as for example advanced mobile communication or graphics using sub micron manufacturing technologies or 3D Integrated Circuits This diversity forms a driving force for the future evolutions of embedded system designs methodologies The main expectations from system designers point of view are related to methods tools and architectures supporting application complexity and design cycle reduction Advanced optimizations are essential to meet design constraints and to enable a wide acceptance of these new technologies Algorithm Architecture Matching for Signal and Image Processing presents a collection of selected contributions from both industry and academia addressing

different aspects of Algorithm Architecture Matching approach ranging from sensors to architectures design The scope of this book reflects the diversity of potential algorithms including signal communication image video 3D Graphics implemented onto various architectures from FPGA to multiprocessor systems Several synthesis and resource management techniques leveraging design optimizations are also described and applied to numerous algorithms Algorithm Architecture Matching for Signal and Image Processing should be on each designer's and EDA tool developer's shelf as well as on those with an interest in digital system design optimizations dealing with advanced algorithms 18th International Conference on Architecture of Computing Systems, ARCS 2005 Paul Lukowicz, 2005 Software Engineering for Image Processing Systems Philip A. Laplante, 2003-07-28 Software Engineering for Image Processing Systems creates a modern engineering framework for the specification design coding testing and maintenance of image processing software and systems The text is designed to benefit not only software engineers but also workers with backgrounds in mathematics the physical sciences and other engineering **Advances in Computer Systems Architecture** Thambipillai Srikanthan, Jingling Xue, 2005-10-13 This book constitutes the refereed proceedings of the 10th Asia Pacific Computer Systems Architecture Conference ACSAC 2005 held in Singapore in October 2005 The 65 revised full papers presented were carefully reviewed and selected from 173 submissions The papers are organized in topical sections on energy efficient and power aware techniques methodologies and architectures for application specific systems processor architectures and microarchitectures high reliability and fault tolerant architectures compiler and OS for emerging architectures data value predictions reconfigurable computing systems and polymorphic architectures interconnect networks and network interfaces parallel architectures and computation models hardware software partitioning verification and testing of complex architectures architectures for secured computing simulation and performance evaluation architectures for emerging technologies and applications and memory systems hierarchy and management *Smart Imaging Systems* Bahram Javidi, 2001 This book presents recent advances in image sensing and processing systems image recognition 3D imaging and processing ultrafast optical networks for image communication and multidimensional information security systems Eleven chapters by international experts provide practical and theoretical insights Useful for students researchers and technology users in IT image processing and optics **Digital Image Processing Methods** Edward R. Dougherty, 2020-08-27 This unique reference presents in depth coverage of the latest methods and applications of digital image processing describing various computer architectures ideal for satisfying specific image processing demands The Selected IRS Systems Architecture United States. Internal Revenue Service, 1991 **Architecture-Aware Optimization Strategies in Real-time Image Processing** Chao Li, Souleymane Balla-Arabe, Fan Yang, 2017-10-30 In the field of image processing many applications require real time execution particularly those in the domains of medicine robotics and transmission to name but a few Recent technological developments have allowed for the integration of more complex algorithms with large data volume into embedded systems in turn producing a series of new

sophisticated electronic architectures at affordable prices This book performs an in depth survey on this topic It is primarily written for those who are familiar with the basics of image processing and want to implement the target processing design using different electronic platforms for computing acceleration The authors present techniques and approaches step by step through illustrative examples This book is also suitable for electronics embedded systems engineers who want to consider image processing applications as sufficient imaging algorithm details are given to facilitate their understanding Medical Imaging and Image-Guided Interventions Ronnie Sebro,2019-07-24 Medical Imaging and Image Guided Interventions is a collection of reviewed and relevant research chapters offering a comprehensive overview of recent developments in this field of study This publication aims at providing a thorough overview of the latest research efforts and opens new possible research paths for further novel developments

Introduction to Computer-based Imaging Systems Divyendu Sinha,Edward R. Dougherty,1998 The authors have designed a tutorial text to provide scientists with a technical understanding of computer based imaging systems and how these systems interact with digital image processing algorithms Contents include Boolean logic image processing image compression basic computer architecture advanced architectures image processors operating systems error detection and correction local area networks object oriented design paradigms and software engineering Contains numerous figures and case studies Annotation copyrighted by Book News Inc Portland OR

Food Process Monitoring Systems A.C. Pinder,G. Godfrey,2012-12-06 The manufacture of foods and beverages is a highly competitive interna tionalindustry and the range ofproducts is becomingincreasingly diverse Manufacturers are required to produce quality foods with the highest possible efficiency and lowest possible cost and international legislation is imposingstrict controls on food safety Process control is the essential link between quality safety and cost Radical changes in the technology of manufacturing bring with them new requirements for monitoring and ultimately controlling increasingly complex parameters The aim of this book is to review the latest developments in monitoring systems particu larly those suitable for the rapid sensing of composition structure or microbial status The emphasis is on up and coming methods that have been proven in the laboratory or in other industrial environments and offer potential in the food sector As such it is hoped that this book will increase the general awareness ofwhat new systems have to offer and will act as a catalyst in the technology transfer process The book features chapters on automated machine vision fluorescence cytometry infrared spectroscopy light scattering spectroscopy ultra sound mass spectrometry and chemical and biological sensors In all cases the basic approach is to describe the underlying principles and then to consider the implementation of a particular technique Examples are given of the practical application to specific problems in the food industry Proceedings of CECNet 2021 A.J. Tallón-Ballesteros,2022-01-19 It is almost impossible to imagine life today without the electronics communications and networks we have all come to take for granted The 6G network is currently under development and some chips able to operate at the Terahertz THz scale have already been introduced so the next decade will probably see the consolidation of 6G

based technology as well as many compliant devices This book presents the proceedings of the 11th International Conference on Electronics Communications and Networks CECNet 2021 initially planned to be held from 18-21 November 2021 in Beijing China but ultimately held as an online event due to ongoing COVID-19 restrictions The CECNet series is now an established annual event attracting participants in the interrelated fields of electronics computers communications and wireless communications engineering and technology from around the world Careful review by program committee members who took into consideration the breadth and depth of those research topics that fall within the scope of CECNet resulted in the selection of the 88 papers presented here from the 325 submissions received This represents an acceptance rate of around 27% Providing an overview of current research and developments in these rapidly evolving fields the book will be of interest to all those working with digital communications networks

Application-Specific Hardware Architecture Design with VHDL Bogdan Belean, 2017-10-17 This book guides readers through the design of hardware architectures using VHDL for digital communication and image processing applications that require performance computing Further it includes the description of all the VHDL related notions such as language levels of abstraction combinational vs sequential logic structural and behavioral description digital circuit design and finite state machines It also includes numerous examples to make the concepts presented in text more easily understandable

Image Processing Using FPGAs Donald Bailey, 2019-06-11 This book presents a selection of papers representing current research on using field programmable gate arrays FPGAs for realising image processing algorithms These papers are reprints of papers selected for a Special Issue of the Journal of Imaging on image processing using FPGAs A diverse range of topics is covered including parallel soft processors memory management image filters segmentation clustering image analysis and image compression Applications include traffic sign recognition for autonomous driving cell detection for histopathology and video compression Collectively they represent the current state of the art on image processing using FPGAs

Scientific and Technical Aerospace Reports, 1994

Applied Reconfigurable Computing. Architectures, Tools, and Applications Lin Gan, Yu Wang, Wei Xue, Thomas Chau, 2022-10-27 This book constitutes the proceedings of the 18th International Symposium on Applied Reconfigurable Computing ARC 2022 held as a virtual event in September 2022 The 13 full papers presented in this volume were reviewed and selected from 16 submissions The papers cover a broad spectrum of applications of reconfigurable computing from driving assistance data and graph processing acceleration computer security to the societal relevant topic of supporting early diagnosis of Covid infectious conditions

Special Computer Architectures for Pattern Processing King-Sun Fu, 2018-05-04 It has been recognized for a long time that a conventional sequential processor is inefficient for operations on pictorial data where relatively simple operations need to be performed on a large number of data elements pixels Though many parallel processing architectures for picture processing have been proposed in the past very few have actually been implemented due to the costs involved With LSI technology it is becoming possible to realize parallel architectures at a

modest cost In the following the authors review some of the proposed architectures for pattern recognition and image processing

Fundamentals in Handwriting Recognition Sebastiano Impedovo, 2012-12-06 For many years researchers in the field of Handwriting Recognition were considered to be working in an area of minor importance in Pattern Recognition They had only the possibility to present the results of their research at general conferences such as the ICPR or publish their papers in journals such as some of the IEEE series or PR together with many other papers generally oriented to the more promising areas of Pattern Recognition The series of International Workshops on Frontiers in Handwriting Recognition and International Conferences on Document Analysis and Recognition together with some special issues of several journals are now fulfilling the expectations of many researchers who have been attracted to this area and are involving many academic institutions and industrial companies But in order to facilitate the introduction of young researchers into the field and give them both theoretically and practically powerful tools it is now time that some high level teaching schools in handwriting recognition be held also in order to unite the foundations of the field Therefore it was my pleasure to organize the NATO Advanced Study Institute on Fundamentals in Handwriting Recognition that had its origin in many exchanges among the most important specialists in the field during the International Workshops on Frontiers in Handwriting Recognition

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