

VHDL code for Generic RAM

```

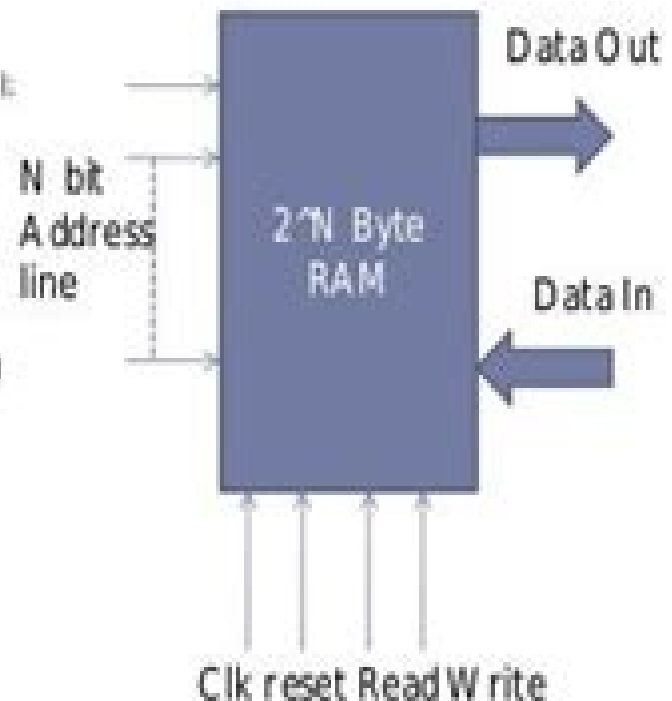
library ieee;
use ieee.std_logic_1164.all;
use ieee.numeric_std.all;
use ieee.math_real.all;
entity memory is
generic(width:positive,size:positive);
port(
    clk:in std_logic;
    rst:in std_logic;
    wr_enable:in std_logic;
    rd_enable:in std_logic;
    addr:in
        std_logic_vector(integer(log2(real(size))-1) downto 0);
    d_in:in std_logic_vector(width-1
        downto 0);
    d_out:out std_logic_vector(width-1
        downto 0);
end entity;
architecture behavioral of memory is
type memory_array is array (size-1
    downto 0) of std_logic_vector
    (width-1 downto 0);
signal mem:memory_array;

```

```

begin
process(clk,rst)
begin
    if (rst='1') then
        d_out <= (others => '0');
        mem <= (others => (others => '0'));
    elsif rising_edge(clk) then
        if (rd_enable = '1') then
            d_out <=
                mem(to_integer(unsigned(addr)));
        end if;
        if (wr_enable = '1') then
            mem(to_integer(unsigned(addr)))
                <= d_in;
        end if;
    end if;
end process;
end architecture;

```



Learning Vhdl With Warp2

RC Schank



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Introduction to Logic Design Sajjan G. Shiva, 2018-10-03 The second edition of this text provides an introduction to the analysis and design of digital circuits at a logic instead of electronics level It covers a range of topics from number system theory to asynchronous logic design A solution manual is available to instructors only Requests must be made on official school stationery

Microelectronics Education Ton J. Mouthaan, Cora Salm, 2012-12-06 Dear participant in the second European Workshop on Microelectronics Education It is a pleasure to present you the Proceedings of the Second European Workshop on Microelectronics Education and to welcome you at the Workshop The Organising Committee is very pleased that it has found several key persons with highly appreciated levels of knowledge and expertise willing to present Invited Contributions to this Workshop We have striven for an interesting spread over important areas like the expected demands for educated engineers in the wide field of Microelectronics and Microsystems in European industry and beyond and innovations in method and focus of our educational programmes This is the second European Workshop in this area the first one was held in Grenoble in France in the spring of 1996 It was the initiative of Georges Kamarinos Nadine Guillemot and Bernard Courtois to organise this Workshop because they felt that Microelectronics was at a turning point to become the core of the largest industry in the world and that this warranted a serious re consideration of our educational imperatives It is now two years since and their feeling has become reality nobody doubts that by the year 2000 the microelectronics industry will be the largest industrial sector It is also obvious that because of that and because of the predicted shortfall of educated engineers we must continuously reconsider the quality of our educational approach

VHDL Starter's Guide Sudhakar Yalamanchili, 1998 VHDL Starter's Guide has been written for the student and practitioner alike as a clear and concise tutorial on VHDL VHSIC Hardware Description Language It provides a hands on step by step introduction to learning VHDL as an applied language to be used in the design and testing of digital logic networks Command syntax and structure are emphasized and the writing is based on many examples of real world logic circuits

Programmable Logic Data Book 1997, 1997 **Electronic Design**, 1994 **ASIC & EDA**, 1993 Subject Guide to Books in Print, 2001 Dr. Dobb's Journal, 1996 *EDN, Electrical Design News*, 1998 *EDN*, 1992 *Forthcoming Books* Rose Arny, 1996-06 **VHDL for Programmable Logic** Kevin Skahill, Jay Legenhausen, 1996 This book provides a focused hands on introduction to using VHDL and programmable logic to solve design problems Whether you are a student looking for a dynamic real world introduction to an industry standard HDL or a professional engineer VHDL for Programmable Logic will be an indispensable resource

Warp2, 199? **F&S Index United States Annual**, 1996 *A Tutorial Introduction to VHDL Programming* Orhan Gazi, 2018-08-18 This book helps readers create good VHDL descriptions and simulate VHDL designs It teaches VHDL using selected sample problems which are solved step by step and with precise explanations so that readers get a clear idea of what a good VHDL code should look like The book is divided into eight chapters covering aspects ranging from the very

basics of VHDL syntax and the module concept to VHDL logic circuit implementations In the first chapter the entity and architecture parts of a VHDL program are explained in detail The second chapter explains the implementations of combinational logic circuits in VHDL language while the following chapters offer information on the simulation of VHDL programs and demonstrate how to define data types other than the standard ones available in VHDL libraries In turn the fifth chapter explains the implementation of clocked sequential logic circuits and the sixth shows the implementation of registers and counter packages The book s last two chapters detail how components functions and procedures as well as floating point numbers are implemented in VHDL The book offers extensive exercises at the end of each chapter inviting readers to learn VHDL by doing it and writing good code [Learning by Example Using VHDL](#) Richard E. Haskell,Darrin M.. Hanna,2008

VHDL: Programming by Example Douglas L. Perry,2002-06-02 Teaches VHDL by example Includes tools for simulation and synthesis CD ROM containing Code Design examples and a working demo of ModelSIM [Learning by Example Using VHDL](#) Richard E. Haskell,Darrin M. Hanna,2008 [Getting Started with FPGAs](#) Russell Merrick,2023-11-21 Skip the complexity and learn to program FPGAs the easy way through this hands on beginner friendly introduction to digital circuit design with Verilog and VHDL Whether you have been toying with field programmable gate arrays FPGAs for years or are completely new to these reprogrammable devices this book will teach you to think like an FPGA engineer and develop reliable designs with confidence Through detailed code examples patient explanations and hands on projects Getting Started with FPGAs will actually get you started Russell Merrick creator of the popular blog Nandland com will guide you through the basics of digital logic look up tables and flip flops as well as high level concepts like state machines You ll explore the fundamentals of the FPGA build process including simulation synthesis and place and route You ll learn about key FPGA primitives such as DSP blocks and PLLs and examine how FPGAs handle math operations and I O Code examples are provided in both Verilog and VHDL making the book a valuable resource no matter your language of choice You ll discover how to Implement common design building blocks like multiplexers LFSRs and FIFOs Cross between clock domains without triggering metastable conditions or timing errors Avoid common pitfalls when performing math Transmit and receive data at lightning speeds using SerDes Write testbench code to verify your designs are working With this accessible hands on guide you ll be creating your own functional FPGA projects in no time Getting started with FPGAs has never been easier **VHDL 101** William Kafig,2011-01-28 VHDL 101 is written for Electrical Engineers and others wishing to break into FPGA design and assumes a basic knowledge of digital design and some experience with engineering process Bill Kafig industry expert swiftly brings the reader up to speed on techniques and functions commonly used in VHDL VHSIC Hardware Description Language as well as commands and data types Extensive simple complete designs accompany the content for maximum comprehension The book concludes with a section on design re use which is of utmost importance to today s engineer who needs to meet a deadline and lower costs per unit Gets you up to speed with VHDL fast reducing time to market and driving

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