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**Introduction to
High-speed Circuit and
Interconnect Analysis**

Introduction To Highspeed Circuit And Interconnect Analysis

Gary K. Yeap, Farid N. Najm



Introduction To Highspeed Circuit And Interconnect Analysis:

Interconnect-Centric Design for Advanced SOC and NOC Jari Nurmi,H. Tenhunen,J. Isoaho,Axel Jantsch,2006-03-20 In Interconnect centric Design for Advanced SoC and NoC we have tried to create a comprehensive understanding about on chip interconnect characteristics design methodologies layered views on different abstraction levels and finally about applying the interconnect centric design in system on chip design Traditionally on chip communication design has been done using rather ad hoc and informal approaches that fail to meet some of the challenges posed by next generation SOC designs such as performance and throughput power and energy reliability predictability synchronization and management of concurrency To address these challenges it is critical to take a global view of the communication problem and decompose it along lines that make it more tractable We believe that a layered approach similar to that defined by the communication networks community should also be used for on chip communication design The design issues are handled on physical and circuit layer logic and architecture layer and from system design methodology and tools point of view Formal communication modeling and refinement is used to bridge the communication layers and network centric modeling of multiprocessor on chip networks and socket based design will serve the development of platforms for SoC and NoC integration Interconnect centric Design for Advanced SoC and NoC is concluded by two application examples interconnect and memory organization in SoCs for advanced set top boxes and TV and a case study in NoC platform design for more generic applications **On-Chip Inductance in High Speed Integrated Circuits** Yehea I. Ismail,Eby G. Friedman,2012-12-06 The appropriate interconnect model has changed several times over the past two decades due to the application of aggressive technology scaling New more accurate interconnect models are required to manage the changing physical characteristics of integrated circuits Currently RC models are used to analyze high resistance nets while capacitive models are used for less resistive interconnect However on chip inductance is becoming more important with integrated circuits operating at higher frequencies since the inductive impedance is proportional to the frequency The operating frequencies of integrated circuits have increased dramatically over the past decade and are expected to maintain the same rate of increase over the next decade approaching 10 GHz by the year 2012 Also wide wires are frequently encountered in important global nets such as clock distribution networks and in upper metal layers and performance requirements are pushing the introduction of new materials for low resistance interconnect such as copper interconnect already used in many commercial CMOS technologies On Chip Inductance in High Speed Integrated Circuits deals with the design and analysis of integrated circuits with a specific focus on on chip inductance effects It has been described throughout this book that inductance can have a tangible effect on current high speed integrated circuits For example neglecting inductance and using an RC interconnect model in a production 0.25 μm CMOS technology can cause large errors over 35% in estimates of the propagation delay of on chip interconnect It has also been shown that including inductance in the repeater insertion design process as compared to using

an RC model improves the overall repeater solution in terms of area power and delay with average savings of 40.8%, 15.6% and 6.7% respectively. On-Chip Inductance in High Speed Integrated Circuits is full of design and analysis techniques for RLC interconnect. These techniques are compared to techniques traditionally used for RC interconnect design to emphasize the effect of inductance. On-Chip Inductance in High Speed Integrated Circuits will be of interest to researchers in the area of high frequency interconnect noise and high performance integrated circuit design.

Modeling and Simulation of High Speed VLSI Interconnects Michel S. Nakhla, Q.J. Zhang, 2011-06-28. Modeling and Simulation of High Speed VLSI Interconnects brings together in one place important contributions and state of the art research results in this rapidly advancing area. Modeling and Simulation of High Speed VLSI Interconnects serves as an excellent reference providing insight into some of the most important issues in the field.

The VLSI Handbook Wai-Kai Chen, 2019-07-17. Over the years the fundamentals of VLSI technology have evolved to include a wide range of topics and a broad range of practices. To encompass such a vast amount of knowledge, The VLSI Handbook focuses on the key concepts, models, and equations that enable the electrical engineer to analyze, design, and predict the behavior of very large scale integrated circuits. It provides the most up-to-date information on IC technology you can find. Using frequent examples, the Handbook stresses the fundamental theory behind professional applications. Focusing not only on the traditional design methods, it contains all relevant sources of information and tools to assist you in performing your job. This includes software, databases, standards, seminars, conferences, and more. The VLSI Handbook answers all your needs in one comprehensive volume at a level that will enlighten and refresh the knowledge of experienced engineers and educate the novice. This one source reference keeps you current on new techniques and procedures and serves as a review for standard practice. It will be your first choice when looking for a solution.

Computational Methods In Large Scale Simulation Heow-pueh Lee, Khin-yong Lam, 2005-10-26. This volume documents the research carried out by visiting scientists attached to the Institute for Mathematical Sciences (IMS) at the National University of Singapore and the Institute of High Performance Computing (IHPC) under the program Advances and Mathematical Issues in Large Scale Simulation. From 2002 to 2003, researchers from various countries gathered to initiate interesting and innovative work on various themes related to multiscale simulation and fast algorithms. Today, modeling and simulation are used extensively to solve complex problems and to reduce the use of experimentation during the design and analysis stage. It is important to know the various issues that have to be considered in the successful development of computational methodologies for such work. This volume is a compilation of the research by various visiting scientists in the area of modeling and multiscale simulation. Each article covers a major project and documents how computational methodology, mathematical modeling, high performance computing, and simulation are combined in a multiscale scheme to solve a variety of complex problems. Some of these include the design, synthesis, processing, characterization, and manufacture of nanomaterials and nanostructures, new algorithms for computational work, and grid computing. Through the included

examples readers can realize the vast potential of computational modeling and large scale simulation for the solution of problems in a variety of disciplines and applications

High-Speed Interconnects in VLSI Design, Modeling, and Signal Integrity Dr. Bhaskar Gugulothu, The rapid advancement in VLSI Very Large Scale Integration technology has ushered in a new era of high performance systems where interconnects have become a critical bottleneck in determining speed power and reliability This textbook provides a comprehensive understanding of high speed interconnect design analytical and simulation based modeling techniques and signal integrity challenges in modern integrated circuits It is intended for graduate students researchers and industry professionals involved in IC design and signal integrity analysis

Electrical Design of Through Silicon Via Manho Lee, Jun So Pak, Joungho Kim, 2014-05-11 Through Silicon Via TSV is a key technology for realizing three dimensional integrated circuits 3D ICs for future high performance and low power systems with small form factors This book covers both qualitative and quantitative approaches to give insights of modeling TSV in a various viewpoints such as signal integrity power integrity and thermal integrity Most of the analysis in this book includes simulations numerical modelings and measurements for verification The author and co authors in each chapter have studied deep into TSV for many years and the accumulated technical know hows and tips for related subjects are comprehensively covered

Design Automation, Languages, and Simulations Wai-Kai Chen, 2003-03-26 As the complexity of electronic systems continues to increase the micro electronic industry depends upon automation and simulations to adapt quickly to market changes and new technologies Compiled from chapters contributed to CRC s best selling VLSI Handbook this volume of the Principles and Applications in Engineering series covers a broad rang

High-Speed VLSI Interconnections Ashok K. Goel, 2007-10-19 This Second Edition focuses on emerging topics and advances in the field of VLSI interconnections In the decade since High Speed VLSI Interconnections was first published several major developments have taken place in the field Now updated to reflect these advancements this Second Edition includes new information on copper interconnections nanotechnology circuit interconnects electromigration in the copper interconnections parasitic inductances and RLC models for comprehensive analysis of interconnection delays and crosstalk Each chapter is designed to exist independently or as a part of one coherent unit and several appropriate exercises are provided at the end of each chapter challenging the reader to gain further insight into the contents being discussed Chapter subjects include Preliminary Concepts Parasitic Resistances Capacitances and Inductances Interconnection Delays Crosstalk Analysis Electromigration Induced Failure Analysis Future Interconnections High Speed VLSI Interconnections Second Edition is an indispensable reference for high speed VLSI designers RF circuit designers and advanced students of electrical engineering

Design and Crosstalk Analysis in Carbon Nanotube Interconnects P. Uma Sathyakam, Partha Sharathi Mallick, 2020-10-31 This book provides a single source reference on carbon nanotubes for interconnect applications It presents the recent advances in modelling and challenges of carbon nanotube CNT based VLSI interconnects Starting with a background of carbon nanotubes and

interconnects this book details various aspects of CNT interconnect models the design metrics of CNT interconnects crosstalk analysis of recently proposed CNT interconnect structures and geometries Various topics covered include the use of semiconducting CNTs around metallic CNTs CNT interconnects with air gaps use of emerging ultra low k materials and their integration with CNT interconnects and geometry based crosstalk reduction techniques This book will be useful for researchers and design engineers working on carbon nanotubes for interconnects for both 2D and 3D integrated circuits

NBS Special Publication, 1968 *Computer Literature Bibliography: 1964-1967* W. W. Youden, 1965 **Performance Optimization Techniques in Analog, Mixed-Signal, and Radio-Frequency Circuit Design** Fakhfakh, Mourad, Tlelo-Cuautle, Esteban, Fino, Maria Helena, 2014-10-31 Improving the performance of existing technologies has always been a focal practice in the development of computational systems However as circuitry is becoming more complex conventional techniques are becoming outdated and new research methodologies are being implemented by designers Performance Optimization Techniques in Analog Mixed Signal and Radio Frequency Circuit Design features recent advances in the engineering of integrated systems with prominence placed on methods for maximizing the functionality of these systems This book emphasizes prospective trends in the field and is an essential reference source for researchers practitioners engineers and technology designers interested in emerging research and techniques in the performance optimization of different circuit designs *Low Power VLSI Design and Technology* Gary K. Yeap, Farid N. Najm, 1996 Low power and low energy VLSI has become an important issue in today's consumer electronics This book is a collection of pioneering applied research papers in low power VLSI design and technology A comprehensive introductory chapter presents the current status of the industry and academic research in the area of low power VLSI design and technology Other topics cover logic synthesis floorplanning circuit design and analysis from the perspective of low power requirements The readers will have a sampling of some key problems in this area as the low power solutions span the entire spectrum of the design process The book also provides excellent references on up to date research and development issues with practical solution techniques *MCM C/Mixed Technologies and Thick Film Sensors* W.K. Jones, Karel Kurzweil, Gábor Harsányi, Sylvia Mergui, 2012-12-06 Multi chip modules MCMs with high wiring density controlled impedance interconnects and thermal management capability have recently been developed to address the problems posed by advances in electronic systems that make demands for higher speeds and complexity MCM C Mixed Technologies and Thick Film Sensors highlights recent advances in MCM C technology Developments in materials and processes which have led to increased interconnection density are reviewed finer resolution thick film inks high performance low temperature dielectric tapes precision via generation by both laser and mechanical methods and enhanced screen printing technologies have given us feature resolution to the 50 µm line space level Thermal management has greatly benefitted from such new materials as cofire AlN and diamond MCM C technology is compatible with thick film sensors and work is reviewed on environmental gas sensors

pressure and temperature sensors and the development of novel materials in this area Nano Interconnects Afreen Khursheed,Kavita Khare,2021-12-23 This textbook comprehensively covers on chip interconnect dimension and application of carbon nanomaterials for modeling VLSI interconnect and buffer circuits It provides analysis of ultra low power high speed nano interconnects based on different facets such as material modeling circuit modeling and the adoption of repeater insertion strategies and measurement techniques It covers important topics including on chip interconnects interconnect modeling electrical impedance modeling of on chip interconnects modeling of repeater buffer and variability analysis Pedagogical features including solved problems and unsolved exercises are interspersed throughout the text for better understanding Aimed at senior undergraduate and graduate students in the field of electrical engineering electronics and communications engineering for courses on Advanced VLSI Interconnects Advanced VLSI Design VLSI Interconnects VLSI Design Automation and Techniques this book Provides comprehensive coverage of fundamental concepts related to nanotube transistors and interconnects Discusses properties and performance of practical nanotube devices and related applications Covers physical and electrical phenomena of carbon nanotubes as well as applications enabled by this nanotechnology Discusses the structure properties and characteristics of graphene based on chip interconnect Examines interconnect power and interconnect delay issues arising due to downscaling of device size **Compact Models and Measurement**

Techniques for High-Speed Interconnects Rohit Sharma,Tapas Chakravarty,2012-02-17 Compact Models and Measurement Techniques for High Speed Interconnects provides detailed analysis of issues related to high speed interconnects from the perspective of modeling approaches and measurement techniques Particular focus is laid on the unified approach variational method combined with the transverse transmission line technique to develop efficient compact models for planar interconnects This book will give a qualitative summary of the various reported modeling techniques and approaches and will help researchers and graduate students with deeper insights into interconnect models in particular and interconnect in general Time domain and frequency domain measurement techniques and simulation methodology are also explained in this book **Signal Propagation on Interconnects** Hartmut Grabinski,Petra Nordholz,2013-11-11 This book comprises a selection of the most representative contributions to the 1st IEEE Workshop on Signal Propagation on Interconnects that was held at Travemunde Germany in May 1997 It represents therefore a survey of the actual problems currently concerning researchers and professionals in the field of signal propagation on interconnects Signal Propagation on Interconnects contains chapters which cover a wide area of important research results dealing with simulation and measurement of noise and radiated emissions on boards describing ground bounce effects as well as inductance calculations in multilayer packages There is also reference and coverage of timing simulation techniques on chip as well as on board level Signal Propagation on Interconnects is intended to give developers and researchers in the field of chip and package design a review of the state of the art regarding the influence of interconnect effects on the electrical performance of electronic

circuits In addition the book illustrates ways to overcome performance problems related to the parasitic influences of interconnects It is an invaluable text for circuit design engineers developers and researchers in the field of signal integrity

Advances in Applied Mechanics Zitouni Azari,Khalid El Had,Mohamed ElAmine Ait Ali,Aberrahim El Mahi,Fakher Chaari,Mohamed Haddar,2023-12-18 This book reports on advances in applied mechanics research and applications It covers methods modeling and simulation tools for products design material behavior analysis and for optimizing production processes It also reports on cutting edge techniques for increasing performance of mechanical systems The book gathers selected contributions to the 11th International Congress for Applied Mechanics JET 2022 held on November 16 18 2022 in Marrakech Morocco The congress was organized by the Franco Maghreb Association of Mechanics and Materials AF3M with the close collaboration of the Moroccan Society of Mechanical Sciences SMSM Overall this book gives a special emphasis to theoretical and practical advances in mechanics supporting sustainable and environmentally friendly development It covers applications in transportation building engineering bioengineering and manufacturing Error Control for Network-on-Chip Links Bo Fu,Paul Ampadu,2011-10-09 This book provides readers with a comprehensive review of the state of the art in error control for Network on Chip NOC links Coverage includes detailed description of key issues in NOC error control faced by circuit and system designers as well as practical error control techniques to minimize the impact of these errors on system performance

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