

IEEE Standard for a Chip and Module Interconnect Bus: SBus

Sponsor

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Abstract: An input/output expansion bus with a 32- or 64-bit width is described in this standard. The SBus is designed for systems requiring a small number of expansion ports. SBus Cards may be connected to a standard SBus Connector mounted on the motherboard. SBus Devices may also be attached to the SBus directly on the system's motherboard. The dimensions of the SBus Card are 82.8 mm by 146.7 mm, making the cards appropriate for small computer systems that make extensive use of highly integrated circuits. The SBus Cards are designed to be installed in a plane parallel to the system's motherboard as mezzanine cards. They are designed to provide connections for devices external to the computer system through an exposed back panel. The form factor is useful in Futurebus+, VMEbus, desktop computers, and similar applications. The SBus has the capability of transferring data at rates up to 168 Mbytes/s, depending on the implementation options selected.

SBus Cards may either serve as Masters on the bus, providing all virtual address information as well as the data to be transferred, or they may serve as Slaves on the bus, providing data transfer according to the requirements of some other SBus Master. The SBus Master for a data transfer is selected by an arbitration process managed by the single SBus Controller on the SBus. The SBus Connector provides a virtual to physical address translation service.

Keywords: I/O bus, SBus, SBus Card, Standard for Boot Firmware

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IEEE Standards Institute of Electrical and Electronics Engineers,1994 **Digest of Papers - Compcon** ,1994
Digest of papers COMPCON Spring (39, 1994, San Francisco, Calif.),1994 **Advanced Multimicroprocessor Bus Architectures** Janusz Zalewski,1995 *Books in Print, 2005-2006* Michael Cairns,2005 **Books in Print** ,1991 *Index to IEEE Publications* Institute of Electrical and Electronics Engineers,1990 Issues for 1973 cover the entire IEEE technical literature **IEEE Std 1496-1993** , Paperbound Books in Print 1995 Reed Reference Publishing,R5ference Reed,1995-12 Electronic Design ,1993 *Subject Guide to Books in Print* ,2001 **Forthcoming Books** Rose Army,1994-02 **IEEE Standard for Boot (initialization Configuration) Firmware : Bus Supplement for IEEE 1496 (SBus)** ,1994 Firmware is the read only memory ROM based software that controls a computer between the time it is turned on and the time the primary operating system takes control of the machine Firmware s responsibilities include testing and initializing the hardware determining the hardware configuration loading or booting the operating system and providing interactive debugging facilities in case of faulty hardware or software The core requirements and practices specified by IEEE Std 1275 1994 must be supplemented by system specific requirements to form a complete specification for the firmware for a particular system This standard establishes such additional requirements pertaining to the bus architecture defined by IEEE Std 1496 1993 IEEE Standard for a Chip and Module interconnect Bus Sbus **SBus** ,2001 **ISO/IEC 15205** , ISO/IEC 15205 ,2000 IEEE Standard for Module Test and Maintenance Bus (MTM-Bus) Protocol ,1995 This standard specifies a serial backplane test and maintenance bus MTM Bus that can be used to integrate modules from different design teams or vendors into testable and maintainable subsystems Physical link and command layers are specified Standard interface protocol and commands can be used to provide the basic test and maintenance features needed for a module as well as access to on module assets memory peripherals etc and IEEE Std 1149 1 boundary scan Standard commands and functions support fault isolation to individual modules and test of backplane interconnect between modules **IEEE Standard for a High-performance Serial Bus** ,2002 Abstract Supplemental information for a high speed serial bus that integrates well with most IEEE standard 32 bit and 64 bit parallel buses is specified It is intended to extend the usefulness of a low cost interconnect between external peripherals This standard follows the IEEE Std 1212 2001 Command and Status Register CSR architecture *IEEE Standard for a High Performance Serial Bus* Institute of Electrical and Electronics Engineers,1996 *IEEE Standard for a High Performance Serial Bus* IEEE Computer Society. Microprocessor and Microcomputer Standards Committee,2000 Amended information for a high speed Serial Bus that integrates well with most IEEE standard 32 bit and 64 bit parallel buses is specified This amendment is intended to extend the usefulness of a low cost interconnect between external peripherals as described in IEEE Std 1394 1995 This amendment to IEEE Std 1394 1995 follows the ISO IEC 13213 1994 Command and Status Register CSR Architecture

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