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# Interconnection Networks

**Jose Duato, Sudhakar  
Yalamanchili, Lionel Ni**



## **Interconnection Networks:**

**Interconnection Networks** Jose Duato, Sudhakar Yalamanchili, Lionel Ni, 2002-08-06 The performance of most digital systems today is limited by their communication or interconnection not by their logic or memory As designers strive to make more efficient use of scarce interconnection bandwidth interconnection networks are emerging as a nearly universal solution to the system level communication problems for modern digital systems Interconnection networks have become pervasive in their traditional application as processor memory and processor processor interconnect Point to point interconnection networks have replaced buses in an ever widening range of applications that include on chip interconnect switches and routers and I O systems In this book the authors present in a structured way the basic underlying concepts of most interconnection networks and provide representative solutions that have been implemented in the industry or proposed in the research literature Gives a coherent comprehensive treatment of the entire field Presents a formal statement of the basic concepts alternative design choices and design trade offs Provides thorough classifications clear descriptions accurate definitions and unified views to structure the knowledge on interconnection networks Focuses on issues critical to designers

*Interconnection Networks* Jose Duato, Sudhakar Yalamanchili, Lionel Ni, 2003 Foreword Foreword to the First Printing Preface Chapter 1 Introduction Chapter 2 Message Switching Layer Chapter 3 Deadlock Livelock and Starvation Chapter 4 Routing Algorithms Chapter 5 Collective Communication Support Chapter 6 Fault Tolerant Routing Chapter 7 Network Architectures Chapter 8 Messaging Layer Software Chapter 9 Performance Evaluation Appendix A Formal Definitions for Deadlock Avoidance Appendix B Acronyms References Index

**Interconnection Networks** J.-C. Bermond, 2016-06-06 Most of the articles in this book deal with static or point to point Interconnection Networks In particular new constructions are proposed based on different tools from discrete mathematics Many new records have been established in the table of the maximum number of vertices of graphs with maximum degree and diameter D Properties of these networks and of more classical ones are analyzed in many of the other papers About 40% of the articles deal with fault tolerance or vulnerability properties using either combinatorial tools or probabilistic ones

**Principles and Practices of Interconnection Networks**

William James Dally, Brian Patrick Towles, 2004-03-06 One of the greatest challenges faced by designers of digital systems is optimizing the communication and interconnection between system components Interconnection networks offer an attractive and economical solution to this communication crisis and are fast becoming pervasive in digital systems Current trends suggest that this communication bottleneck will be even more problematic when designing future generations of machines Consequently the anatomy of an interconnection network router and science of interconnection network design will only grow in importance in the coming years This book offers a detailed and comprehensive presentation of the basic principles of interconnection network design clearly illustrating them with numerous examples chapter exercises and case studies It incorporates hardware level descriptions of concepts allowing a designer to see all the steps of the process from abstract

design to concrete implementation Case studies throughout the book draw on extensive author experience in designing interconnection networks over a period of more than twenty years providing real world examples of what works and what doesn't Tightly couples concepts with implementation costs to facilitate a deeper understanding of the tradeoffs in the design of a practical network A set of examples and exercises in every chapter help the reader to fully understand all the implications of every design decision

**Interconnection Networks for Large-scale Parallel Processing** Howard Jay Siegel, 1990 Parallel computer systems are being used to forecast the weather make maps simulate chemical reactions control air traffic guide missiles provide robots with vision and manage ballistic missile defense A major problem in designing large scale parallel systems is the construction of an interconnection network to provide interprocessor communications This book presents the theoretical basis and a number of case studies that demonstrate how this work is done This revision includes the most recent research in the field

**Interconnection Network Reliability Evaluation** Neeraj Kumar Goyal, S. Rajkumar, 2020-10-28 This book presents novel and efficient tools techniques and approaches for reliability evaluation reliability analysis and design of reliable communication networks using graph theoretic concepts In recent years human beings have become largely dependent on communication networks such as computer communication networks telecommunication networks mobile switching networks etc for their day to day activities In today's world humans and critical machines depend on these communication networks to work properly Failure of these communication networks can result in situations where people may find themselves isolated helpless and exposed to hazards It is a fact that every component or system can fail and its failure probability increases with size and complexity The main objective of this book is to devise approaches for reliability modeling and evaluation of such complex networks Such evaluation helps to understand which network can give us better reliability by their design New designs of fault tolerant interconnection network layouts are proposed which are capable of providing high reliability through path redundancy and fault tolerance through reduction of common elements in paths This book covers the reliability evaluation of various network topologies considering multiple reliability performance parameters two terminal reliability broadcast reliability all terminal reliability and multiple sources to multiple destinations reliability

**Interconnection Networks and Mapping and Scheduling Parallel Computations** Derbiau Frank Hsu, Arnold L. Rosenberg, Dominique Sotteau, 1995-01-01 This book contains the refereed proceedings of a DIMACS Workshop on Massively Parallel Computation

**Advanced Computer Architecture and Parallel Processing** Hesham El-Rewini, Mostafa Abd-El-Barr, 2005-04-08 Computer architecture deals with the physical configuration logical structure formats protocols and operational sequences for processing data controlling the configuration and controlling the operations over a computer It also encompasses word lengths instruction codes and the interrelationships among the main parts of a computer or group of computers This two volume set offers a comprehensive coverage of the field of computer organization and architecture

*Design of Interconnection Networks for Programmable Logic* Guy Lemieux, David Lewis, 2013-06-29

Programmable Logic Devices PLDs have become the key implementation medium for the vast majority of digital circuits designed today While the highest volume devices are still built with full fabrication rather than field programmability the trend towards ever fewer ASICs and more FPGAs is clear This makes the field of PLD architecture ever more important as there is stronger demand for faster smaller cheaper and lower power programmable logic PLDs are 90% routing and 10% logic This book focuses on that 90% that is the programmable routing the manner in which the programmable wires are connected and the circuit design of the programmable switches themselves Anyone seeking to understand the design of an FPGA needs to become literate in the complexities of programmable routing architecture This book builds on the state of the art of programmable interconnect by providing new methods of investigating and measuring interconnect structures as well as new programmable switch basic circuits The early portion of this book provides an excellent survey of interconnect structures and circuits as they exist today Lemieux and Lewis then provide a new way to design sparse crossbars as they are used in PLDs and show that the method works with an empirical validation This is one of a few routing architecture works that employ analytical methods to deal with the routing architecture design The analysis permits interesting insights not typically possible with the standard empirical approach

### **Fundamentals of Computer Organization and Architecture**

Mostafa Abd-El-Barr,Hesham El-Rewini,2005-02-22 This is the first book in the two volume set offering comprehensive coverage of the field of computer organization and architecture This book provides complete coverage of the subjects pertaining to introductory courses in computer organization and architecture including Instruction set architecture and design Assembly language programming Computer arithmetic Processing unit design Memory system design Input output design and organization Pipelining design techniques Reduced Instruction Set Computers RISCs The authors who share over 15 years of undergraduate and graduate level instruction in computer architecture provide real world applications examples of machines case studies and practical experiences in each chapter

### Graph Symmetry Gena Hahn,Gert

Sabidussi,2013-03-14 The last decade has seen two parallel developments one in computer science the other in mathematics both dealing with the same kind of combinatorial structures networks with strong symmetry properties or in graph theoretical language vertex transitive graphs in particular their prototypical examples Cayley graphs In the design of large interconnection networks it was realised that many of the most frequently used models for such networks are Cayley graphs of various well known groups This has spawned a considerable amount of activity in the study of the combinatorial properties of such graphs A number of symposia and congresses such as the bi annual IWIN starting in 1991 bear witness to the interest of the computer science community in this subject On the mathematical side and independently of any interest in applications progress in group theory has made it possible to make a realistic attempt at a complete description of vertex transitive graphs The classification of the finite simple groups has played an important role in this respect

### **Graph Theory and**

**Interconnection Networks** Lih-Hsing Hsu,Cheng-Kuan Lin,2008-09-26 The advancement of large scale integrated circuit

technology has enabled the construction of complex interconnection networks Graph theory provides a fundamental tool for designing and analyzing such networks Graph Theory and Interconnection Networks provides a thorough understanding of these interrelated topics After a brief introduction to gra

**Proceedings of the 1995 International Conference on Parallel Processing** Dharma P. Agrawal,1995-08-08 This set of technical books contains all the information presented at the 1995 International Conference on Parallel Processing This conference held August 14 18 featured over 100 lectures from more than 300 contributors and included three panel sessions and three keynote addresses The international authorship includes experts from around the globe from Texas to Tokyo from Leiden to London Compiled by faculty at the University of Illinois and sponsored by Penn State University these Proceedings are a comprehensive look at all that s new in the field of parallel processing

*Parallel Programming* Thomas Rauber,Gudula R Nger,2010-03-10 Innovations in hardware architecture like hyper threading or multicore processors mean that parallel computing resources are available for inexpensive desktop computers In only a few years many standard software products will be based on concepts of parallel programming implemented on such hardware and the range of applications will be much broader than that of scientific computing up to now the main application area for parallel computing Rauber and R nger take up these recent developments in processor architecture by giving detailed descriptions of parallel programming techniques that are necessary for developing efficient programs for multicore processors as well as for parallel cluster systems and supercomputers Their book is structured in three main parts covering all areas of parallel computing the architecture of parallel systems parallel programming models and environments and the implementation of efficient application algorithms The emphasis lies on parallel programming techniques needed for different architectures The main goal of the book is to present parallel programming techniques that can be used in many situations for many application areas and which enable the reader to develop correct and efficient parallel programs Many examples and exercises are provided to show how to apply the techniques The book can be used as both a textbook for students and a reference book for professionals The presented material has been used for courses in parallel programming at different universities for many years

**Distributed Computing and Networking** Shrisha Rao,2008-02-06 This book constitutes the refereed proceedings of the 9th International Conference on Distributed Computing and Networking ICDCN 2008 formerly known as IWDC International Workshop on Distributed Computing held in Kolkata India in January 2008 The 30 revised full papers and 27 revised short papers presented together with 3 keynote talks and 1 invited lecture were carefully reviewed and selected from 185 submissions The papers are organized in topical sections on agreement protocols fault tolerance and synchronization self stabilization scheduling clustering and data mining parallel architectures and algorithms mobile agents and cryptography in the distributed computing track and on sensor networks internet and security wireless networks ad hoc networks optical networks QoS and multimedia in the networking track

**Computer Architecture and Parallel Processing** Bharat

Bhushan Agarwal, Sumit Prakash Tayal, 2009      **Advances in Computers**, 1987-06-01 Advances in Computers      Design of Interconnection Networks for Programmable Logic Guy Lemieux, David Lewis, 2003-11-30 Programmable Logic Devices PLDs have become the key implementation medium for the vast majority of digital circuits designed today While the highest volume devices are still built with full fabrication rather than field programmability the trend towards ever fewer ASICs and more FPGAs is clear This makes the field of PLD architecture ever more important as there is stronger demand for faster smaller cheaper and lower power programmable logic PLDs are 90% routing and 10% logic This book focuses on that 90% that is the programmable routing the manner in which the programmable wires are connected and the circuit design of the programmable switches themselves Anyone seeking to understand the design of an FPGA needs to become literate in the complexities of programmable routing architecture This book builds on the state of the art of programmable interconnect by providing new methods of investigating and measuring interconnect structures as well as new programmable switch basic circuits The early portion of this book provides an excellent survey of interconnection structures and circuits as they exist today Lemieux and Lewis then provide a new way to design sparse crossbars as they are used in PLDs and show that the method works with an empirical validation This is one of a few routing architecture works that employ analytical methods to deal with the routing architecture design The analysis permits interesting insights not typically possible with the standard empirical approach      **Tutorial, Interconnection Networks for Parallel and Distributed Processing** Chuan-lin Wu, Tse-yun Feng, 1984      **Topological Structure and Analysis of Interconnection Networks** Junming Xu, 2013-04-17 The advent of very large scale integrated circuit technology has enabled the construction of very complex and large interconnection networks By most accounts the next generation of supercomputers will achieve its gains by increasing the number of processing elements rather than by using faster processors The most difficult technical problem in constructing a supercomputer will be the design of the interconnection network through which the processors communicate Selecting an appropriate and adequate topological structure of interconnection networks will become a critical issue on which many research efforts have been made over the past decade The book is aimed to attract the readers attention to such an important research area Graph theory is a fundamental and powerful mathematical tool for designing and analyzing interconnection networks since the topological structure of an interconnection network is a graph This fact has been universally accepted by computer scientists and engineers This book provides the most basic problems concepts and well established results on the topological structure and analysis of interconnection networks in the language of graph theory The material originates from a vast amount of literature but the theory presented is developed carefully and skillfully The treatment is generally self contained and most stated results are proved No exercises are explicitly exhibited but there are some stated results whose proofs are left to the reader to consolidate his understanding of the material

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