

INTERNATIONAL INTERCONNECT TECHNOLOGY CONFERENCE



# IITC 2024



JUNE 3-6 | SAN JOSE, CALIFORNIA



# Interconnect Technology Conference IITC 2001 International Conference

**H. Baumgart**



## **Interconnect Technology Conference Iitc 2001 International Conference:**

**Advanced Interconnects for ULSI Technology** Mikhail Baklanov, Paul S. Ho, Ehrenfried Zschech, 2012-04-02 Finding new materials for copper low k interconnects is critical to the continuing development of computer chips While copper low k interconnects have served well allowing for the creation of Ultra Large Scale Integration ULSI devices which combine over a billion transistors onto a single chip the increased resistance and RC delay at the smaller scale has become a significant factor affecting chip performance Advanced Interconnects for ULSI Technology is dedicated to the materials and methods which might be suitable replacements It covers a broad range of topics from physical principles to design fabrication characterization and application of new materials for nano interconnects and discusses Interconnect functions characterisations electrical properties and wiring requirements Low k materials fundamentals advances and mechanical properties Conductive layers and barriers Integration and reliability including mechanical reliability electromigration and electrical breakdown New approaches including 3D optical wireless interchip and carbon based interconnects Intended for postgraduate students and researchers in academia and industry this book provides a critical overview of the enabling technology at the heart of the future development of computer chips **Semiconductor Wafer Bonding** H.

Baumgart, 2002 **Circuits at the Nanoscale** Krzysztof Iniewski, 2018-10-08 Circuits for Emerging Technologies Beyond CMOS New exciting opportunities are abounding in the field of body area networks wireless communications data networking and optical imaging In response to these developments top notch international experts in industry and academia present Circuits at the Nanoscale Communications Imaging and Sensing This volume unique in both its scope and its focus addresses the state of the art in integrated circuit design in the context of emerging systems A must for anyone serious about circuit design for future technologies this book discusses emerging materials that can take system performance beyond standard CMOS These include Silicon on Insulator SOI Silicon Germanium SiGe and Indium Phosphide InP Three dimensional CMOS integration and co integration with Microelectromechanical MEMS technology and radiation sensors are described as well Topics in the book are divided into comprehensive sections on emerging design techniques mixed signal CMOS circuits circuits for communications and circuits for imaging and sensing Dr Krzysztof Iniewski is a director at CMOS Emerging Technologies Inc a consulting company in Vancouver British Columbia His current research interests are in VLSI circuits for medical applications He has published over 100 research papers in international journals and conferences and he holds 18 international patents granted in the United States Canada France Germany and Japan In this volume he has assembled the contributions of over 60 world reknown experts who are at the top of their field in the world of circuit design advancing the bank of knowledge for all who work in this exciting and burgeoning area **Handbook of 3D Integration, Volume 3** Philip Garrou, Mitsumasa Koyanagi, Peter Ramm, 2014-04-22 Edited by key figures in 3D integration and written by top authors from high tech companies and renowned research institutions this book covers the intricate details of 3D process

technology As such the main focus is on silicon via formation bonding and debonding thinning via reveal and backside processing both from a technological and a materials science perspective The last part of the book is concerned with assessing and enhancing the reliability of the 3D integrated devices which is a prerequisite for the large scale implementation of this emerging technology Invaluable reading for materials scientists semiconductor physicists and those working in the semiconductor industry as well as IT and electrical engineers *Chemical-Mechanical Polishing of Low Dielectric Constant Polymers and Organosilicate Glasses* Christopher Lyle Borst, William N. Gill, Ronald J. Gutmann, 2013-11-27

As semiconductor manufacturers implement copper conductors in advanced interconnect schemes research and development efforts shift toward the selection of an insulator that can take maximum advantage of the lower power and faster signal propagation allowed by copper interconnects One of the main challenges to integrating a low dielectric constant low kappa insulator as a replacement for silicon dioxide is the behavior of such materials during the chemical mechanical planarization CMP process used in Damascene patterning Low kappa dielectrics tend to be softer and less chemically reactive than silicon dioxide providing significant challenges to successful removal and planarization of such materials The focus of this book is to merge the complex CMP models and mechanisms that have evolved in the past decade with recent experimental results with copper and low kappa CMP to develop a comprehensive mechanism for low and high removal rate processes The result is a more in depth look into the fundamental reaction kinetics that alter selectively consume and ultimately planarize a multi material structure during Damascene patterning **Thin Film Materials, Processes, and Reliability** G. S. Mathad, 2003 **Semiconductor Technology (ISTC2001)** Ming Yang, 2001

**Handbook of Silicon Wafer Cleaning Technology** Karen Reinhardt, Werner Kern, 2018-03-16 Handbook of Silicon Wafer Cleaning Technology Third Edition provides an in depth discussion of cleaning etching and surface conditioning for semiconductor applications The fundamental physics and chemistry associated with wet and plasma processing are reviewed including surface and colloidal aspects This revised edition includes the developments of the last ten years to accommodate a continually involving industry addressing new technologies and materials such as germanium and III V compound semiconductors and reviewing the various techniques and methods for cleaning and surface conditioning Chapters include numerous examples of cleaning technique and their results The book helps the reader understand the process they are using for their cleaning application and why the selected process works For example discussion of the mechanism and physics of contamination metal particle and organic includes information on particle removal metal passivation hydrogen terminated silicon and other processes that engineers experience in their working environment In addition the handbook assists the reader in understanding analytical methods for evaluating contamination The book is arranged in an order that segments the various cleaning techniques aqueous and dry processing Sections include theory chemistry and physics first then go into detail for the various methods of cleaning specifically particle removal and metal removal amongst others Focuses on

cleaning techniques including wet plasma and other surface conditioning techniques used to manufacture integrated circuits

Reliable reference for anyone that manufactures integrated circuits or supplies the semiconductor and microelectronics industries Covers processes and equipment as well as new materials and changes required for the surface conditioning process *Semiconductor Wafer Bonding VII : Science, Technology, and Applications*, 2003 **Advanced Nanoscale ULSI Interconnects: Fundamentals and Applications** Yosi Shacham-Diamand, Tetsuya Osaka, Madhav Datta, Takayuki Ohba, 2009-09-19 In *Advanced ULSI interconnects fundamentals and applications* we bring a comprehensive description of copper based interconnect technology for ultra lar scale integration ULSI technology for integrated circuit IC application In grated circuit technology is the base for all modern electronics systems You can nd electronics systems today everywhere from toys and home appliances to a planes and space shuttles Electronics systems form the hardware that together with software are the bases of the modern information society The rapid growth and vast exploitation of modern electronics system create a strong demand for new and improved electronic circuits as demonstrated by the amazing progress in the eld of ULSI technology This progress is well described by the famous Moore s law which states in its most general form that all the metrics that describe integrated circuit performance e g speed number of devices chip area improve expon tially as a function of time For example the number of components per chip d bles every 18 months and the critical dimension on a chip has shrunk by 50% every 2 years on average in the last 30 years This rapid growth in integrated circuits te nology results in highly complex integrated circuits with an increasing number of interconnects on chips and between the chip and its package The complexity of the interconnect network on chips involves an increasing number of metal lines per interconnect level more interconnect levels and at the same time a reduction in the interconnect line critical dimensions **Handbook of Wafer Bonding** Peter Ramm, James Jian-Qiang Lu, Maaik M. V. Taklo, 2012-02-13 The focus behind this book on wafer bonding is the fast paced changes in the research and development in three dimensional 3D integration temporary bonding and micro electro mechanical systems MEMS with new functional layers Written by authors and edited by a team from microsystems companies and industry near research organizations this handbook and reference presents dependable first hand information on bonding technologies Part I sorts the wafer bonding technologies into four categories Adhesive and Anodic Bonding Direct Wafer Bonding Metal Bonding and Hybrid Metal Dielectric Bonding Part II summarizes the key wafer bonding applications developed recently that is 3D integration MEMS and temporary bonding to give readers a taste of the significant applications of wafer bonding technologies This book is aimed at materials scientists semiconductor physicists the semiconductor industry IT engineers electrical engineers and libraries Proceedings, 2001 Electromigration in Metals Paul S. Ho, Chao-Kun Hu, Martin Gall, Valeriy Sukharev, 2022-05-12 Learn to assess electromigration reliability and design more resilient chips in this comprehensive and practical resource Beginning with fundamental physics and building to advanced methodologies this book enables the reader to develop highly reliable on chip wiring stacks and power grids

Through a detailed review on the role of microstructure interfaces and processing on electromigration reliability as well as characterisation testing and analysis the book follows the development of on chip interconnects from microscale to nanoscale Practical modeling methodologies for statistical analysis from simple 1D approximation to complex 3D description can be used for step by step development of reliable on chip wiring stacks and industrial grade power ground grids This is an ideal resource for materials scientists and reliability and chip design engineers

*Chemistry in Microelectronics* Yannick Le Tiec, 2013-02-28 Microelectronics is a complex world where many sciences need to collaborate to create nano objects we need expertise in electronics microelectronics physics optics and mechanics also crossing into chemistry electrochemistry as well as biology biochemistry and medicine Chemistry is involved in many fields from materials chemicals gases liquids or salts the basics of reactions and equilibrium to the optimized cleaning of surfaces and selective etching of specific layers In addition over recent decades the size of the transistors has been drastically reduced while the functionality of circuits has increased This book consists of five chapters covering the chemicals and sequences used in processing from cleaning to etching the role and impact of their purity along with the materials used in Front End Of the Line which corresponds to the heart and performance of individual transistors then moving on to the Back End Of the Line which is related to the interconnection of all the transistors Finally the need for specific functionalization also requires key knowledge on surface treatments and chemical management to allow new applications

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About the Authors Yannick Le Tiec is a technical expert at CEA Leti Minatec since 2002 He is a CEA Leti assignee at IBM Albany NY to develop the advanced 14 nm CMOS node and the FDSOI technology He held different technical positions from the advanced 300 mm SOI CMOS pilot line to different assignments within SOITEC for advanced wafer development and later within INES to optimize solar cell ramp up and yield He has been part of the ITRS Front End technical working group at ITRS since 2008

**Handbook of Thin Film Deposition** Krishna Seshan, 2012-12-06 The Handbook of Thin Film Deposition is a comprehensive reference focusing on thin film technologies and applications used in the semiconductor industry and the closely related areas of thin film deposition thin film micro properties photovoltaic solar energy applications new materials for memory applications and methods for thin film optical processes In a major restructuring this edition of the handbook lays the foundations with an up to date treatment of lithography contamination and yield management and reliability of thin

films The established physical and chemical deposition processes and technologies are then covered the last section of the book being devoted to more recent technological developments such as microelectromechanical systems photovoltaic applications digital cameras CCD arrays and optical thin films A practical survey of thin film technologies aimed at engineers and managers involved in all stages of the process design fabrication quality assurance and applications Covers core processes and applications in the semiconductor industry and new developments in the photovoltaic and optical thin film industries The new edition takes covers the transition taking place in the semiconductor world from Al SiO<sub>2</sub> to copper interconnects with low k dielectrics Written by acknowledged industry experts from key companies in the semiconductor industry including Intel and IBM Foreword by Gordon E Moore co founder of Intel and formulator of the renowned Moore's Law relating to the technology development cycle in the semiconductor industry

*Simulation and Optimization of Digital Circuits* Vazgen Melikyan, 2018-04-12 This book describes new fuzzy logic based mathematical apparatus which enable readers to work with continuous variables while implementing whole circuit simulations with speed similar to gate level simulators and accuracy similar to circuit level simulators The author demonstrates newly developed principles of digital integrated circuit simulation and optimization that take into consideration various external and internal destabilizing factors influencing the operation of digital ICs The discussion includes factors including radiation ambient temperature electromagnetic fields and climatic conditions as well as non ideality of interconnects and power rails

**SiGe--materials, Processing, and Devices** David Louis Harame, 2004

*Design of 3D Integrated Circuits and Systems* Rohit Sharma, 2018-09-03 Three dimensional 3D integration of microsystems and subsystems has become essential to the future of semiconductor technology development 3D integration requires a greater understanding of several interconnected systems stacked over each other While this vertical growth profoundly increases the system functionality it also exponentially increases the design complexity Design of 3D Integrated Circuits and Systems tackles all aspects of 3D integration including 3D circuit and system design new processes and simulation techniques alternative communication schemes for 3D circuits and systems application of novel materials for 3D systems and the thermal challenges to restrict power dissipation and improve performance of 3D systems Containing contributions from experts in industry as well as academia this authoritative text Illustrates different 3D integration approaches such as die to die die to wafer and wafer to wafer Discusses the use of interposer technology and the role of Through Silicon Vias TSVs Presents the latest improvements in three major fields of thermal management for multiprocessor systems on chip MPSoCs Explores ThruChip Interface TCI NAND flash memory stacking and emerging applications Describes large scale integration testing and state of the art low power testing solutions Complete with experimental results of chip level 3D integration schemes tested at IBM and case studies on advanced complementary metal oxide semiconductor CMOS integration for 3D integrated circuits ICs Design of 3D Integrated Circuits and Systems is a practical reference that not only covers a wealth of design issues encountered in 3D integration but also

demonstrates their impact on the efficiency of 3D systems      **Wafer Level 3-D ICs Process Technology** Chuan Seng Tan, Ronald J. Gutmann, L. Rafael Reif, 2009-06-29 This book focuses on foundry based process technology that enables the fabrication of 3 D ICs The core of the book discusses the technology platform for pre packaging wafer lever 3 D ICs However this book does not include a detailed discussion of 3 D ICs design and 3 D packaging This is an edited book based on chapters contributed by various experts in the field of wafer level 3 D ICs process technology They are from academia research labs and industry      THz Communications Thomas Kürner, Daniel M. Mittleman, Tadao Nagatsuma, 2021-12-07 This book describes the fundamentals of THz communications spanning the whole range of applications propagation and channel models RF transceiver technology antennas baseband techniques and networking interfaces The requested data rate in wireless communications will soon reach from 100 Gbit s up to 1 Tbps necessitating systems with ultra high bandwidths of several 10s of GHz which are available only above 200 GHz In the last decade research at these frequency bands has made significant progress enabling mature experimental demonstrations of so called THz communications which are thus expected to play a vital role in future wireless networks In addition to chapters by leading experts on the theory modeling and implementation of THz communication technology the book also features the latest experimental results and addresses standardization and regulatory aspects This book will be of interest to both academic researchers and engineers in the telecommunications industry



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