Logic Synthesis for Control Automata

by

Samary Baranov

Logic Synthesis For Control Automata

Alexander Barkalov,Larysa Titarenko

Logic Synthesis For Control Automata:

  Logic Synthesis for Control Automata Samary Baranov,2012-12-06 Logic Synthesis for Control Automata provides techniques for logic design of very complex control units with hardly any constraints on their size i e the number of inputs outputs and states These techniques cover all stages of control unit design including description of control unit behavior by using operator schemes of algorithms binary decision trees and various transformations of these descriptions composition decomposition minimization etc synthesis of a control automaton finite state machine synthesis of an automaton logic circuit with matrix structure as a part of LSI or VLSI circuits as multilevel circuit with logic gates with standard LSI and VLSI circuits with and without memory Each chapter contains many examples illustrating the use of the models and methods described Moreover the special last chapter demonstrates in detail the whole design methodology presented in the previous chapters through the examples of the logic design for a control unit The models methods and algorithms described in the book can be applied to a broad class of digital system design problems including design of complex controllers robots control units of computers and for designing CAD systems of VLSI circuits using FPGA PLD and SIC technologies Logic Synthesis for Control Automata is a valuable reference for graduate students researchers and engineers involved in the design of very complex controllers VLSI circuits and CAD systems The inclusion of many examples and problems makes it most suitable for a course on the subject   **Logic Synthesis for Finite State Machines Based on Linear Chains of States** Alexander Barkalov,Larysa Titarenko,Jacek Bieganowski,2017-06-24 This book discusses Moore finite state machines FSMs implemented with field programmable gate arrays FPGAs including look up table LUT elements and embedded memory blocks EMBs To minimize the number of LUTs in FSM logic circuits the authors propose replacing a state register with a state counter They also put forward an approach allowing linear chains of states to be created which simplifies the system of input memory functions and therefore decreases the number of LUTs in the resulting FSM circuit The authors combine this approach with using EMBs to implement the system of output functions microoperations This allows a significant decrease in the number of LUTs as well as eliminating a lot of interconnections in the FSM logic circuit As a rule it also reduces the area occupied by the circuit and diminishes the resulting power dissipation This book is an interesting and valuable resource for students and postgraduates in the area of computer science as well as for designers of digital systems that included complex control units   **Design of Reconfigurable Logic Controllers** Andrei Karatkevich,Arkadiusz Bukowiec,Michał Doligalski,Jacek Tkacz,2015-12-23 This book presents the original concepts and modern techniques for specification synthesis optimisation and implementation of parallel logical control devices It deals with essential problems of reconfigurable control systems like dependability modularity and portability Reconfigurable systems require a wider variety of design and verification options than the application specific integrated circuits The book presents a comprehensive selection of possible design techniques The diversity of the modelling approaches covers Petri nets state machines and

activity diagrams The preferences of the presented optimization and synthesis methods are not limited to increasing of the efficiency of resource use One of the biggest advantages of the presented methods is the platform independence the FPGA devices and single board computers are some of the examples of possible platforms These issues and problems are illustrated with practical cases of complete control systems If you expect a new look at the reconfigurable systems designing process or need ideas for improving the quality of the project this book is a good choice g process or need ideas for improving the quality of the project this book is a good choice   Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm Reiner W. Hartenstein,Andres Keevallik,2003-06-29 This book constitutes the refereed proceedings of the 8th International Workshop on Field Programmable Logics and Applications FPL 98 held in Tallinn Estonia in August September 1998 The 39 revised full papers presented were carefully selected for inclusion in the book from a total of 86 submissions Also included are 30 refereed high quality posters The papers are organized in topical sections on design methods general aspects prototyping and simulation development methods accelerators system architectures hardware software codesign system development algorithms on FPGAs and applications   **Field Programmable Logic and Applications** Patrick Lysaght,James Irvine,Reiner Hartenstein,2004-06-22 This book contains the papers presented at the 9th International Workshop on Field ProgrammableLogic and Applications FPL 99 hosted by the University of Strathclyde in Glasgow Scotland August 30 September 1 1999 FPL 99 is the ninth in the series of annual FPL workshops The FPL 99 programme committee has been fortunate to have received a large number of high quality papers addressing a wide range of topics From these 33 papers have been selected for presentation at the workshop and a further 32 papers have been accepted for the poster sessions A total of 65 papers from 20 countries are included in this volume FPL is a subject area that attracts researchers from both electronic engine ing and computer science Whether we are engaged in research into soft ha ware or hard software seems to be primarily a question of perspective What is unquestionable is that the interaction of groups of researchers from di erent backgrounds results in stimulating and productive research As we prepare for the new millennium the premier European forum for searchers in eld programmable logic remains the FPL workshop Next year the FPL series of workshopswill celebrate its tenth anniversary The contribution of so many overseas researchers has been a particularly attractive feature of these events giving them a truly international perspective while the informal and convivial atmosphere that pervades the workshops have been their hallmark We look forward to preserving these features in the future while continuing to expand the size and quality of the events   Advanced Techniques for Embedded Systems Design and Test Juan C. López,Román Hermida,Walter Geisselhardt,2013-03-09 As electronic technology reaches the point where complex systems can be integrated on a single chip and higher degrees of performance can be achieved at lower costs designers must devise new ways to undertake the laborious task of coping with the numerous and non trivial problems that arise during the conception of such systems On the other hand shorter design cycles so that electronic products can fit into shrinking market

windows put companies and consequently designers under pressure in a race to obtain reliable products in the minimum period of time New methodologies supported by automation and abstraction have appeared which have been crucial in making it possible for system designers to take over the traditional electronic design process and embedded systems is one of the fields that these methodologies are mainly targeting The inherent complexity of these systems with hardware and software components that usually execute concurrently and the very tight cost and performance constraints make them specially suitable to introduce higher levels of abstraction and automation so as to allow the designer to better tackle the many problems that appear during their design Advanced Techniques for Embedded Systems Design and Test is a comprehensive book presenting recent developments in methodologies and tools for the specification synthesis verification and test of embedded systems characterized by the use of high level languages as a road to productivity Each specific part of the design process from specification through to test is looked at with a constant emphasis on behavioral methodologies Advanced Techniques for Embedded Systems Design and Test is essential reading for all researchers in the design and test communities as well as system designers and CAD tools developers **System-on-Chip for Real-Time Applications** Wael Badawy,Graham A. Julien,2012-12-06 System on Chip for Real Time Applications will be of interest to engineers both in industry and academia working in the area of SoC VLSI design and application It will also be useful to graduate and undergraduate students in electrical and computer engineering and computer science A selected set of papers from the 2nd International Workshop on Real Time Applications were used to form the basis of this book It is organized into the following chapters Introduction Design Reuse Modeling Architecture Design Techniques Memory Circuits Low Power Interconnect and Technology MEMS System on Chip for Real Time Applications contains many signal processing applications and will be of particular interest to those working in that community **Design of Digital Systems and Devices** Marian Adamski,Alexander Barkalov,Marek Wegrzyn,2011-02-04 Logic design of digital devices is a very important part of the Computer Science It deals with design and testing of logic circuits for both data path and control unit of a digital system Design methods depend strongly on logic elements using for implementation of logic circuits Different programmable logic devices are wide used for implementation of logic circuits Nowadays we witness the rapid growth of new and new chips but there is a strong lack of new design methods This book includes a variety of design and test methods targeted on different digital devices It covers methods of digital system design the development of theoretical base for construction and designing of the PLD based devices application of UML for digital design A considerable part of the book is devoted to design methods oriented on implementing control units using FPGA and CPLD chips Such important issues as design of reliable FSMs automatic design of concurrent logic controllers the models and methods for creating infrastructure IP services for the SoCs are also presented The editors of the book hope that it will be interesting and useful for experts in Computer Science and Electronics as well as for students who are viewed as designers of future digital devices and systems **Synthesis and**

Optimization of FPGA-Based Systems Valery Sklyarov,Iouliia Skliarova,Alexander Barkalov,Larysa Titarenko,2014-03-14 The book is composed of two parts The first part introduces the concepts of the design of digital systems using contemporary field programmable gate arrays FPGAs Various design techniques are discussed and illustrated by examples The operation and effectiveness of these techniques is demonstrated through experiments that use relatively cheap prototyping boards that are widely available The book begins with easily understandable introductory sections continues with commonly used digital circuits and then gradually extends to more advanced topics The advanced topics include novel techniques where parallelism is applied extensively These techniques involve not only core reconfigurable logical elements but also use embedded blocks such as memories and digital signal processing slices and interactions with general purpose and application specific computing systems Fully synthesizable specifications are provided in a hardware description language VHDL and are ready to be tested and incorporated in engineering designs A number of practical applications are discussed from areas such as data processing and vector based computations e g Hamming weight counters comparators The second part of the book covers the more theoretical aspects of finite state machine synthesis with the main objective of reducing basic FPGA resources minimizing delays and achieving greater optimization of circuits and systems   **Logic Synthesis for FSM-Based Control Units** Alexander Barkalov,Larysa Titarenko,2009-11-25 This book presents the hardware implementation of control algorithms represented by graph schemes of algorithm It includes new methods of logic synthesis and optimization for logic circuits of Mealy and Moore FSMs oriented on both ASIC and FPLD   **VLSI: Systems on a Chip** Luis Miguel Silveira,Srinivas Devadas,Ricardo A. Reis,2013-11-11 For over three decades now silicon capacity has steadily been doubling every year and a half with equally staggering improvements continuously being observed in operating speeds This increase in capacity has allowed for more complex systems to be built on a single silicon chip Coupled with this functionality increase speed improvements have fueled tremendous advancements in computing and have enabled new multi media applications Such trends aimed at integrating higher levels of circuit functionality are tightly related to an emphasis on compactness in consumer electronic products and a widespread growth and interest in wireless communications and products These trends are expected to persist for some time as technology and design methodologies continue to evolve and the era of Systems on a Chip has definitely come of age While technology improvements and spiraling silicon capacity allow designers to pack more functions onto a single piece of silicon they also highlight a pressing challenge for system designers to keep up with such amazing complexity To handle higher operating speeds and the constraints of portability and connectivity new circuit techniques have appeared Intensive research and progress in EDA tools design methodologies and techniques is required to empower designers with the ability to make efficient use of the potential offered by this increasing silicon capacity and complexity and to enable them to design test verify and build such systems   *The Engineering Handbook* Richard C Dorf,2018-10-03 First published in 1995 The Engineering Handbook quickly became the definitive

engineering reference Although it remains a bestseller the many advances realized in traditional engineering fields along with the emergence and rapid growth of fields such as biomedical engineering computer engineering and nanotechnology mean that the time has come to bring this standard setting reference up to date New in the Second Edition 19 completely new chapters addressing important topics in bioinstrumentation control systems nanotechnology image and signal processing electronics environmental systems structural systems 131 chapters fully revised and updated Expanded lists of engineering associations and societies The Engineering Handbook Second Edition is designed to enlighten experts in areas outside their own specialties to refresh the knowledge of mature practitioners and to educate engineering novices Whether you work in industry government or academia this is simply the best most useful engineering reference you can have in your personal office or institutional library **FPGA-BASED Hardware Accelerators** Iouliia Skliarova,Valery Sklyarov,2019-05-30 This book suggests and describes a number of fast parallel circuits for data vector processing using FPGA based hardware accelerators Three primary areas are covered searching sorting and counting in combinational and iterative networks These include the application of traditional structures that rely on comparators swappers as well as alternative networks with a variety of core elements such as adders logical gates and look up tables The iterative technique discussed in the book enables the sequential reuse of relatively large combinational blocks that execute many parallel operations with small propagation delays For each type of network discussed the main focus is on the step by step development of the architectures proposed from initial concepts to synthesizable hardware description language specifications Each type of network is taken through several stages including modeling the desired functionality in software the retrieval and automatic conversion of key functions leading to specifications for optimized hardware modules The resulting specifications are then synthesized implemented and tested in FPGAs using commercial design environments and prototyping boards The methods proposed can be used in a range of data processing applications including traditional sorting the extraction of maximum and minimum subsets from large data sets communication time data processing finding frequently occurring items in a set and Hamming weight distance counters comparators The book is intended to be a valuable support material for university and industrial engineering courses that involve FPGA based circuit and system design **Artificial Intelligence in Theory and Practice** Max Bramer,2006-08-10 The papers in this volume comprise the refereed proceedings of the conference Artificial Intelligence in Theory and Practice IFIP AI 2006 which formed part of the 19th World Computer Congress of IFIP the International Federation for Information Processing WCC 2006 in Santiago Chile in August 2006 The conference is organised by the IFIP Technical Committee on Artificial Intelligence Technical Committee 12 and its Working Group 12 5 Artificial Intelligence Applications All papers were reviewed by at least two members of our Programme Committee The best papers were selected for the conference and are included in this volume The international nature of IFIP is amply reflected in the large number of countries represented here The conference featured invited talks by Rose Dieng John Atkinson John

Debenham and myself IFIP AI 2006 also included the Second IFIP Symposium on Professional Practice in Artificial Intelligence organised by Professor John Debenham which ran alongside the refereed papers I should like to thank the conference chair Professor Debenham for all his efforts in organising the Symposium and the members of our programme committee for reviewing an unexpectedly large number of papers to a very tight deadline This is the latest in a series of conferences organised by IFIP Technical Committee 12 dedicated to the techniques of Artificial Intelligence and their real world applications The wide range and importance of these applications is clearly indicated by the papers in this volume Further information about TCI 2 can be found on our website http www ifiptcl2 org   Handbook of Nanoscience, Engineering, and Technology William A. Goddard III,Donald Brenner,Sergey Edward Lyshevski,Gerald J Iafrate,2002-10-29 Nanotechnology science and engineering spearhead the 21st century revolution that is leading to fundamental breakthroughs in the way materials devices and systems are understood designed made and used With contributions from a host of world class experts and pioneers in the field this handbook sets forth the fundamentals of nanoelectromechanical systems NEMS studies their fabrication and explores some of their most promising applications It provides comprehensive information and references for nanoscale structures devices and systems molecular technology and nanoelectromechanical theory and promises to become a standard reference for the field   The Electrical Engineering Handbook - Six Volume Set Richard C. Dorf,2018-12-14 In two editions spanning more than a decade The Electrical Engineering Handbook stands as the definitive reference to the multidisciplinary field of electrical engineering Our knowledge continues to grow and so does the Handbook For the third edition it has grown into a set of six books carefully focused on specialized areas or fields of study Each one represents a concise yet definitive collection of key concepts models and equations in its respective domain thoughtfully gathered for convenient access Combined they constitute the most comprehensive authoritative resource available Circuits Signals and Speech and Image Processing presents all of the basic information related to electric circuits and components analysis of circuits the use of the Laplace transform as well as signal speech and image processing using filters and algorithms It also examines emerging areas such as text to speech synthesis real time processing and embedded signal processing Electronics Power Electronics Optoelectronics Microwaves Electromagnetics and Radar delves into the fields of electronics integrated circuits power electronics optoelectronics electromagnetics light waves and radar supplying all of the basic information required for a deep understanding of each area It also devotes a section to electrical effects and devices and explores the emerging fields of microlithography and power electronics Sensors Nanoscience Biomedical Engineering and Instruments provides thorough coverage of sensors materials and nanoscience instruments and measurements and biomedical systems and devices including all of the basic information required to thoroughly understand each area It explores the emerging fields of sensors nanotechnologies and biological effects Broadcasting and Optical Communication Technology explores communications information theory and devices covering all of the basic information

needed for a thorough understanding of these areas It also examines the emerging areas of adaptive estimation and optical communication Computers Software Engineering and Digital Devices examines digital and logical devices displays testing software and computers presenting the fundamental concepts needed to ensure a thorough understanding of each field It treats the emerging fields of programmable logic hardware description languages and parallel computing in detail Systems Controls Embedded Systems Energy and Machines explores in detail the fields of energy devices machines and systems as well as control systems It provides all of the fundamental concepts needed for thorough in depth understanding of each area and devotes special attention to the emerging area of embedded systems Encompassing the work of the world s foremost experts in their respective specialties The Electrical Engineering Handbook Third Edition remains the most convenient reliable source of information available This edition features the latest developments the broadest scope of coverage and new material on nanotechnologies fuel cells embedded systems and biometrics The engineering community has relied on the Handbook for more than twelve years and it will continue to be a platform to launch the next wave of advancements The Handbook s latest incarnation features a protective slipcase which helps you stay organized without overwhelming your bookshelf It is an attractive addition to any collection and will help keep each volume of the Handbook as fresh as your latest research **Developments In Language Theory Ii, At The Crossroads Of Mathematics, Computer Science And Biology** Jurgen Dassow,Grzegorz Rozenberg,Arto Salomaa,1996-05-25 The contributions of the proceedings cover almost all parts of the theory of formal languages from pure theoretical investigations to applications to programming languages Main topics are combinatorial properties of words sequences of words and sets of words grammar systems and grammars with controlled derivations generation of higher dimensional objects and graphs trace languages numerical parameters of automata and languages **Language, Culture, Computation: Computing - Theory and Technology** Nachum Dershowitz,Ephraim Nissan,2014-12-03 This Festschrift volume is published in Honor of Yaacov Choueka on the occasion of this 75th birthday The present three volumes liber amicorum several years in gestation honours this outstanding Israeli computer scientist and is dedicated to him and to his scientific endeavours Yaacov s research has had a major impact not only within the walls of academia but also in the daily life of lay users of such technology that originated from his research An especially amazing aspect of the temporal span of his scholarly work is that half a century after his influential research from the early 1960s a project in which he is currently involved is proving to be a sensation as will become apparent from what follows Yaacov Choueka began his research career in the theory of computer science dealing with basic questions regarding the relation between mathematical logic and automata theory From formal languages Yaacov moved to natural languages He was a founder of natural language processing in Israel developing numerous tools for Hebrew He is best known for his primary role together with Aviezri Fraenkel in the development of the Responsa Project one of the earliest fulltext retrieval systems in the world More recently he has headed the Friedberg Genizah Project which is bringing the treasures of the Cairo

Genizah into the Digital Age This first part of the three volume set covers a range of topics in computer science The papers are grouped in topical sections on the jubilaris Yaacov and his oeuvre theory of computation science computing and tools for engineering information retrieval **RoboCup 2007: Robot Soccer World Cup XI** Ubbo Visser,Fernando Ribeiro,Takeshi Ohashi,Frank Dellaert,2008-07-18 This book constitutes the proceedings of the 11th RoboCup International Symposium held in Atlanta GA USA in July 2007 immediately after the 2007 RoboCupSoccer RoboCupRescue and RoboCupJunior competitions Papers presented at the symposium focused on topics related to these three events and to artificial intelligence and robotics in general The 18 revised full papers and 42 revised poster papers included in the book were selected from 133 submissions Each paper was reviewed by at least three program committee members The program committee also nominated two papers for the Best Paper and Best Student Paper awards respectively The book provides a valuable source of reference and inspiration for R D professionals and educationalists active or interested in robotics and artificial intelligence **Computer Information Systems and Industrial Management** Khalid Saeed,Władysław Homenda,Rituparna Chaki,2017-05-16 This book constitutes the proceedings of the 16th IFIP TC8 International Conference on Computer Information Systems and Industrial Management CISIM 2017 held in Bialystok Poland in June 2017 The 60 regular papers presented together with 5 keynotes were carefully reviewed and Selected from 85 submissions They are organized in the following topical sections algorithms biometrics and pattern recognition applications data analysis and information retrieval engineering of enterprise software products industrial management and other applications modelling and optimization various aspects of computer security

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