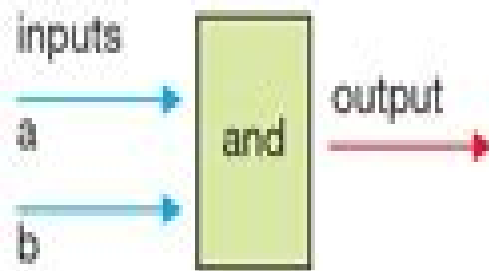


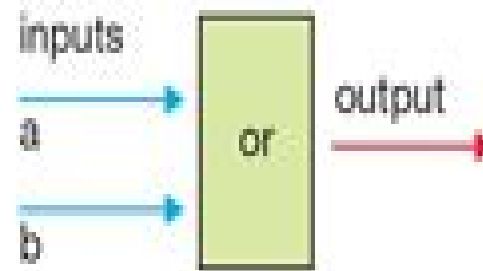
Logic circuits

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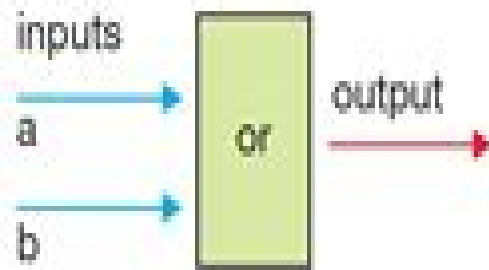
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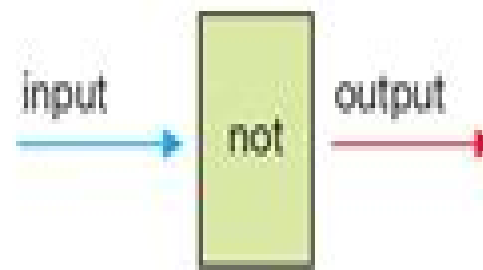
| inputs | | output |
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| 1 | 0 | 1 |
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| inputs | | output |
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| a | b | |
| 0 | 0 | 0 |
| 0 | 1 | 1 |
| 1 | 0 | 1 |
| 1 | 1 | 1 |

NOT



| input | output |
|-------|--------|
| 0 | 1 |
| 1 | 0 |

Logic Circuit Design

Shimon P. Vingron



Logic Circuit Design:

Practical Design of Digital Circuits Ian Kampel, 2015-08-11 *Practical Design of Digital Circuits Basic Logic to Microprocessors* demonstrates the practical aspects of digital circuit design. The intention is to give the reader sufficient confidence to embark upon his own design projects utilizing digital integrated circuits as soon as possible. The book is organized into three parts. Part 1 teaches the basic principles of practical design and introduces the designer to his tools or rather the range of devices that can be called upon. Part 2 shows the designer how to put these together into viable designs. It includes two detailed descriptions of actual design exercises. The first of these is a fairly simple exercise in CMOS design, the second is a much more complex design for an electronic game using TTL devices. Part 3 focuses on microprocessors. It illustrates how a particular design problem changes emphasis when a microprocessor is introduced. This book is aimed at a fairly broad market; it is intended to aid the linear design engineer to cross the barrier into digital electronics; it should provide interesting supporting reading for students studying digital electronics from the more academic viewpoint and it should enable the enthusiast to design much more ambitious and sophisticated projects than he could otherwise attempt if restricted to linear devices.

CMOS Logic Circuit Design John P. Uyemura, 2007-05-08 This is an up to date treatment of the analysis and design of CMOS integrated digital logic circuits. The self contained book covers all of the important digital circuit design styles found in modern CMOS chips emphasizing solving design problems using the various logic styles available in CMOS.

Logic Circuit Design Shimon P. Vingron, 2012-03-30 In three main divisions the book covers combinational circuits, latches and asynchronous sequential circuits. Combinational circuits have no memorising ability while sequential circuits have such an ability to various degrees. Latches are the simplest sequential circuits, ones with the shortest memory. The presentation is decidedly non standard. The design of combinational circuits is discussed in an orthodox manner using normal forms and in an unorthodox manner using set theoretical evaluation formulas relying heavily on Karnaugh maps. The latter approach allows for a new design technique called composition. Latches are covered very extensively. Their memory functions are expressed mathematically in a time independent manner allowing the use of normal non temporal Boolean logic in their calculation. The theory of latches is then used as the basis for calculating asynchronous circuits. Asynchronous circuits are specified in a tree representation, each internal node of the tree representing an internal latch of the circuit, the latches specified by the tree itself. The tree specification allows solutions of formidable problems such as algorithmic state assignment, finding equivalent states non recursively and verifying asynchronous circuits.

Logic Circuit Design Shimon P. Vingron, 2023-11-27 The 2nd edition has been thoroughly revised and is intended as a wakeup call in the stagnant and dormant field of switching algebra and logic circuit design. It presents the material in a concise but thorough way. The topics selected are an in depth presentation of switching algebra, a theory of memory circuits, sometimes called flop flops, a new approach to asynchronous circuits and a newly added part presenting a unique programming technique or language for

programmable logic controllers PLCs Be ready for the unorthodox and controversial *Digital Electronics: A Primer - Introductory Logic Circuit Design* Mark S Nixon, 2015-01-27 This practical introduction explains exactly how digital circuits are designed from the basic circuit to the advanced system It covers combinational logic circuits which collect logic signals to sequential logic circuits which embody time and memory to progress through sequences of states The primer also highlights digital arithmetic and the integrated circuits that implement the logic functions Based on the author's extensive experience in teaching digital electronics to undergraduates the book translates theory directly into practice and presents the essential information in a compact digestible style Worked problems and examples are accompanied by abbreviated solutions with demonstrations to ensure that the design material and the circuits operation are fully understood This is essential reading for any electronic or electrical engineering student new to digital electronics and requiring a succinct yet comprehensive introduction

Logic Circuit Design Alan W. Shaw, 1993 With Shaw's conversational writing style sophomore engineering and computer science majors will find this text's coverage of combinational and sequential logic design easy to understand and a pleasure to read The text is remarkably clear and provides extensive examples Shaw maintains an ongoing relationship with industry which is reflected in the text's primary goal of preparing students for entry into the workplace ready to design The text's design first organization allows students to master a simple systematic design process then move to design and analysis of more complex circuits The use of polarized notation offers students an easy to learn notation that clarifies the thought process in design allows a simplification of the sign process and improves documentation Software simulation is stressed in all designed circuits allowing students to test circuits before committing them to hardware An early introduction to programmable logic devices reflects their importance in design

Introduction to Logic Circuits & Logic Design with VHDL Brock J. LaMeres, 2016-09-15 This textbook introduces readers to the fundamental hardware used in modern computers The only pre requisite is algebra so it can be taken by college freshman or sophomore students or even used in Advanced Placement courses in high school This book presents both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based This textbook enables readers to design digital systems using the modern HDL approach while ensuring they have a solid foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the content with learning goals and assessment at its core Each section addresses a specific learning outcome that the learner should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure learner performance on each outcome This book can be used for either a sequence of two courses consisting of an introduction to logic circuits Chapters 1-7 followed by logic design Chapters 8-13 or a single accelerated course that uses the early chapters as reference material

Introduction to Logic

Circuits & Logic Design with Verilog Brock J. LaMer, 2023-10-19 This textbook for courses in Digital Systems Design introduces students to the fundamental hardware used in modern computers Coverage includes both the classical approach to digital system design i e pen and paper in addition to the modern hardware description language HDL design approach computer based Using this textbook enables readers to design digital systems using the modern HDL approach but they have a broad foundation of knowledge of the underlying hardware and theory of their designs This book is designed to match the way the material is actually taught in the classroom Topics are presented in a manner which builds foundational knowledge before moving onto advanced topics The author has designed the presentation with learning goals and assessment at its core Each section addresses a specific learning outcome that the student should be able to do after its completion The concept checks and exercise problems provide a rich set of assessment tools to measure student performance on each outcome

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The Essence of Logic Circuits Stephen H. Unger, 1997 Today designing a state of the art circuit means knowing how to pack more and more logic on a chip Featuring an extensive introductory material this complete carefully organized guide brings you valuable information on designing modern logic circuits from gates switches and other basic elements to meet the rising demands on modern circuit technology THE ESSENCE OF LOGIC CIRCUITS allows computer scientists and students to start from scratch and gain a comprehensive understanding of most important topics in the field

An Introduction to Logic Circuit Testing Parag K. Lala, 2008-11-08 An Introduction to Logic Circuit Testing provides a detailed coverage of techniques for test generation and testable design of digital electronic circuits systems The material covered in the book should be sufficient for a course or part of a course in digital circuit testing for senior level undergraduate and first year graduate students in Electrical Engineering and Computer Science The book will also be a valuable resource for engineers working in the industry This book has four chapters Chapter 1 deals with various types of faults that may occur in very large scale integration VLSI based digital circuits Chapter 2 introduces the major concepts of all test generation techniques such as redundancy fault coverage sensitization and backtracking Chapter 3 introduces the key

concepts of testability followed by some ad hoc design for testability rules that can be used to enhance testability of combinational circuits Chapter 4 deals with test generation and response evaluation techniques used in BIST built in self test schemes for VLSI chips Table of Contents Introduction Fault Detection in Logic Circuits Design for Testability Built in Self Test References *Digital Circuits* Emery, 1985-03-19 This textbook is intended to introduce the student of electronics to the fundamentals of digital circuits both combinational and sequential in a reasonable and systematic manner It proceeds from basic logic concepts to circuits and designs **Computer-Aided Design Techniques for Low Power Sequential Logic Circuits** José Monteiro, Srinivas Devadas, 2012-12-06 Rapid increases in chip complexity increasingly faster clocks and the proliferation of portable devices have combined to make power dissipation an important design parameter The power consumption of a digital system determines its heat dissipation as well as battery life For some systems power has become the most critical design constraint Computer Aided Design Techniques for Low Power Sequential Logic Circuits presents a methodology for low power design The authors first present a survey of techniques for estimating the average power dissipation of a logic circuit At the logic level power dissipation is directly related to average switching activity A symbolic simulation method that accurately computes the average switching activity in logic circuits is then described This method is extended to handle sequential logic circuits by modeling correlation in time and by calculating the probabilities of present state lines Computer Aided Design Techniques for Low Power Sequential Logic Circuits then presents a survey of methods to optimize logic circuits for low power dissipation which target reduced switching activity A method to retime a sequential logic circuit where registers are repositioned such that the overall glitching in the circuit is minimized is also described The authors then detail a powerful optimization method that is based on selectively precomputing the output logic values of a circuit one clock cycle before they are required and using the precomputed value to reduce internal switching activity in the succeeding clock cycle Presented next is a survey of methods that reduce switching activity in circuits described at the register transfer and behavioral levels Also described is a scheduling algorithm that reduces power dissipation by maximising the inactivity period of the modules in a given circuit Computer Aided Design Techniques for Low Power Sequential Logic Circuits concludes with a summary and directions for future research **Introduction to Logic Circuit Design**

Preliminary Edition Brock J. Lameris, 2015-08-31 **Introduction to Logic Circuit Design** Brock J. Lameris, 2015-10-12

EBOOK: Fundamentals of Digital Logic Stephen Brown, Zvonko Vranesic, 2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being

discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containing all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text **Electronic Logic Circuits** J.

Gibson,2013-01-11 Most branches of organizing utilize digital electronic systems This book introduces the design of such systems using basic logic elements as the components The material is presented in a straightforward manner suitable for students of electronic engineering and computer science The book is also of use to engineers in related disciplines who require a clear introduction to logic circuits This third edition has been revised to encompass the most recent advances in technology as well as the latest trends in components and notation It includes a wide coverage of application specific integrated circuits ASCIs many worked examples and a step by step logical and practical approach **Fundamentals of**

Digital Logic and Microcomputer Design M. Rafiquzzaman,2005-07-08 Fundamentals of Digital Logic and Microcomputer Design has long been hailed for its clear and simple presentation of the principles and basic tools required to design typical digital systems such as microcomputers In this Fifth Edition the author focuses on computer design at three levels the device level the logic level and the system level Basic topics are covered such as number systems and Boolean algebra combinational and sequential logic design as well as more advanced subjects such as assembly language programming and microprocessor based system design Numerous examples are provided throughout the text Coverage includes Digital circuits at the gate and flip flop levels Analysis and design of combinational and sequential circuits Microcomputer organization architecture and programming concepts Design of computer instruction sets CPU memory and I/O System design features associated with popular microprocessors from Intel and Motorola Future plans in microprocessor development An instructor's manual available upon request Additionally the accompanying CD ROM contains step by step procedures for installing and using Altera Quartus II software MASM 6 11 8086 and 68asm sim 68000 provides valuable simulation results via screen shots Fundamentals of Digital Logic and Microcomputer Design is an essential reference that will provide you with the fundamental tools you need to design typical digital systems *Circuit Design: Know It All* Darren Ashby,Bonnie Baker,Ian

Hickman,Walt Kester,Robert Pease,Tim Williams,Bob Zeidman,2011-04-19 The Newnes Know It All Series takes the best of what our authors have written to create hard working desk references that will be an engineer's first port of call for key information design techniques and rules of thumb Guaranteed not to gather dust on a shelf Electronics Engineers need to master a wide area of topics to excel The Circuit Design Know It All covers every angle including semiconductors IC Design and Fabrication Computer Aided Design as well as Programmable Logic Design A 360 degree view from our best selling authors Topics include fundamentals Analog Linear and Digital circuits The ultimate hard working desk reference all the essential information techniques and tricks of the trade in one volume *DIGITAL LOGIC DESIGN* ALAM, MANSAF,ALAM,

BASHIR,2015-10-15 This textbook covers latest topics in the field of digital logic design along with tools to design the digital

logic circuits It is designed for the undergraduate students pursuing courses in areas of engineering disciplines such as Electrical and Electronics Electronics and Communication Electronics and Instrumentation Telecommunications and Computer Science and Engineering It is also useful as a text for MCA M Sc Electronics and M Sc Computer Science students The contents of this book have been organized in a systematic manner so as to inculcate sound knowledge and concepts amongst its readers It covers basic concepts in combinational and sequential circuit design such as digital electronics digital signal processing number system data and information representation and computer arithmetic Besides this advanced topics in digital logic design such as various types of counter design register design ALU design threshold circuit and digital computer design are also discussed in the book Key features Question Bank containing numerous multiple choice questions with their answers Short answer questions long answer questions and multiple choice questions at the end of each chapter Extensive use of graphs and diagrams for better understanding of the subject

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

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