



Electron Beam Testing Technology

John Thiam Leong Thong



Electron Beam Testing Technology:

Electron Beam Testing Technology John T.L. Thong, 2013-06-29 Although exploratory and developmental activity in electron beam testing EBT 25 years it was not had already been in existence in research laboratories for over until the beginning of the 1980s that it was taken up seriously as a technique for integrated circuit IC testing While ICs were being fabricated on design rules of several microns the mechanical needle probe served quite adequately for internal chip probing This scenario changed with growing device complexity and shrinking geometries prompting IC manufacturers to take note of this new testing technology It required several more years and considerable investment by electron beam tester manufacturers however to come up with user friendly automated systems that were acceptable to IC test engineers These intervening years witnessed intense activity in the development of instrumentation testing techniques and system automation as evidenced by the proliferation of technical papers presented at conferences With the shift of interest toward applications the technology may now be considered as having come of age

Electron Beam Testing Technology for High-speed Device Characterisation John Thiam Leong Thong, 1989 Vacuum Electronics Joseph A. Eichmeier, Manfred

Thumm, 2008-03-04 Nineteen experts from the electronics industry research institutes and universities have joined forces to prepare this book It does nothing less than provide a complete overview of the electrophysical fundamentals the present state of the art and applications as well as the future prospects of microwave tubes and systems The book does the same for optoelectronics vacuum devices electron and ion beam devices light and X ray emitters particle accelerators and vacuum interrupters

Scientific and Technical Aerospace Reports, 1995 **Multi-Chip Module Test Strategies** Yervant Zorian, 2012-12-06 MCMs today consist of complex and dense VLSI devices mounted into packages that allow little physical access to internal nodes The complexity and cost associated with their test and diagnosis are major obstacles to their use Multi Chip Module Test Strategies presents state of the art test strategies for MCMs This volume of original research is designed for engineers interested in practical implementations of MCM test solutions and for designers looking for leading edge test and design for testability solutions for their next designs Multi Chip Module Test Strategies consists of eight contributions by leading researchers It is designed to provide a comprehensive and well balanced coverage of the MCM test domain Multi Chip Module Test Strategies has also been published as a special issue of the Journal of Electronic Testing Theory and Applications JETTA Volume 10 Numbers 1 and 2

Advances in Imaging and Electron Physics Peter W. Hawkes, 2004-12-18 A special volume devoted principally to the role of the late Sir Charles Oatley in the development of the scanning electron microscope It contains historical articles and reminiscences by most of the scientists who have worked on the scanning electron microscope in Oatley's laboratory Emphasizes broad and in depth article collaborations between world renowned scientists in the field of image and electron physics Although the scanning electron microscope had a prehistory in Germany and the USA its real champion was Charles Oatley who launched his project in the Cambridge

University Engineering Department shortly after the end of World War II A first microscope was built successfully by D McMullan one of the Guest Editors of this volume and a succession of progressively improved instruments followed One in particular built by K C A Smith was commissioned specially for the Canadian Pulp and Paper Research Institute for use in their Montreal laboratories All these efforts culminated in the commercial model built by the Cambridge Instrument Company and marketed in 1965 under the trade name Stereoscan Although this story has been told on several occasions in particular in these Advances it seemed appropriate in the centenary year of the birth of Sir Charles Oatley that more details should be published to celebrate these achievements This volume is the result It contains not only historical articles and reminiscences by most of the scientists who have worked on the scanning electron microscope in Oatley's laboratory but also full or partial reproductions of many of the key publications beginning with McMullan's early paper of 1953 and including Oatley's own Early history of the scanning electron microscope 1982 A website has been created in which supplementary material is collected This volume is a tribute to a bold pioneering scientist and a vivid record of the creation of the first commercial scanning electron microscopes and of subsequent developments A special volume devoted principally to the role of the late Sir Charles Oatley in the development of the scanning electron microscope It contains historical articles and reminiscences by most of the scientists who have worked on the scanning electron microscope in Oatley's laboratory Emphasizes broad and in depth article collaborations between world renowned scientists in the field of image and electron physics

Beam Processing Technologies Norman G. Einspruch, S. S. Cohen, Raj N. Singh, 2014-12-01 Beam Processing Technologies is a collection of papers that deals with the miniaturization of devices that will be faster consume less power and cost less per operation or fabrication One paper discusses metal oxide semiconductor MOS integrated circuit technology including the operation of devices whose lateral and vertical dimensions are scaled down If the devices silicon doping profiles are increased by the same scale factor they can operate on lower voltages and currents with the same performance Another paper describes laser beam processing and wafer scale integration as techniques to increase the number of devices on a silicon chip Electron beam technologies can be used in many fabrication processes such as in microlithography selective oxidation doping metrology Ion beam applications depend on the presence of the ion introduced into the device e.g. implantation doping on pseudoelastic collisions e.g. physical sputtering or crystal damage and on inelastic scattering e.g. polymer resist exposure Silicon molecular beam epitaxy SiMBE can also grow high quality layers at low temperature particularly concerning germanium especially as regards the growth system design and utilization of n and p type doping Chemical beam epitaxy CBE is another epitaxial growth technique that can surpass MBE and metal organic chemical vapor deposition MO CVD The collection is suitable for chemical engineers industrial physicists and researchers whose work involves micro fabrication and development of integrated circuits

Methods and Materials in Microelectronic Technology Joachim Bargon, 2013-03-09 The papers collected in this volume were presented at the International Symposium on Methods

and Materials in Microelectronic Technology This symposium was sponsored by IBM Germany and it was held September 29 October 1 1982 in Bad Neuenahr West Germany The progress of semiconductor and microelectronic technology has become so rapid and the field so sophisticated that it is imperative to exchange the latest insight gained as frequently as it can be accomplished In addition it is peculiar for this field that the bulk of the investigations are carried out at industrial research and development laboratories which makes some of the results less readily accessible Because of these circumstances the academic community which among other things is supposed to communicate the progress in this field to students of different disciplines finds it rather difficult to stay properly informed It was the intent of this IBM sponsored symposium to bring together key scientists from academic institutions primarily from Europe with principal investigators of the industrial scene Accordingly this symposium exposed technologists to scientists and vice versa Scientific advances often lead directly to technological innovations In turn new technologies are often arrived at empirically and because of that are initially poorly understood Scientific inquiry then attempts to probe these processes and phenomena in order to achieve a better understanding Thus science and technology are intricately interconnected and it is important that technical exchange between technologists and scientists is facilitated since the problems are typically interdisciplinary in nature *ISTFA 2019: Proceedings of the 45th International Symposium for Testing and Failure Analysis* ASM International, 2019-12-01 The theme for the 2019 conference is Novel Computing Architectures Papers will include discussions on the advent of Artificial Intelligence and the promise of quantum computing that are driving disruptive computing architectures Neuromorphic chip designs on one hand and Quantum Bits on the other still in R D will introduce new computing circuitry and memory elements novel materials and different test methodologies These novel computing architectures will require further innovation which is best achieved through a collaborative Failure Analysis community composed of chip manufacturers tool vendors and universities Technical Abstract Bulletin ,1964

Advanced Smart Information and Communication Technology and Systems Mykhailo Ilchenko, Leonid Uryvsky, Larysa Globa, 2025-07-28 This book highlights the most important research areas in Information and Communication Technologies and their impact on digital society and environment sustainable development namely the research in fields of Information and Communication Technologies artificial intelligence in ICT lunar missions maintenance security of data and services smart robotic systems implementation in the digital environment and mathematical modeling for practical and research tasks in communication and data processing fields provided by various groups of researchers from Germany and Ukraine in cooperation with scientists from different countries The presented studies contain a discussion on the use of multi cloud integration approaches in particular based on methods of deep learning practical implementation of the Internet of Things IoT the modern study of V2V Communication and research in fields of mathematical modeling in applied problems The book focuses on the basics of information and analytical activities in the digital global space to providing broadband Internet access without decreasing the quality of experience QoE level improving

services provision and system architecture with improving security The study of modern communication and information technologies contains original works dealing with many aspects of their improvement and use for research that shows some effective technological solutions that can be used for the implementation of novel cloud infrastructure and radio electronics systems These results can be used in the implementation of novel systems and to promote the exchange of information in e societies Given its scope the book offers a valuable resource for scientists lecturers specialists working at enterprises and graduate and undergraduate students who engage with problems in Information and Communication Technologies as well as aspects of society and environment sustainable development **The Militarily Critical Technologies List** ,1996

Testing and Diagnosis of VLSI and ULSI F. Lombardi,M.G. Sami,2012-12-06 This volume contains a collection of papers presented at the NATO Advanced Study Institute on Testing and Diagnosis of VLSI and ULSI held at Villa Olmo Como Italy June 22 July 3 1987 High Density technologies such as Very Large Scale Integration VLSI Wafer Scale Integration WSI and the not so far promises of Ultra Large Scale Integration ULSI have exasperated the problema associated with the testing and diagnosis of these devices and systema Traditional techniques are fast becoming obsolete due to unique requirements such as limited controllability and observability increasing execution complexity for test vector generation and high cost of fault simulation to mention just a few New approaches are imperative to achieve the highly sought goal of the three months turn around cycle time for a state of the art computer chip The importance of testing and diagnostic processes is of primary importance if costs must be kept at acceptable levels The objective of this NATO ASI was to present analyze and discuss the various facets of testing and diagnosis with respect to both theory and practice The contents of this volume reflect the diversity of approaches currently available to reduce test and diagnosis time These approaches are described in a concise yet clear way by renowned experts of the field Their contributions are aimed at a wide readership the uninitiated researcher will find the tutorial chapters very rewarding The expert will be introduced to advanced techniques in a very comprehensive manner *SEM Microcharacterization of Semiconductors* D. B. Holt,D. C. Joy,2013-10-22 Applications of SEM techniques of microcharacterization have proliferated to cover every type of material and virtually every branch of science and technology This book emphasizes the fundamental physical principles The first section deals with the foundation of microcharacterization in electron beam instruments and the second deals with the interpretation of the information obtained in the main operating modes of a scanning electron microscope

Review Oak Ridge National Laboratory,1996 **ISTFA**

2017: Proceedings from the 43rd International Symposium for Testing and Failure Analysis ASM

International,2017-12-01 The theme for the November 2017 conference was Striving for 100% Success Rate Papers focus on the tools and techniques needed for maximizing the success rate in every aspect of the electronic device failure analysis process *Advanced Circuits for Emerging Technologies* Krzysztof Iniewski,2012-04-17 The book will address the state of

the art in integrated circuit design in the context of emerging systems New exciting opportunities in body area networks

wireless communications data networking and optical imaging are discussed Emerging materials that can take system performance beyond standard CMOS like Silicon on Insulator SOI Silicon Germanium SiGe and Indium Phosphide InP are explored Three dimensional 3 D CMOS integration and co integration with sensor technology are described as well The book is a must for anyone serious about circuit design for future technologies The book is written by top notch international experts in industry and academia The intended audience is practicing engineers with integrated circuit background The book will be also used as a recommended reading and supplementary material in graduate course curriculum Intended audience is professionals working in the integrated circuit design field Their job titles might be design engineer product manager marketing manager design team leader etc The book will be also used by graduate students Many of the chapter authors are University Professors **Istfa 2001** ASM International,2001-01-01 **Microelectronics Failure Analysis** EDFAS Desk Reference Committee,2011 Includes bibliographical references and index **Integration of Test with Design and Manufacturing** IEEE Computer Society,1987

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