Fourth Edition

Fundamentals of
DIGITAL LOGIC
with VHDL DESIGN

STEPHEN BROWN | ZVONKO VRANESIC

Fundamentals Of Digital Logic With Vhdl Design

Christoph Scholl

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functions and applications to the design of digital devices and systems Requiring no prior knowledge of electrical circuits or electronics it supplies the **Digital Principles and Design** Donald D. Givone,2003 **Computer Arithmetics for Nanoelectronics** Vlad P. Shmerko,Svetlana N. Yanushkevich,Sergey Edward Lyshevski,2018-10-03 Emphasizes the Basic Principles of Computational Arithmetic and Computational Structure Design Taking an interdisciplinary approach to the nanoscale generation of computer devices and systems Computer Arithmetics for Nanoelectronics develops a consensus between computational properties provided by data structures and phenomenological properties of nano and molecular technology Covers All Stages of the Design Cycle from Task Formulation to Molecular Based Implementation The book introduces the theoretical base and properties of various data structures along with techniques for their manipulation optimization and implementation It also assigns the computational properties of logic design data structures to 3D structures furnishes information theoretical measures and design aspects and discusses the testability problem The last chapter presents a nanoscale prospect for natural computing based on assorted computing paradigms from nature Balanced Coverage of State of the Art Concepts Techniques and Practices Up to date comprehensive and pragmatic in its approach this text provides a unified overview of the relationship between the fundamentals of digital system design computer architectures and micro and nanoelectronics **Functional Decomposition with Applications to FPGA Synthesis** Christoph Scholl,2013-03-09 During the last few years Field Programmable Gate Arrays FPGAs have become increasingly important Thanks to recent breakthroughs in technology FPGAs offer millions of system gates at low cost and considerable speed Functional decomposition has emerged as an essential technique in automatic logic synthesis for FPGAs Functional decomposition as a technique to find realizations for Boolean functions was already introduced in the late fifties and early sixties by Ashenhurst Curtis Roth and Karp In recent years however it has attracted a great deal of renewed attention for several reasons First it is especially well suited for the synthesis of lookup table based FPGAs Also the increased capacities of today s computers as well as the development of new methods have made the method applicable to larger scale problems Modern techniques for functional decomposition profit from the success of Reduced Ordered Binary Decision Diagrams ROBDDs data structures that provide compact representations for many Boolean functions occurring in practical applications We have now seen the development of algorithms for functional decomposition which work directly based on ROBDDs so that the decomposition algorithm works based on compact representations and not on function tables or decomposition matrices as in previous approaches The book presents in a consistent manner a comprehensive presentation of a multitude of results stemming from the author s as well as various researchers work in the field Apart from the basic method it also covers functional decomposition for incompletely specified functions decomposition for multi output functions and non disjoint decomposition Functional Decomposition with Application to FPGA Synthesis will be of interest both to researchers and advanced students in logic synthesis VLSI CAD and Design Automation as well as professionals working in FPGA design and

the developmentof algorithms for FPGA synthesis **Design and Testing of Reversible Logic** Ashutosh Kumar Singh,Masahiro Fujita,Anand Mohan,2019-07-29 The book compiles efficient design and test methodologies for the implementation of reversible logic circuits The methodologies covered in the book are design approaches test approaches fault tolerance in reversible circuits and physical implementation techniques The book also covers the challenges and the reversible logic circuits to meet these challenges stimulated during each stage of work cycle The novel computing paradigms are being explored to serve as a basis for fast and low power computation

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